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Specifications for
Blanview TFT-LCD Monitor
(4.3" WQVGA 480 x RGB x 272 Landscape)

Version 1.0

(Please be sure to check the specifications latest version.)

MODEL COM43H4M43KLC

Customer's Approval

Signature:

Name:

Section:

Title:

Date:

ORTUSTECH

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[illegible]

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1. Application

This Specification is applicable to 10.9cm (4.3 inch) Blanview TFT-LCD monitor for non-military use.

- ◎ ORTUS TECHNOLOGY makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and ORTUS TECHNOLOGY shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains ORTUS TECHNOLOGY's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of ORTUS TECHNOLOGY'S confidential information and copy right.
- ◎ If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult ORTUS TECHNOLOGY on such use in advance.
- ◎ This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- ◎ It must be noted as an mechanical design manner, especial attention in housing design to prevent arcuation/flexureor caused by stress to the LCD module shall be considered.
- ◎ ORTUS TECHNOLOGY assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- ◎ ORTUS TECHNOLOGY is not responsible for any nonconformities and defects that are not specified in this specifications.
- ◎ If any issue arises as to information provided in this Specification or any other information, ORTUS TECHNOLOGY and Purchaser shall discuss them in good faith and seek solution.
- ◎ ORTUS TECHNOLOGY assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.
- ◎ This Product is compatible for RoHS directive.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000

2. Outline Specifications

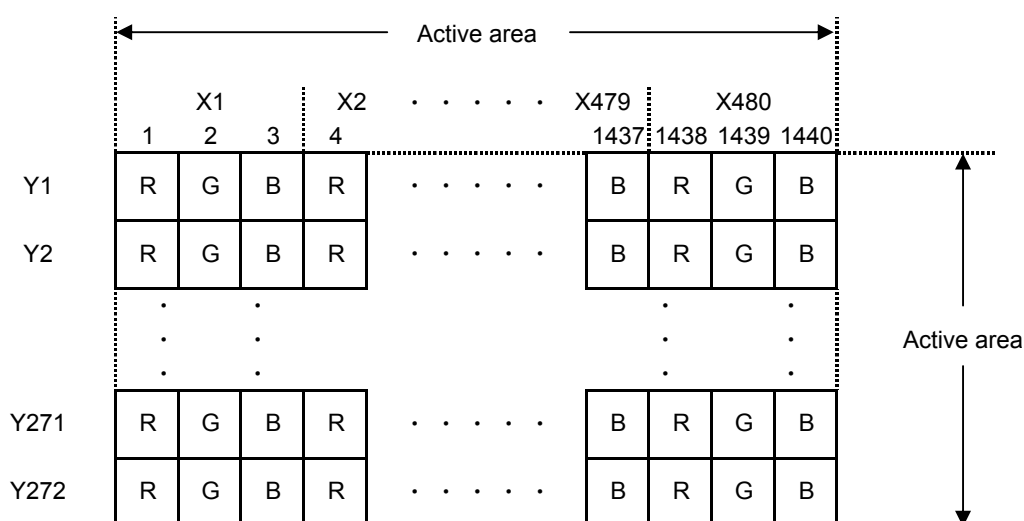
2.1 Features of the Product

- 4.3 inch diagonal display, 1,440 [H] x 272 [V] dots.
- 8-bit 16,777,216 color display capability.
- Single power supply operation of 3.3V.
- Built in Timing generator (TG), Counter-electrode driving circuitry and power supply circuit.
- High bright white LED back-light.
- Blanview TFT-LCD, improved outdoor readability.

	Indoor		Outdoor	
	Readability	Power Efficiency (Battery Life)	Readability	Power Efficiency (Battery Life)
Transmissive	Good	Good	Fair	Poor
Transflective	Fair	Poor	Good	Good
Blanview	Good	Good	Good	Good

2.2 Display Method

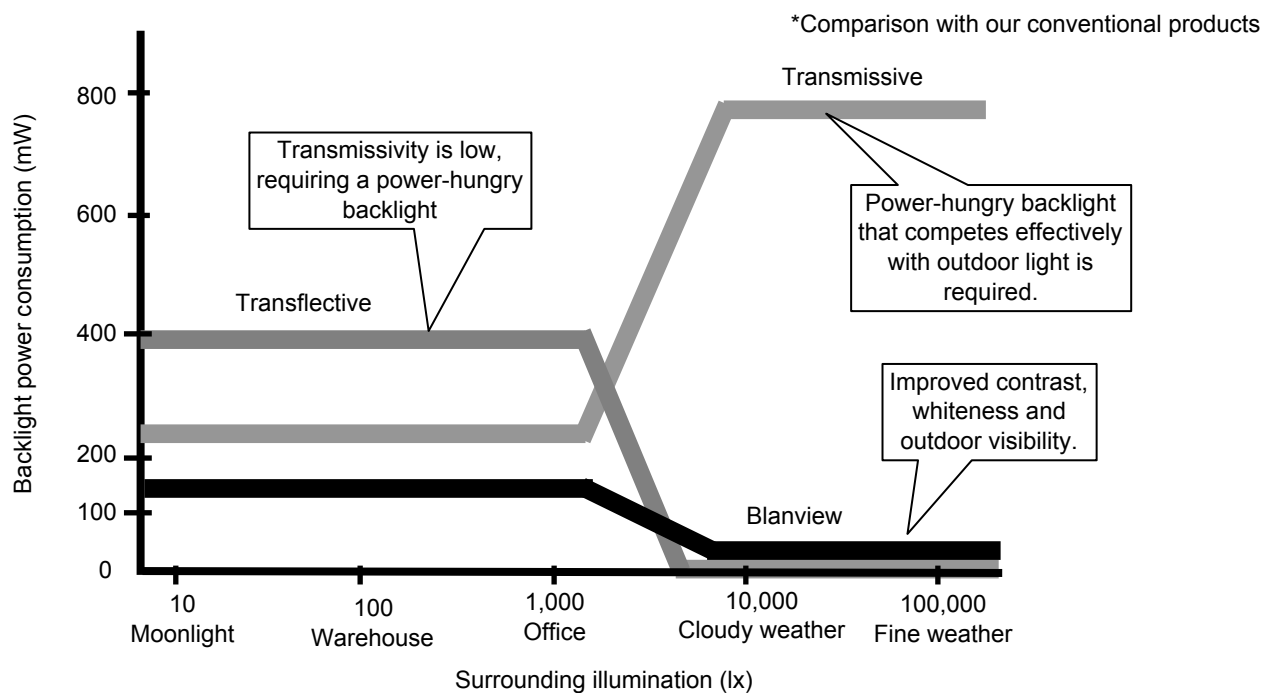
Items	Specifications	Remarks
Display type	TN type 16,777,216 colors. Blanview, Normally white.	
Driving method	a-Si TFT Active matrix. Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement".
Signal input method	8-bit RGB, parallel input.	
Backlight type	High bright white LED.	



Dot arrangement (FPC cable placed downside)

<Features of Blanview>

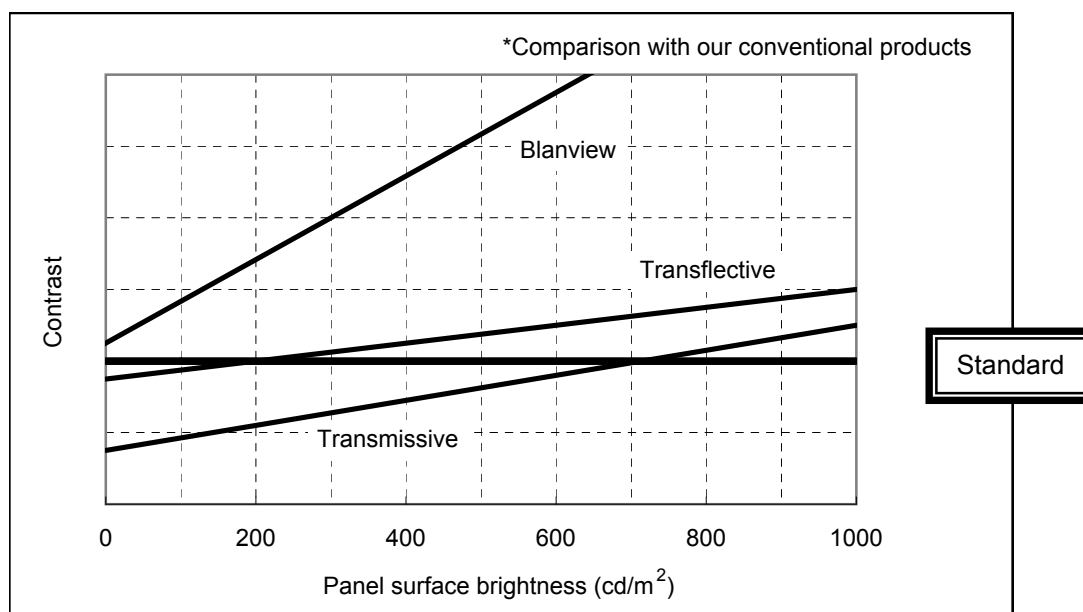
- Backlight power consumption required to assure visibility. (equivalent to 3.5"QVGA)



- Contrast characteristics under 100,000lx. (same condition as direct sunlight.)

With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line. (ORTUS TECHNOLOGY criteria)

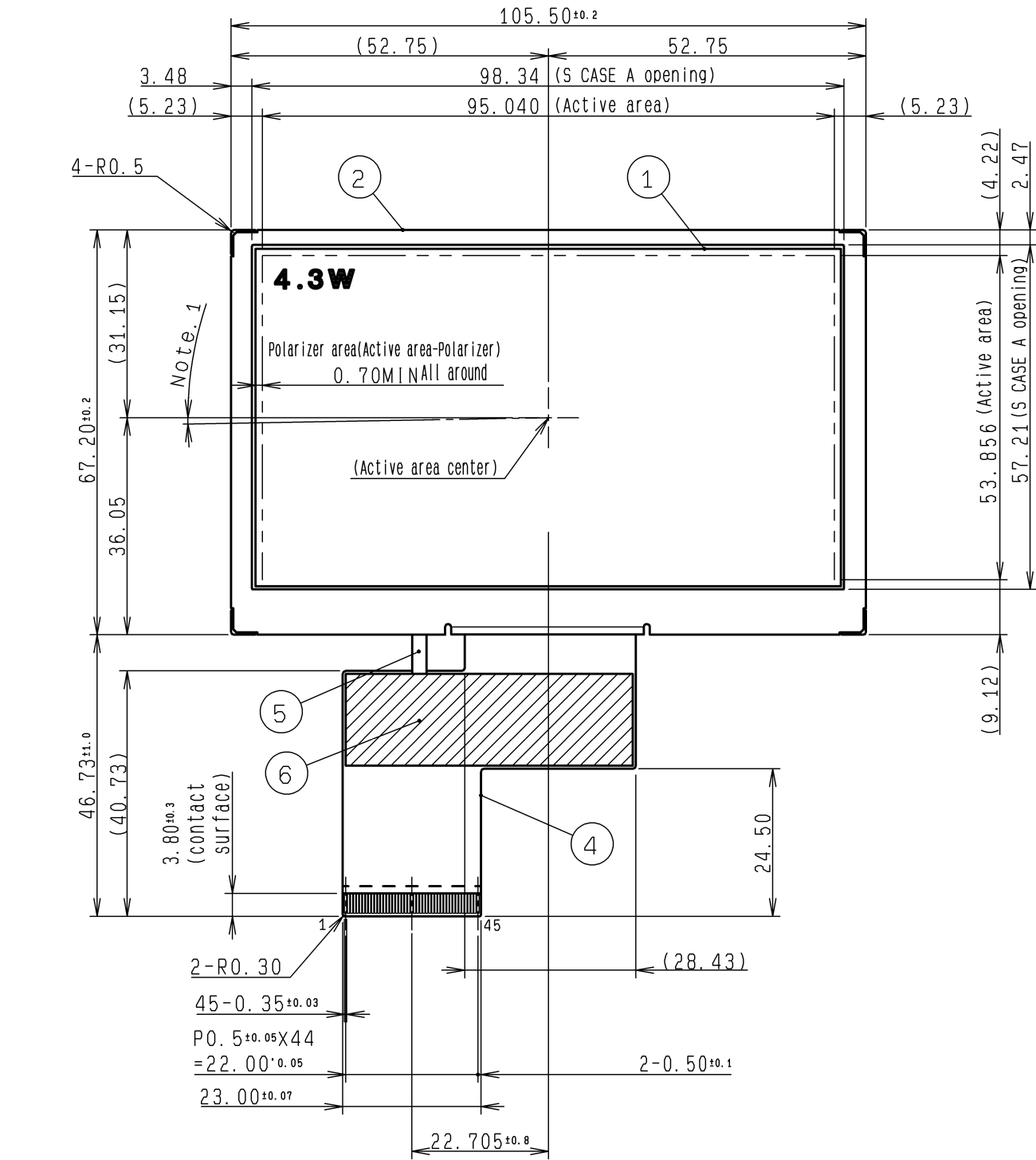


3. Dimensions and Shape

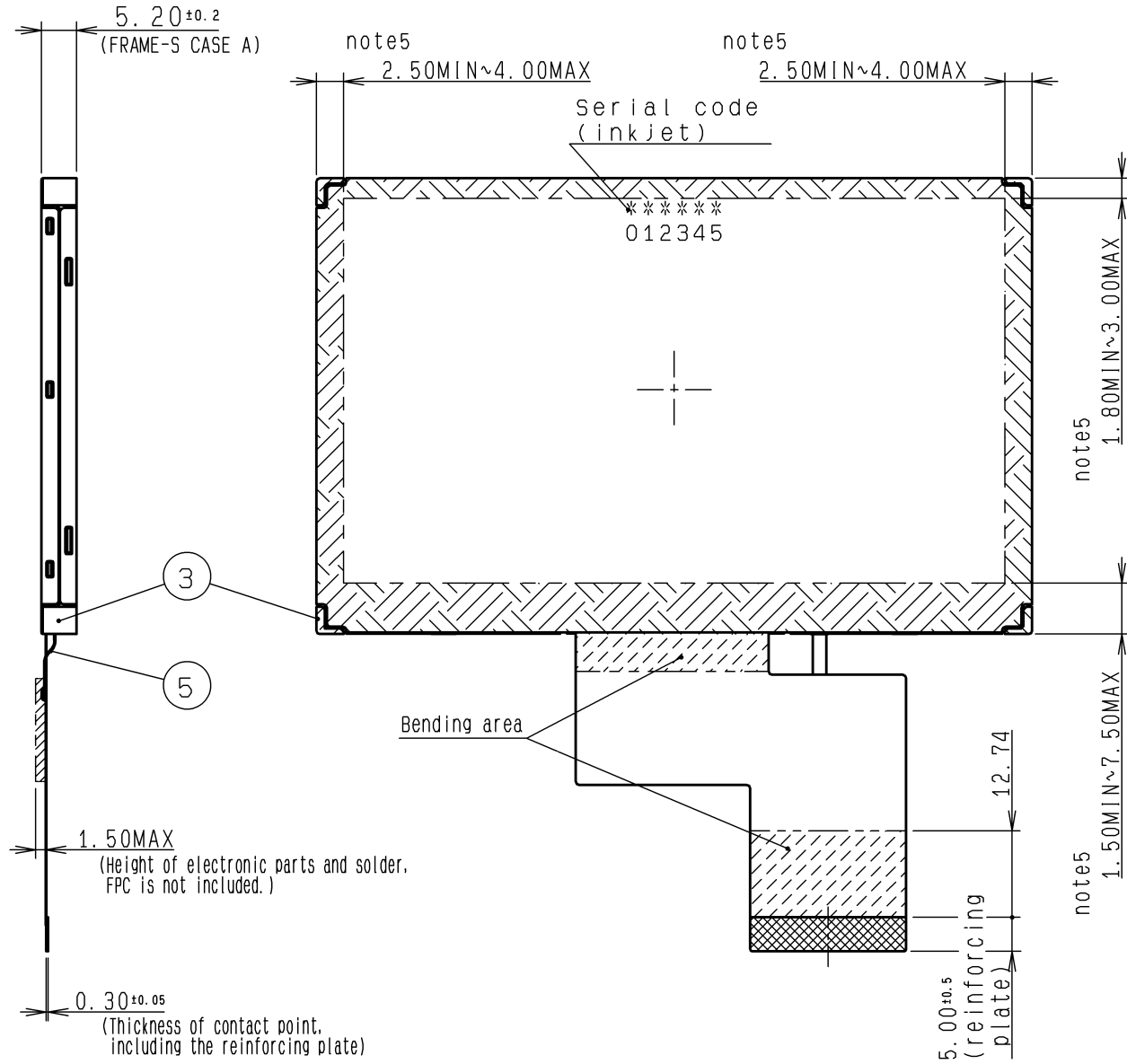
3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	105.50[H] × 67.20[V] × 5.20[D]	mm	Exclude FPC cable.
Active area	95.040[H] × 53.856[V]	mm	10.9cm diagonal.
Number of dots	1,440[H] × 272[V]	dot	
Dot pitch	66.0[H] × 198.0[V]	μm	
Surface hardness of the polarizer	3	H	Load:2.0N
Weight	58	g	Include FPC cable.

3.2 Outward Form



- Note.1 Angular deviation of LCD cell from the TFT-LCD monitor's reference axis shall be less than [±40'].
- Note.2 Recommended FPC connectors
For LCD : HIROSE/FH12A-45S-0.5SH(55)(Top contact)
FPC pin assignment differs from a position of Datum Pin of recommended FPC connector.
Please notice the difference when designing your circuit with much care.
- Note.3 Protective film is affixed on front surface of the screen.
Location tolerance of the protective film shall be ±1.5 mm to the polarizer.
- Note.4 Exercise care not to apply any forces to the cable holder of the S CASE A.
- Note.5 In case TFT-LCD monitor is fixed to the case of your product, it's recommended that monitor is fixed in to area.
- Note.6 Refer to "11.CRITERIA OF JUDGMENT" about the appearance specification of a polarizer.



	10			
	9			
	8			
	7			
I TAPE	6			
LED FPC	5			
LCD FPC	4			
FRAME	3			
S CASE A	2			
TFT-LCD PANEL	1		Glass substrate thickness=0.5t	
PART NAME	Part No.	Spec.No., Standard	Material	Remarks
APPROVED K.Hashizume	GENERAL TOLERANCE ±0.5	SCALE 1/1	UNIT mm	ORTUSTECH ORTUS TECHNOLOGY CO., LTD. DO NOT DUPLICATE, CONFIDENTIAL AND PROPRIETARY
CHECKED	ISSUE (Y:M:D) 15:12:25	MODEL DX-4M43K**		
CHECKED	NAME			
DESIGN J.Hiranobu				
DRAW J.Hiranobu				
OUTLINE-D4M43				
RJD527832D301				ASS'Y

(8/32)
16TLM002
Issue: Jan. 22, 2016

3.3 Serial № print (S-print)

1) Display Items

S-print indicates the least significant digit of manufacture year (1digit), manufacture month with below alphabet (1letter), model code (5characters), serial number (6digits).

* Contents of Display

*	*	*****	*****
-	-		
a	b	c	d

	Contents of display			
a	The least significant digit of manufacture year			
b	Manufacture month	Jan-A Feb-B Mar-C Apr-D	May-E Jun-F Jul-G Aug-H	Sep-I Oct-J Nov-K Dec-L
c	Model code	43BXC (Made in Japan) 43BYC (Made in Malaysia)		
d	Serial number			

* Example of indication of Serial № print (S-print)

•Made in Japan

6J43BXC000125

means "manufactured in October 2016, 4.3" BX type, C specifications, serial number 000125"

•Made in Malaysia

6J43BYC000125

means "manufactured in October 2016, 4.3" BY type, C specifications, serial number 000125"

2) Location of Serial № print (S-print)

Refer to 3.2 "Outward Form".

3)Others

Please note that it is likely to disappear with an organic solvent about the Serial print.

4. Pin Assignment

No.	Symbol	Function
1	VSS	GND.
2	VSS	GND.
3	VDD	Power supply.
4	VDD	Power supply.
5	D00	Display data(R). 00h: Black D00:LSB D07:MSB Driver has internal gamma conversion.
6	D01	
7	D02	
8	D03	
9	D04	
10	D05	
11	D06	
12	D07	Display data(G). 00h: Black D10:LSB D17:MSB Driver has internal gamma conversion.
13	D10	
14	D11	
15	D12	
16	D13	
17	D14	
18	D15	
19	D16	Display data(B). 00h: Black D20:LSB D27:MSB Driver has internal gamma conversion.
20	D17	
21	D20	
22	D21	
23	D22	
24	D23	
25	D24	
26	D25	
27	D26	
28	D27	
29	VSS	GND.
30	CLK	Clock signal.Latching data at the falling edge.
31	STBYB	Standby signal input. (Hi:Normal operation, Lo:Standby operation)
32	HSYNC	Horizontal sync signal input. (Low active)
33	VSYNC	Vertical sync signal input. (Low active)
34	DE	Input data effective signal. (It is effective for the period of "Hi")
35	NC	OPEN.
36	VSS	GND.
37	NC	OPEN.
38	NC	OPEN.
39	NC	OPEN.
40	NC	OPEN.
41	VSS	GND.
42	BLL	Backlight drive (cathode side)
43	BLH	Backlight drive (anode side)
44	NC	OPEN.
45	NC	OPEN.

- Recommended connector: HIROSE ELECTRIC FH12 series [FH12A-45S-0.5SH(55)]
- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit. Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

5. Absolute Maximum Rating

VSS=0V

Item	Symbol	Condition	Rating		Unit	Applicable terminal
			MIN	MAX		
Supply voltage	VDD	Ta=25° C	-0.3	5.0	V	VDD
Input voltage for logic	VI		-0.3	VDD+0.3	V	CLK, VSYNC, HSYNC, DE, D[27:20], D[17:10], D[07:00], STBYB
LED direction current of order	IL		--	70	mA	BLH - BLL
Storage temperature range	Tstg		-40	95	° C	
Storage humidity range	Hstg	Non condensing in an environmental moisture at or less than 40 ° C 90%RH.				

6. Recommended Operating Conditions

VSS=0V

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Supply voltage	VDD		3.0	3.3	3.6	V	VDD
Input voltage for logic	VI	VDD=3.0~3.6V	0	--	VDD	V	CLK, VSYNC, HSYNC, DE, D[27:20], D[17:10], D[07:00], STBYB
Operating temperature range	Top	Note 1	-30	25	85	° C	Panel surface temperature
Operating humidity range	Hop	Ta ≤ 30° C	20	--	80	%	
		Ta > 30° C	Non condensing in an environmental moisture at or less than 30 ° C 80%RH.				

Note1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to Item "10. CHARACTERISTICS".

7. Characteristics

7.1 DC Characteristics

7.1.1 Display Module

(Unless otherwise noted, $T_a=25^{\circ}\text{C}$, $V_{DD}=3.3\text{V}$, $V_{SS}=0\text{V}$)

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Input voltage for logic	V_{IH}	$V_{DD}=3.0\sim 3.6\text{V}$	$0.7\times V_{DD}$	--	V_{DD}	V	CLK, VSYNC, HSYNC, DE, D[27:20], D[17:10], D[07:00], STBYB
	V_{IL}		0	--	$0.3\times V_{DD}$	V	
Pull down resistor value	Rpd		--	200	--	k Ω	DE, D[27:20], D[17:10], D[07:00]
Pull up resistor value	Rpu		--	200	--	k Ω	VSYNC, HSYNC, STBYB
Current consumption	IDD	fCLK=9MHz Color bar display	--	17	34	mA	VDD
Standby Current	IDDs	Other input with constant voltage	--	100	200	μA	

7.1.2 Backlight

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	$T_a=25^{\circ}\text{C}$	--	25	70	mA	BLH - BLL
Forward voltage	VL	$T_a=25^{\circ}\text{C}$, $I_L=25\text{mA}$	--	13.75	15.75	V	*Reference value
Estimated Life of LED	LL	$T_a=25^{\circ}\text{C}$, $I_L=25\text{mA}$ Note	--	(50,000)	--	hr	

Note: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.

- This figure is estimated for an LED operating alone.

As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.

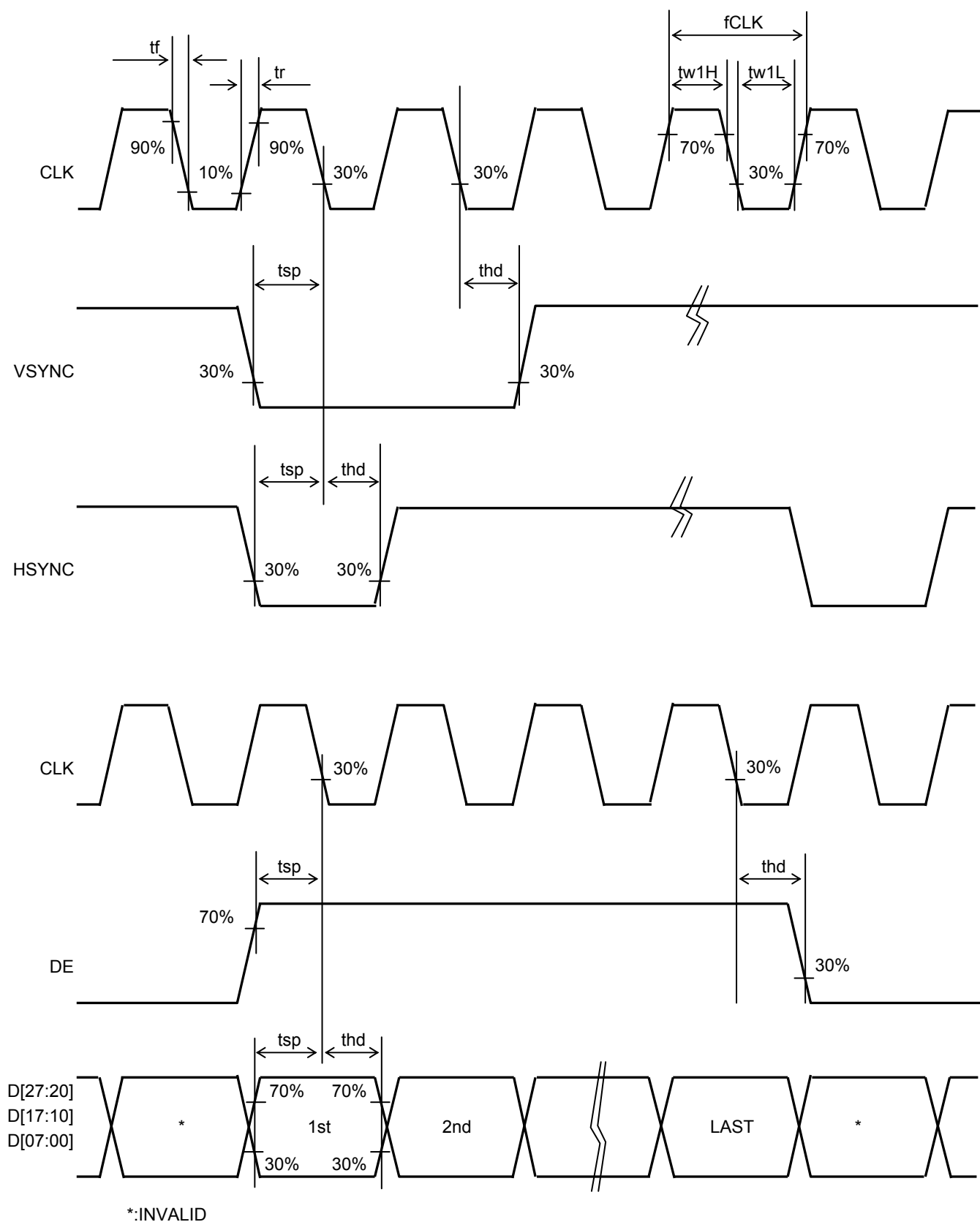
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

7.2 AC Characteristics

(Unless otherwise noted, $T_a=25^{\circ}\text{C}$, $V_{DD}=3.3\text{V}$, $V_{SS}=0\text{V}$)

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
CLK frequency	fCLK		5.0	9.0	12.0	MHz	CLK
CLK rising time	t_r	10% $\rightarrow$ 90%	--	--	9	ns	
CLK falling time	t_f	90% $\rightarrow$ 10%	--	--	9	ns	
CLK Low period	tw_{1L}	$0.3\times V_{DD}$ or less.	$0.4/f_{CLK}$	--	$0.6/f_{CLK}$	ns	
CLK High period	tw_{1H}	$0.7\times V_{DD}$ or more.	$0.4/f_{CLK}$	--	$0.6/f_{CLK}$	ns	
Setup time	tsp		12.0	--	--	ns	CLK, VSYNC, HSYNC, DE, D[27:20], D[17:10], D[07:00]
Hold time	thd		12.0	--	--	ns	

Switching Waveform Characteristics



7.3 Input Timing Characteristics

(Unless otherwise noted, Ta=25°C, VDD=3.3V, VSS=0V)

Item	Symbol	Rating			Unit	Applicable terminal
		MIN	TYP	MAX		
VSYNC frequency Note	fVSYNC	54	60	66	Hz	VSYNC
VSYNC signal cycle time	tv	277	288	400	H	VSYNC, HSYNC
VSYNC pulse width	tw2H	1	--	--	H	
Vertical back porch	tvb	3	8	31	H	
Vertical front porch	tvf	2	8	93	H	
Vertical display period	tvd	--	272	--	H	VSYNC, HSYNC, DE, D[27:20], D[17:10], D[07:00]
HSYNC frequency	fHSYNC	15.38	16.67	18.18	Khz	HSYNC
HSYNC signal cycle time	th	520	525	800	CLK	HSYNC, DE, CLK
HSYNC pulse width	tw3H	1	--	--	CLK	
Horizontal back porch	thb	36	40	255	CLK	
Horizontal front porch	thf	4	5	65	CLK	
Horizontal display period	thdp	--	480	--	CLK	DE, D[27:20], D[17:10], D[07:00], CLK
DE pulse width	tw4H	--	480	--	CLK	DE, CLK

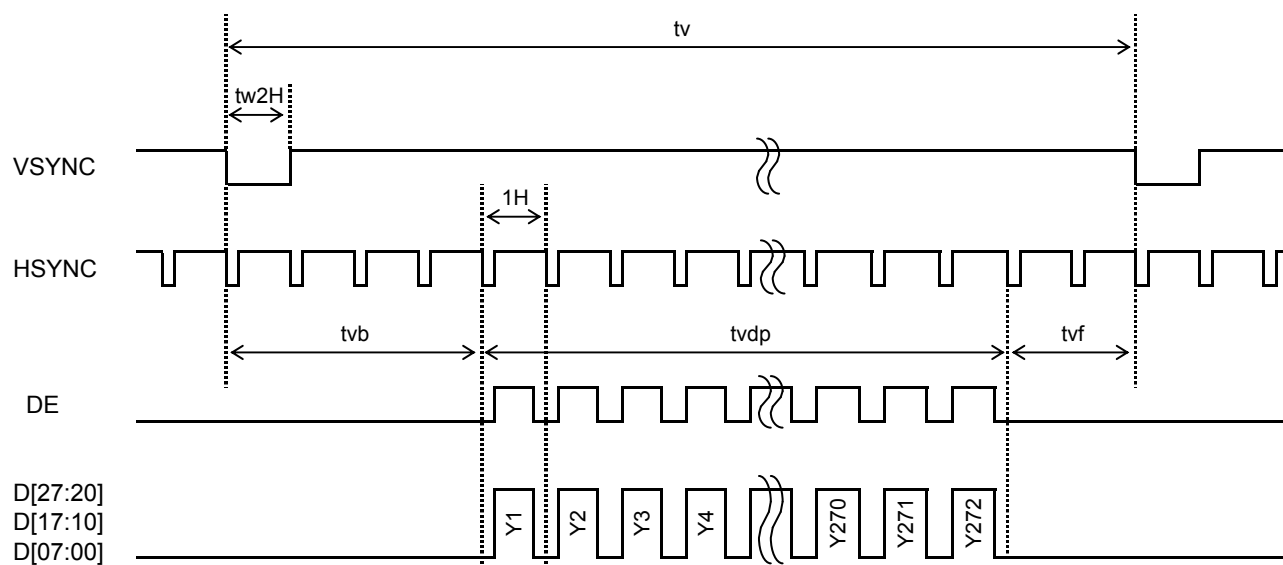
Note: The characteristic of this item is recommended standard.

Please use it after it confirms it enough like the display fineness etc.

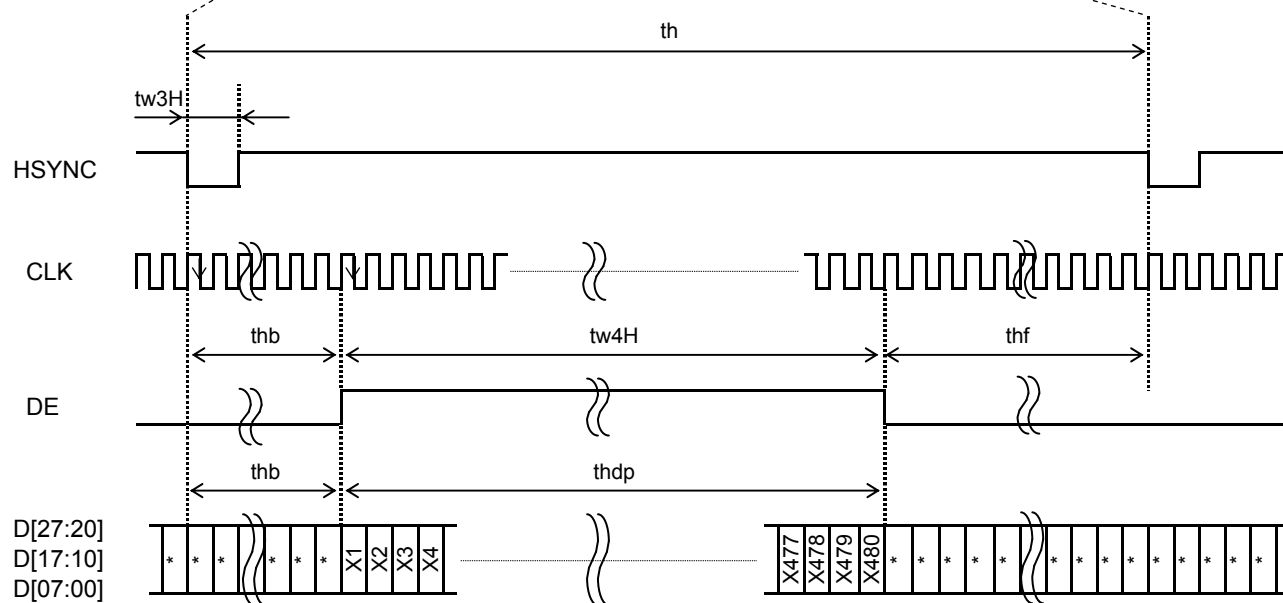
When it comes off from this characteristic and it is used.

7.4 Driving Timing Chart

-Vertical Timing

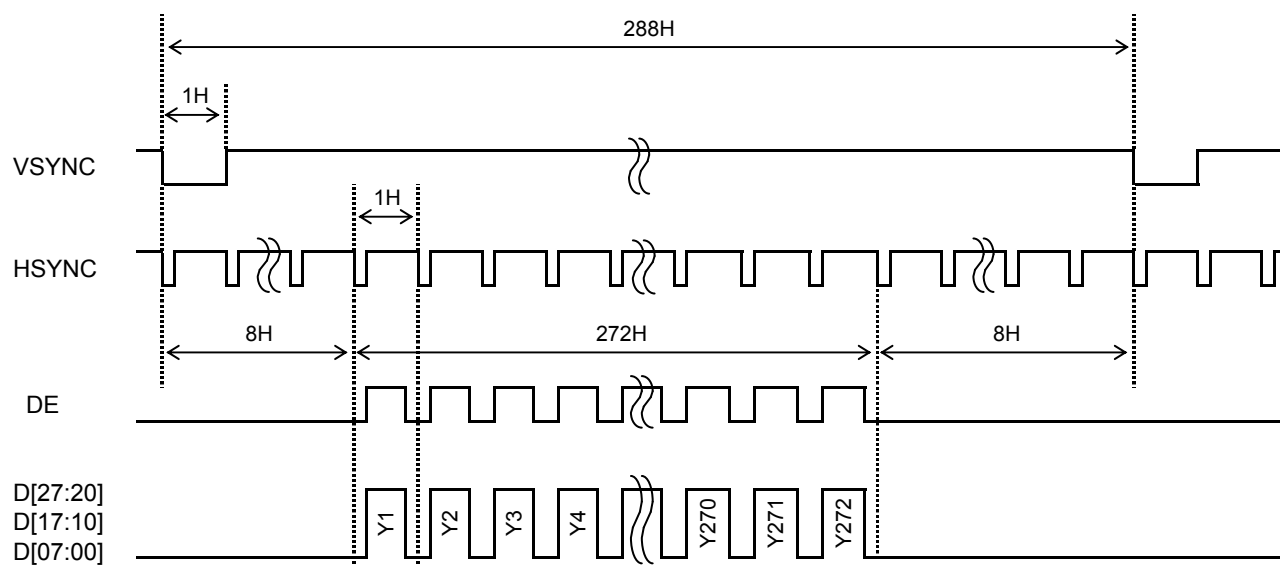


-Horizontal Timing

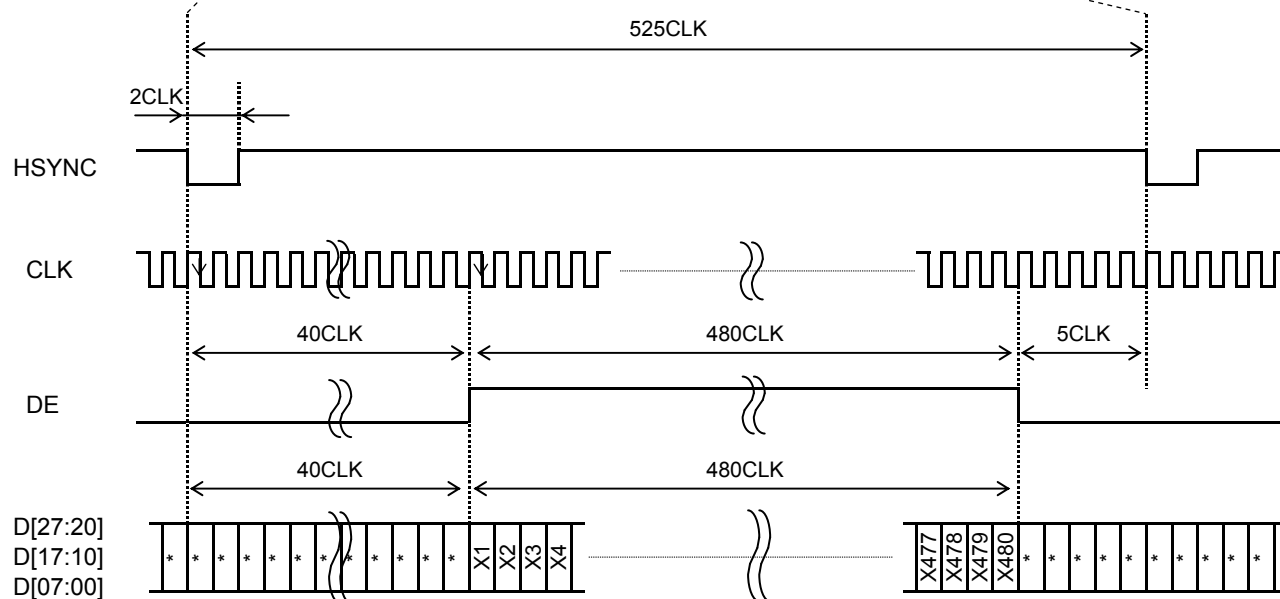


7.5 Example of Driving Timing Chart (fCLK=9.0MHz)

-Vertical Timing

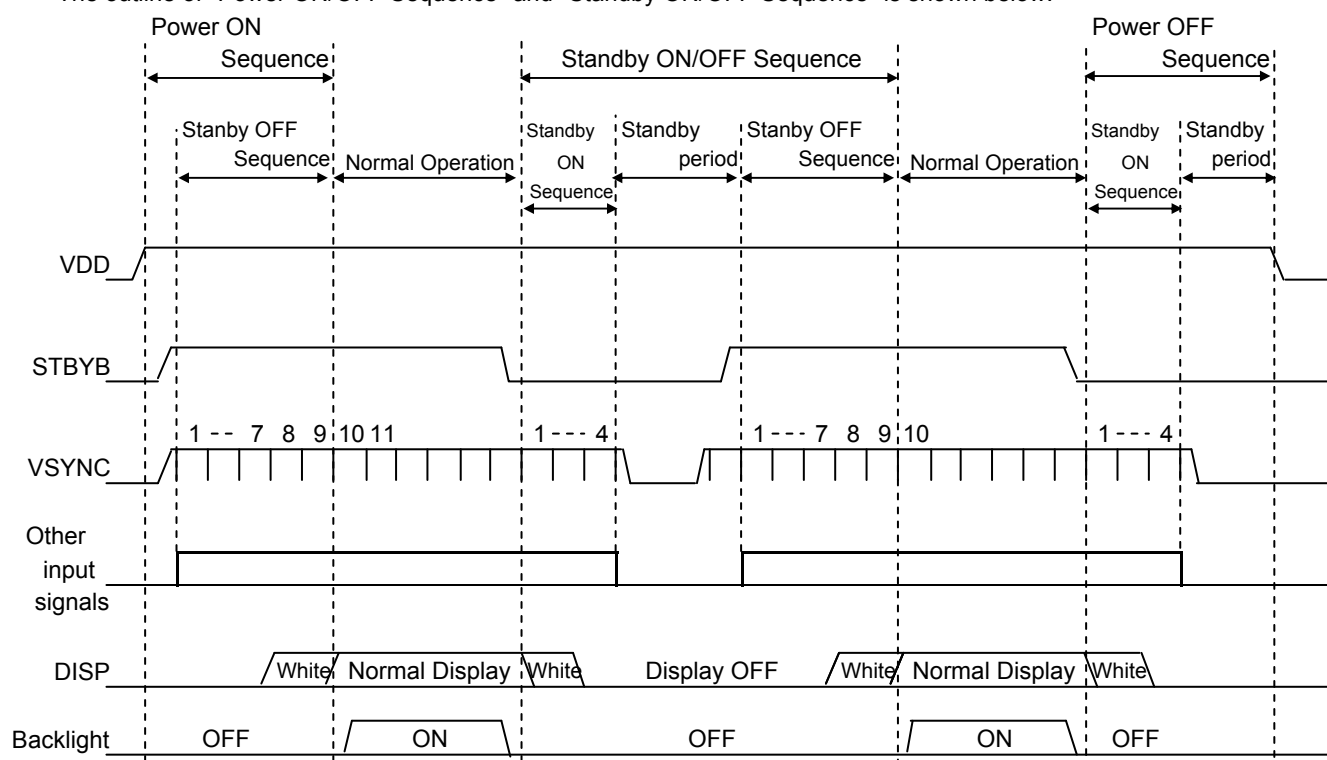


-Horizontal Timing



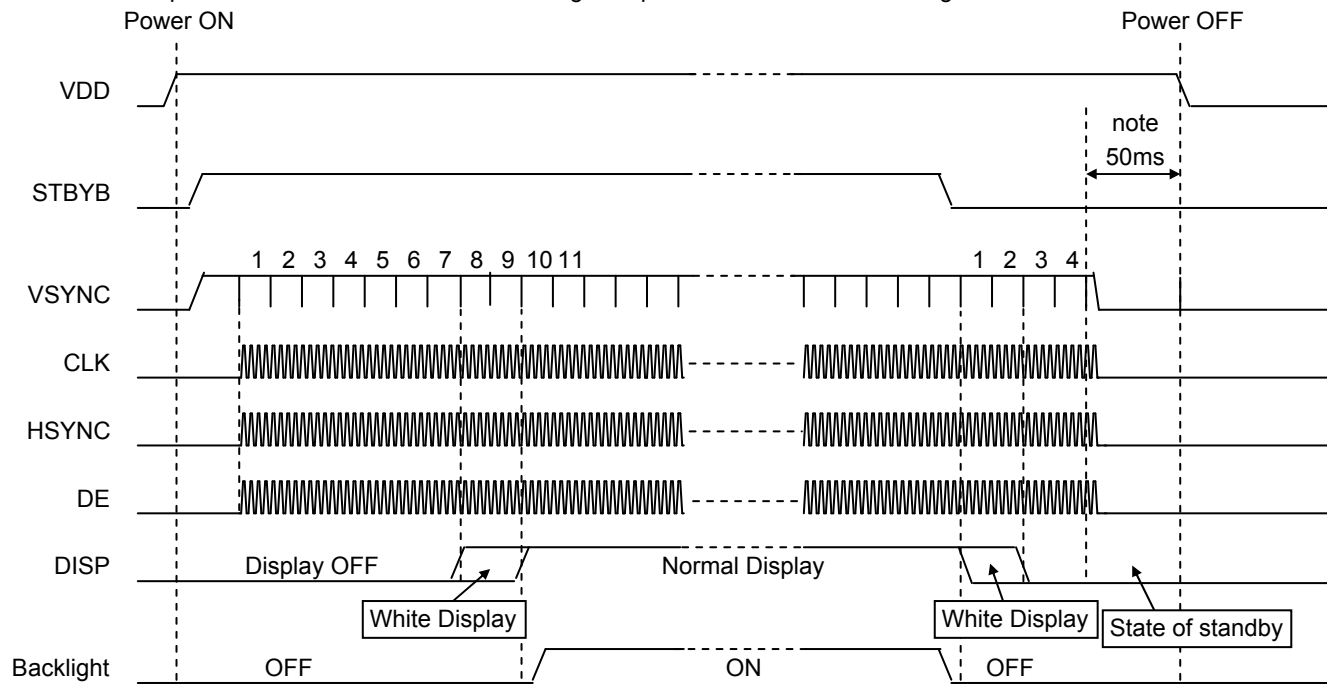
8. Description of Sequence

The outline of "Power ON/OFF Sequence" and "Standby ON/OFF Sequence" is shown below.



8.1 Power ON/OFF Sequence

The sequence of the Power On/Off and the signal input must defend the following conditions.



Note: For Power OFF, please turn off VDD since 50msec after the standby state shifts.

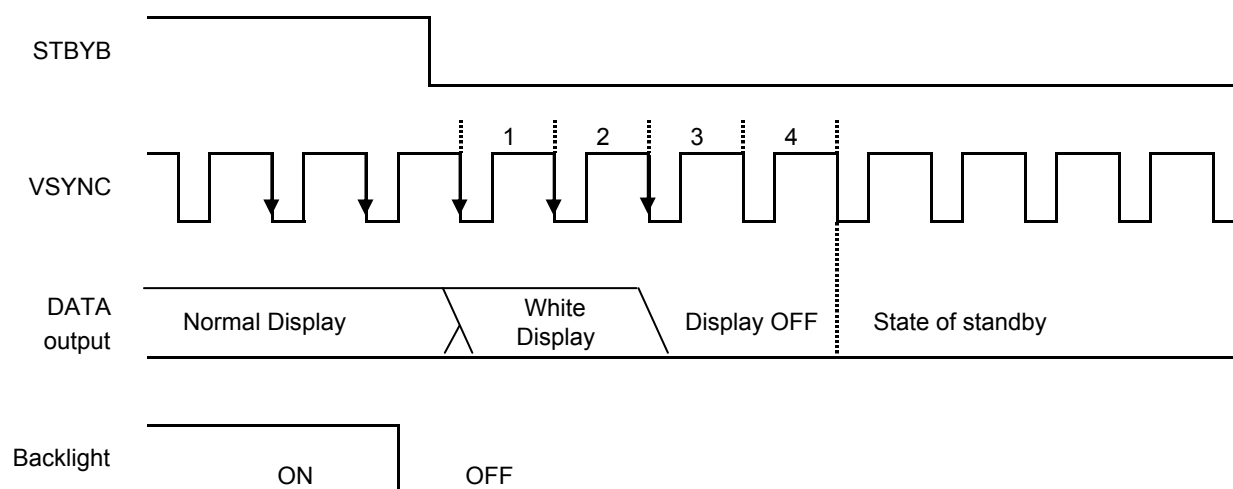
When CLK and the VSYNC signal are stopped or the power supply is turned off to a regulated frame or less, the afterimage might remain.

8.2 Standby ON/OFF Sequence

It explains Standby ON/OFF sequence by the STBYB signal.

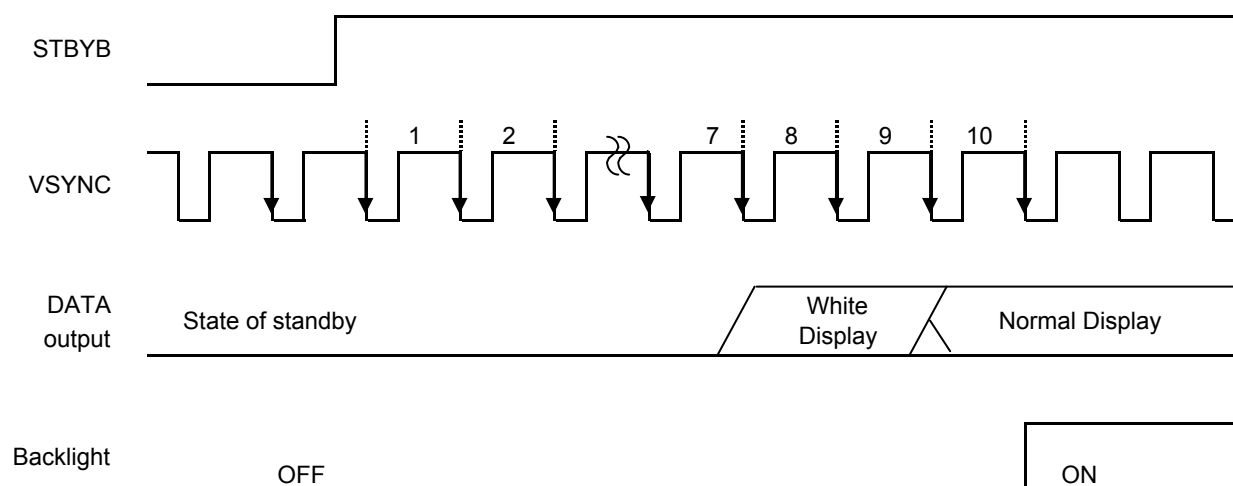
The following time will be needed by the shift in the state of the standby from the standby setting according to the STBYB signal.

Meanwhile, VSYNC signal and the CLK signal should keep being supplied.

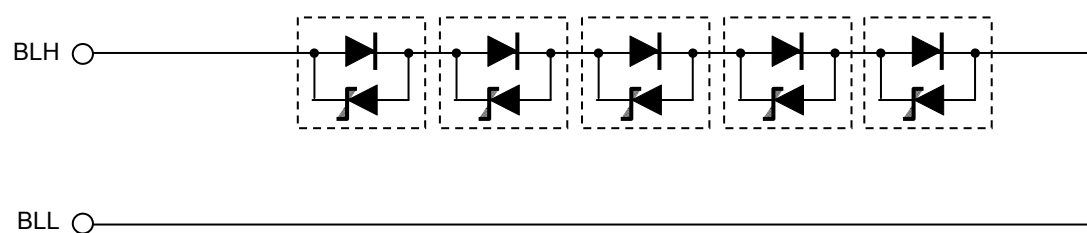


Similarly, the time of nine frames will be needed by the time a usual display is begun from the standby release by the STBYB signal.

Please begin outputting in the 8th frame on the Display Data.



9. LED Circuit



10. Characteristics

10.1 Optical Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) ,
EZcontrast160D (ELDIM)

Driving condition: VDD = 3.3V, VSS = 0V
Optimized VCOMDC

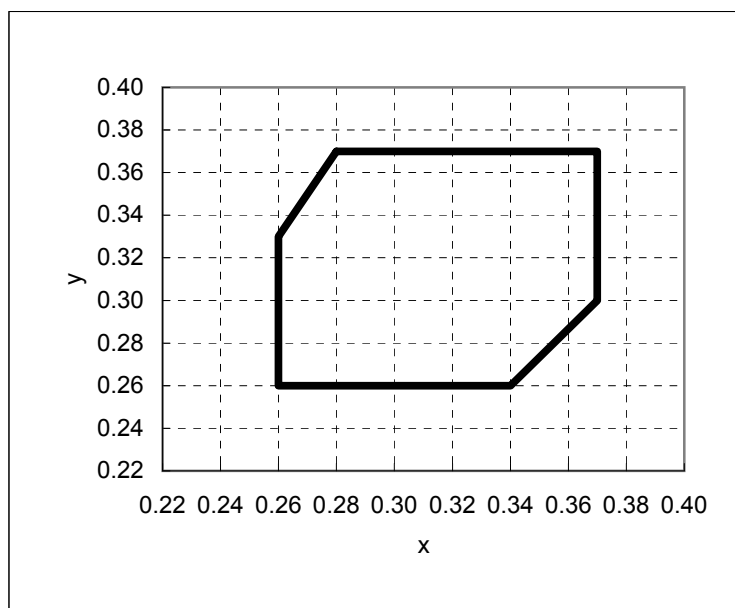
Backlight: IL=25mA

Measured temperature: Ta=25° C

Item		Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
Response time	Rise time	TON	[Data]= FFh→00h	—	—	40	ms	1	※
	Fall time	TOFF	[Data]= 00h→FFh	—	—	60	ms		
Contrast ratio	Backlight ON	CR	[Data]= FFh/00h	240	400	—		2	
	Backlight OFF			—	5.5	—			
Viewing angle	Left	θL	[Data]= FFh/00h CR ≥ 10	80	—	—	deg	3	※
	Right	θR		80	—	—	deg		
	Up	φU		80	—	—	deg		
	Down	φD		80	—	—	deg		
White Chromaticity	x			White chromaticity range				4	
	y								
Burn-in				No noticeable burn-in image shall be observed after 2 hours of window pattern display.				5	
Center brightness			[Data]=FFh	315	550	—	cd/m ²	6	
Brightness distribution			[Data]=FFh	75	—	—	%	7	

* Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics".

※ Measured in the form of LCD module.



White Chromaticity Range

【White Chromaticity Range】

x	y
0.28	0.37
0.26	0.33
0.26	0.26
0.34	0.26
0.37	0.30
0.37	0.37

10.2 Temperature Characteristics

< Measurement Condition >

Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS)

Driving condition: VDD = 3.3V, VSS = 0V

Optimized VCOMDC

Backlight: IL=25mA

Item			Specification		Remark
			Ta=-20°C	Ta=70°C	
Contrast ratio		CR	40 or more	40 or more	Backlight ON
Response time	Rise time	TON	200 msec or less	30 msec or less	※
	Fall time	TOFF	300 msec or less	50 msec or less	※
Display Quality			No noticeable display defect or ununiformity should be observed.		Use the criteria for judgment specified in the section 11.

※ Measured in the form of LCD module.

11. Criteria of Judgment

11.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions
 Driving Signal Raster Patter (RGB, white, black)
 Signal condition [Data]: 00h, 70h, FFh (3steps)
 Observation distance 30 cm
 Illuminance 200 to 350 lx
 Backlight IL=25mA

Defect item		Defect content	Criteria
Display Quality	Line defect	Black, white or color line, 3 or more neighboring defective dots	Not exists
	Dot defect	Uneven brightness on dot-by-dot base due to defective TFT or CF, or dust is counted as dot defect (brighter dot, darker dot)	Refer to table 1
		High bright dot: Visible through 2% ND filter at [Data]=00h	
		Low bright dot: Visible through 5% ND filter at [Data]=0h	
Screen Quality	Dirt	Uneven brightness (white stain, black stain etc)	Invisible through 1% ND filter
	Foreign particle	Point-like	0.25mm < φ
			0.20mm < $\varphi \leq 0.25$ mm
			$\varphi \leq 0.20$ mm
			Ignored
	Liner	3.0mm < length and 0.08mm < width	N=0
		length ≤ 3.0 mm or width ≤ 0.08 mm	Ignored
	Others		Use boundary sample for judgment when necessary

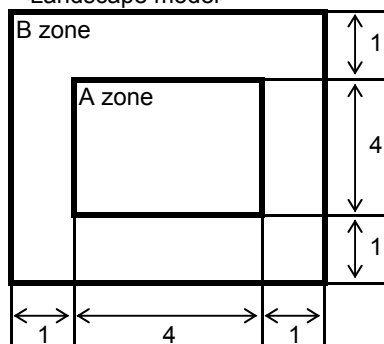
 φ (mm): Average diameter = (major axis + minor axis)/2

Permissible number: N

Table 1

Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
A	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more
B	2	4	4	6	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	7	

<Landscape model>



Division of A and B areas

B area: Active area

Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

11.2 Screen and Other Appearance

Testing conditions

Observation distance

30cm

Illuminance

1200~2000 lx

Item		Criteria	Remark
Polarizer	Flaw	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 3.2 "Outward form")
	Stain		
	Bubble		
	Dust		
	Dent		
S-case		No functional defect occurs	
FPC cable		No functional defect occurs	

12. Reliability Test

Test item		Test condition	number of failures /number of examinations
Durability test	High temperature storage	Ta=95°C 240hr	0 / 3
	Low temperature storage	Ta= -40°C 240hr	0 / 3
	High temperature & high humidity storage	Ta=60°C, RH=90% 240hr non condensing ※	0 / 3
	High temperature operation	Tp=85°C 240hr	0 / 3
	Low temperature operation	Tp= -30°C 240hr	0 / 3
	High temp & humid operation	Tp=40°C, RH=90% 240hr non condensing ※	0 / 3
	Thermal shock storage	-40←→95°C(30min/30min) 100 cycles	0 / 3
Mechanical environmental test	Electrostatic discharge test (Non operation)	Confirms to EIAJ ED-4701/300 C=200pF, R=0Ω, V=±200V Each 3 times of discharge on and power supply and other terminals.	0 / 3
	Surface discharge test (Non operation)	C=250pF, R=100Ω, V=±12kV Each 5 times of discharge in both polarities on the center of screen with the case grounded.	0 / 3
	FPC tension test	Pull the FPC with the force of 3N for 10 sec. in the direction -90-degree to its original direction.	0 / 3
	FPC bend test	Pull the FPC with the force of 3N for 10 sec. in the direction -180-degree to its original direction. Reciprocate it 3 times.	0 / 3
	Vibration test	Total amplitude 1.5mm, f=10~55Hz, X,Y,Z directions for each 2 hours	0 / 3
	Impact test	Use ORTUS TECHNOLOGY original jig (see next page)and make an impact with peak acceleration of 1000m/s ² for 6 msec with half sine-curve at 3 times to each X, Y, Z directions in conformance with JIS C 60068-2-27-2011.	0 / 3
Packing test	Packing vibration-proof test	Acceleration of 19.6m/s ² with frequency of 10→55→10Hz, X,Y, Zdirection for each 30 minutes	0 / 1 Packing
	Packing drop test	Drop from 75cm high. 1 time to each 6 surfaces, 3 edges, 1 corner	0 / 1 Packing

Note: Ta=ambient temperature Tp=Panel temperature

※ The profile of high temperature/humidity storage and High Temperature/humidity operation
(Pure water of over 10MΩ·cm shall be used.)

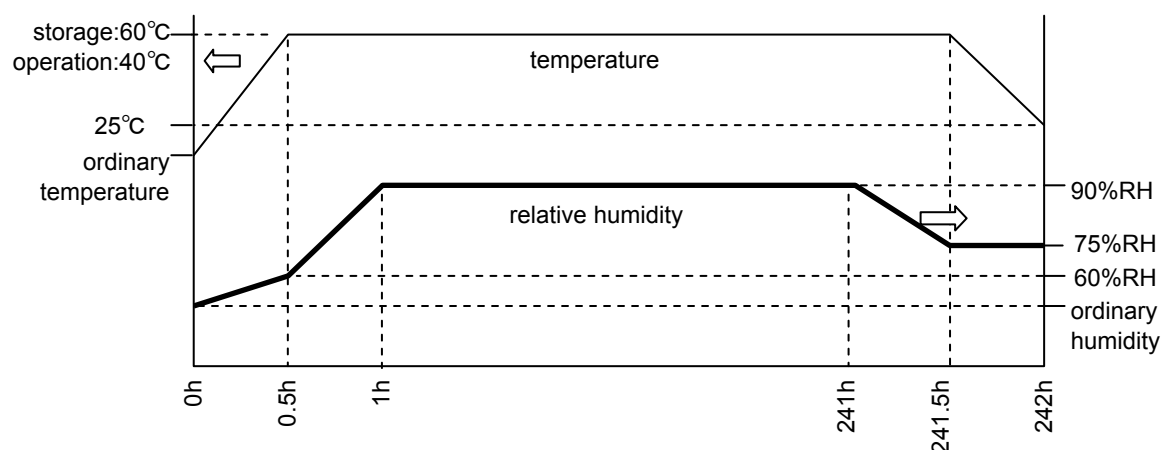
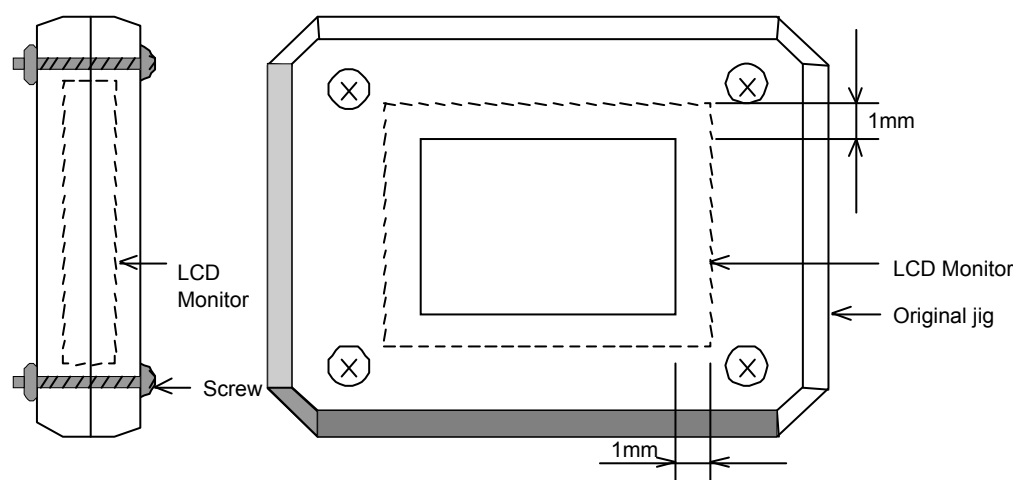


Table2.Reliability Criteria

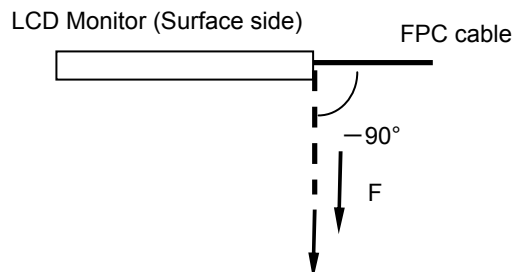
The parameters should be measured after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion.

item	Standard	Remarks
Display quality	No visible abnormality shall be seen. (Except for unevenness by PoI deterioration.)	
Contrast ratio	40 or more	Backlight ON

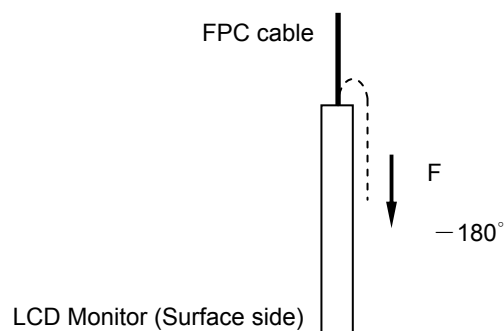
ORTUS TECHNOLOGY Original Jig



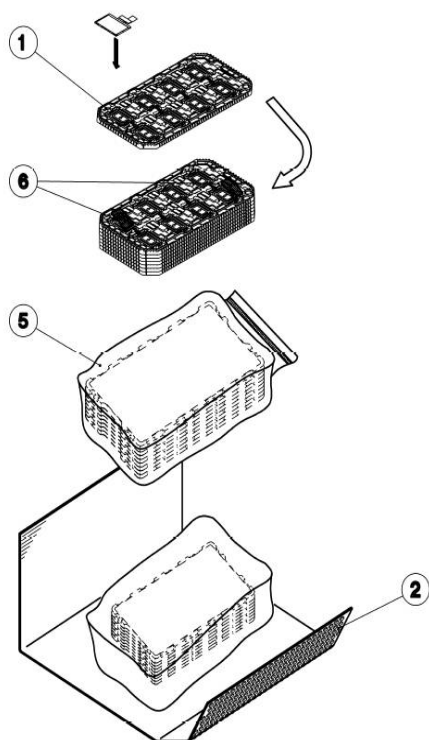
Tension Test Method for FPC cable



Bend Test Method for FPC cable



13. Packing Specifications



Step 1. Each product is to be placed in one of the cut-outs of the tray with the display surface facing upward. (8products per tray)

Step 2. Each tray is to be piled up in same orientation and the trays be in a stack of 6.
One empty tray is to be put on the top of stack of 6 trays.

Step 3. 2 packs of moisture absorbers are to be placed on the top tray as shown in the drawing.

Step 4. Put piled trays into a sealing bag.
Vacuum and seal the sealing bag with the vacuum sealing machine.

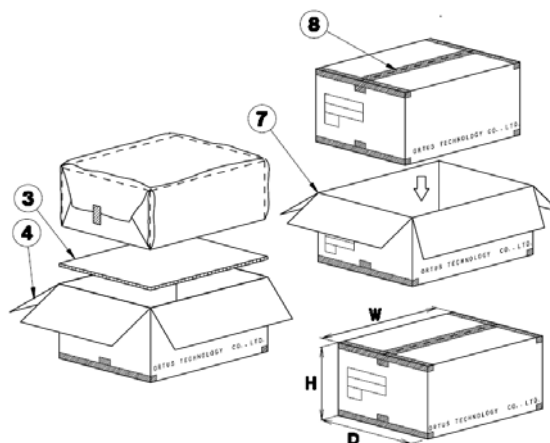
Step 5. The stack of trays in the plastic back is to be inserted into a inner carton.
A corrugated board is to be placed on the top and on the bottom of the inner carton.

Step 6. The two corrugated boards and the inner carton is to be inserted into an outer carton.

Step 7. The outer carton needs to sealed with packing tape as shown in the drawing.

Step 8. The model number, quantity of products, and shipping date are to be printed on the outer carton.
If necessary, shipping labels or impression markings are to be put on the outer carton.

Step 9. The outer carton is to be inserted into a extra outer carton with same direction.
The extra outer carton needs to sealed with packing tape as shown in the drawing.
The model number, quantity of products, and shipping date are to be printed on the extra outer carton.
If necessary, shipping labels or impression markings are to be put on the extra outer carton.



Remark: The return of packing materials is not required.

Packing item name	Specs., Material
① Tray	A-PET
② B SHEET A	Antistatic air bubble sheet
③ Inner board	Corrugated cardboard
④ Outer carton	Corrugated cardboard
⑤ Sealing bag	
⑥ Drier	Moisture absorber
⑦ Extra outer carton	Corrugated cardboard
⑧ Packing tape	
⑨ FOAM SHEET	Antistatic Polyethylene

Dimension of extra outer carton	
D : Approx.	(337mm)
W : Approx.	(618mm)
H : Approx.	(179mm)
Quantity of products packed in one carton:	48
Gross weight : Approx.	5.0Kg

14. Handling Instruction

14.1 Cautions for Handling LCD panels

**Caution**

- (1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
- (2) If the glass breaks, do not touch it with bare hands.
(Fragment of broken glass may stick you or you cut yourself on it.)
- (3) If you get injured, receive adequate first aid and consult a medial doctor.
- (4) Do not let liquid crystal get into your mouth.
(If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.)
- (5) If liquid crystal adheres, rinse it out thoroughly.
(If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.)
- (6) If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
- (7) Do not connect or disconnect this product while its application products is powered on.
- (8) Do not attempt to disassemble or modify this product as it is precision component.
- (9) If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please.
Please insulate it with the insulating tape etc. if necessary.
The defective operation is caused, and there is a possibility to generation of heat and the ignition.
- (10) Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnormal operation is generated.
We recommend you to add excess current protection circuit to power supply.
- (11) The devices on the FPC are damageable to electrostatic discharge, because the terminals of the devices are exposed.
Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors.
Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.

**Caution**

This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

14.2 Precautions for Handling

- 1) Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
Do not touch the surface of the monitor as it is easily scratched.
- 2) Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.
- 3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- 6) Do not stain or damage the contacts of the FPC cable .
FPC cable needs to be inserted until it can reach to the end of connector slot.
During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion.
Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- 7) Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
Especially, it will cause mechanical damage or critical defect if FPC is pull up or bent up to short of display.



- 8) Peel off the protective film on the TFT monitors during mounting process.
Refer to the section 14.5 on how to peel off the protective film.
We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

14.3 Precautions for Operation

- 1) Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- 2) In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- 3) Optimize VCOMDC within recommended operating conditions.
* When VCOMDC is not an optimal value, flicker and image sticking will be occurred.
- 4) Do not plug in or out the FPC cable while power supply is switch on.
Plug the FPC cable in and out while power supply is switched off.
- 5) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- 6) Do not display a fixed image on the screen for a long time.
Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time.
Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

14.4 Storage Condition for Shipping Cartons

Storage environment

- Temperature 0 to 40° C
- Humidity 60%RH or less
No-condensing occurs under low temperature with high humidity condition.
- Atmosphere No poisonous gas that can erode electronic components and/or wiring materials should be detected.
- Time period 1 year
- Unpacking To prevent damages caused by static electricity, anti-static precautionary measures (e.g. earthing, anti-static mat) should be implemented.
After unpack, keep product in the appropriate condition, otherwise bubble seal of Protective film may be printed on Polarizer.
- Maximum piling up 7 cartons

14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

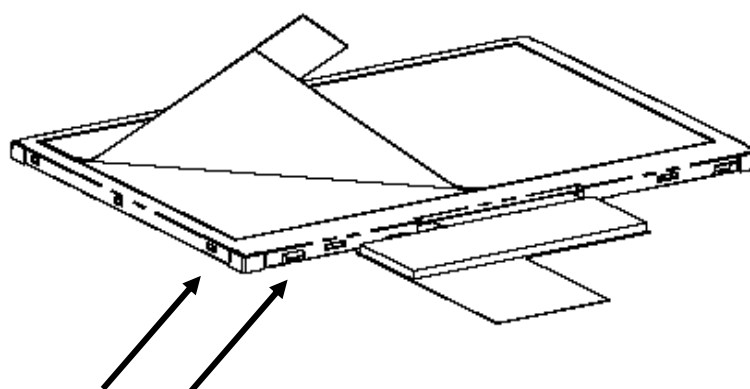
A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature 15 to 27° C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Anti-static treatment should be implemented to work area's floor.
- c) Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

B) Work Method

The following procedures should be taken to prevent the driver ICs from charging and discharging.

- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when FPC is placed at the bottom.
Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
- c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



Direction of blowing air
(Optimize air direction and the distance)

APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

1. Measurement Condition (Backlight ON)

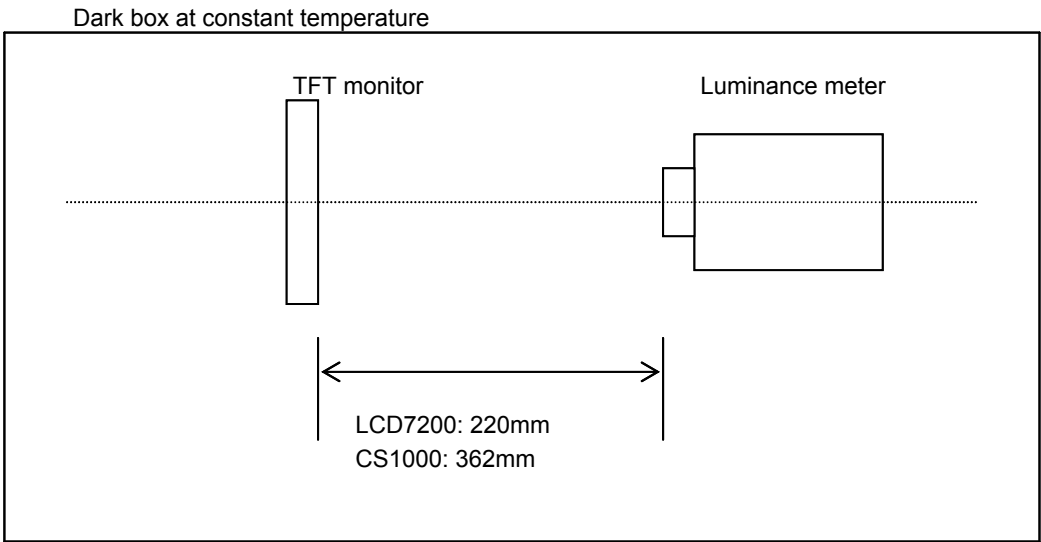
Measuring instruments: CS1000 (KONICA MINOLTA) , LCD7200(OTSUKA ELECTRONICS) ,EZcontrast160D (ELDIM)

Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25° C unless specified

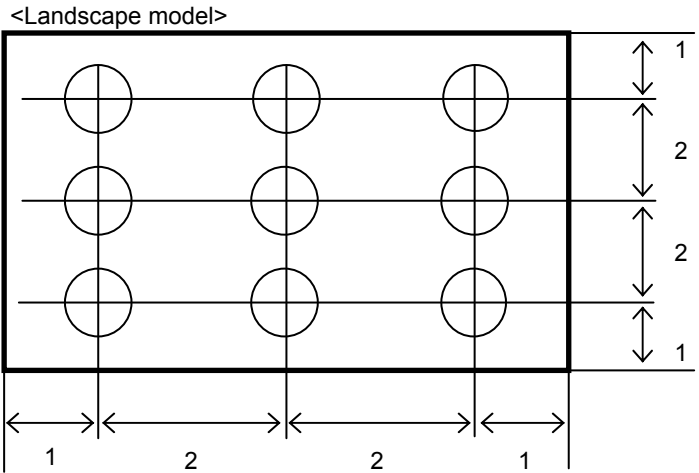
Measurement system: See the chart below. The luminance meter is placed on the normal line of measurement system.

Measurement point: At the center of the screen unless otherwise specified



Measurement is made after 30 minutes of lighting of the backlight.

Measurement point: At the center point of the screen
Brightness distribution: 9 points shown in the following drawing.



Dimensional ratio of active area

Backlight IL=25mA

Measurement Condition (Contrast ratio Backlight OFF only)

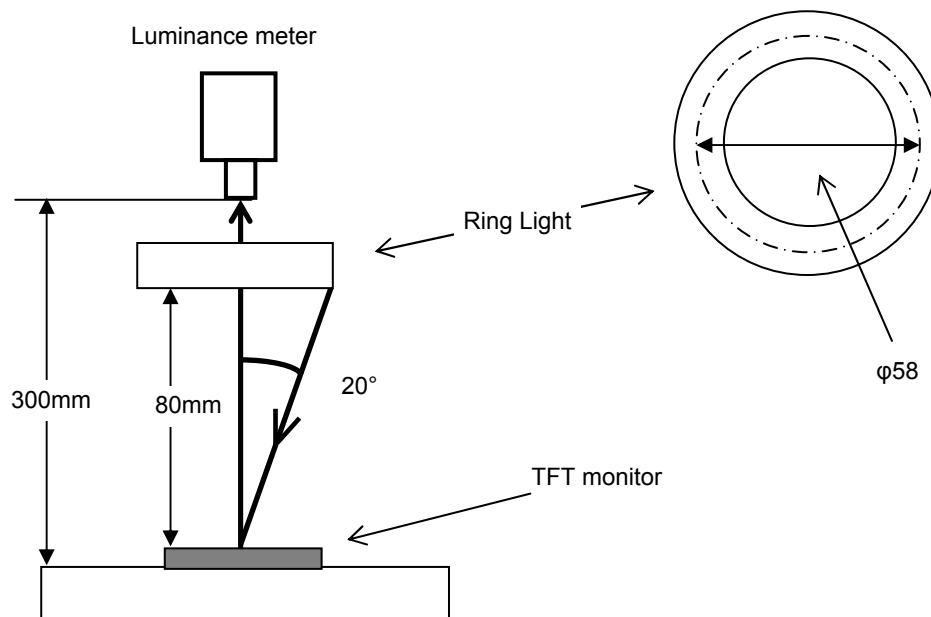
Measuring instruments: LCD7200(OTSUKA ELECTRONICS),Ring Light(40,000 lx,φ58)

Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25° C unless specified

Measurement system: See the chart below.

Measurement point: At the center of the screen.



2. Test Method

Notice	Item	Test method	Measuring instrument	Remark
1	Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white. White 100% 90% 10% 0% Black TON TOFF	LCD7200	Black display [Data]=00h White display [Data]=FFh TON Rise time TOFF Fall time
2	Contrast ratio	Measure maximum luminance Y1([Data]=FFh) and minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 8mmφ(CS1000) Diameter of measuring point: 3mmφ(LCD7200)	CS1000 LCD7200	Backlight ON Backlight OFF
3	Viewing angle Horizontalθ Verticalφ	Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is 10.	EZcontrast160D	
4	White chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at [Data] = FFh Color matching function: 2°view	CS1000	
5	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" ([Data]=00h/FFh).		
6	Center brightness	Measure the brightness at the center of the screen.	CS1000	
7	Brightness distribution	(Brightness distribution) = 100 x B/A % A : max. brightness of the 9 points B : min. brightness of the 9 points	CS1000	