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MODEL NO : ET020QV01-RMODEL VERSION: 01SPEC VERSION : 1.0ISSUED DATE: 2017-12-05

- ☒ Preliminary Specification
☐ Final Product Specification

Customer : _____

Approved by	Notes

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Record of Revision

[illegible]

1 General Specifications

Feature		Spec
Display Spec.	Size	2.0 inch
	Resolution	320 x RGB x 240
	Technology Type	a-Si
	Pixel Configuration	RGB Vertical Stripe
	Pixel pitch(mm)	0.1275×0.1275
	Display Mode	Transflective/Normally black
	Viewing Direction	Free angle
Mechanical Characteristics	LCM (W x H x D) (mm)	46.10 (W) X40.96(H) X2.53
	Active Area(mm)	40.80(W) X30.60 (H)
	With /Without TSP	Without TSP
	LED Numbers	3 LEDS
	Driver IC	ILI9342C
	Weight (g)	TBD
Electrical Characteristics	Interface	6bit MCU Interface
	Color Depth	262K

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: $\pm 5\%$

2 Input/Output Terminals

NO.	Symbol	Level	Function
1-11	NC	-	No connection.
12	GND	VSS	Ground.
13-18	NC	-	No connection.
19	LEDK		LED, cathode.
20-21	NC	-	No connection.
22	LEDA		LED, cathode.
23	GND	VSS	Ground.
24	VCC	2.8V(typ)	Power supply.
25	VSYNC	H/L	Frame synchronizing signal for RGB.
26	HSYNC	H/L	Line synchronizing signal for RGB.
27	DCLK	H/L	Dot clock signal for RGB.
28-29	GND	VSS	Ground.
30-35	DB0-DB5	H/L	Data bus.
36	SDA	H/L	Serial in/out signal.
37	SCL	H/L	Serial interface clock.
38	CS	H/L	Chip selection.
39	NC	-	No connection.
40	RESET	H/L	Reset signal.

3 Absolute Maximum Ratings

GND=0V

Item	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VCC	-0.3	4.6	V	Note1
Operating Temperature	Top	-20	70	°C	
Storage Temperature	Tst	-30	80	°C	

Table 3 Absolute Maximum Ratings

Note 1: Temperature and relative humidity range are shown in the figure below.

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

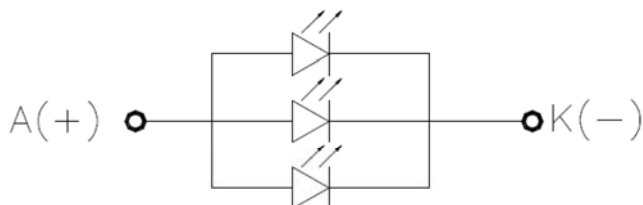
Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply Voltage	VCC	2.5	2.8	3.3	V	
Voltage for analog	IOVCC	1.65	1.8/2.8	3.3	V	
Input high level voltage	V _{IH}	0.7VDD	--	VDD	V	
Input low level voltage	V _{IL}	0	--	0.3VDD	V	

4.2 Backlight Unit

The backlight system is an edge-lighting type with 3 LED. The characteristics of the LED are shown in the following tables.

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Backlight forward voltage	V _F	2.7	3.2	3.4	V	3 LEDs
Backlight forward current	I _F	-	15	-	mA	
LED life time	-	20000	-	-	Hrs	

Note 1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table and the fL=50k Hz until the brightness becomes less than 50%.



背光电路图 (CIRCUIT DIAGRAM)

5 Timing Chart

8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus

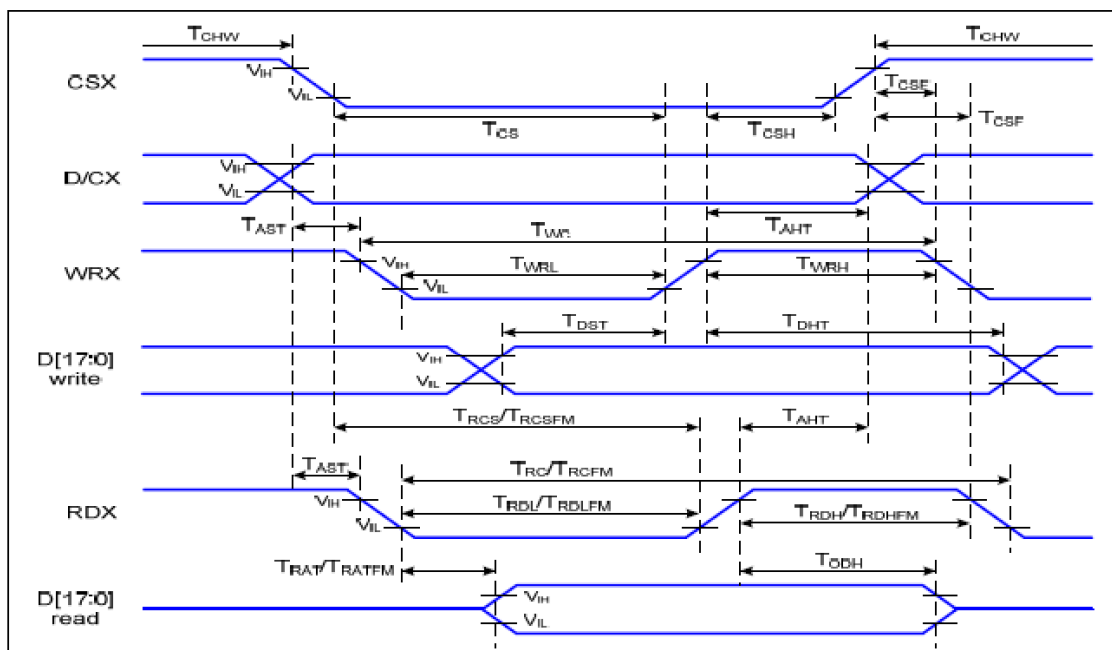


Figure 1 Parallel Interface Timing Characteristics (8080-Series MCU Interface)

VDD1=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta= -30 to 70 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	-
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	-
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	-
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF

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Reset Timing

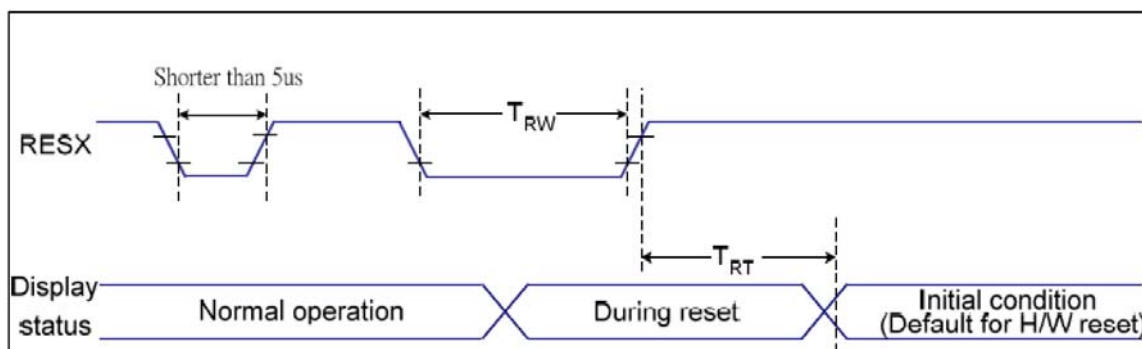


Figure 7 Reset Timing

$V_{DDI}=1.65$ to $3.3V$, $V_{DD}=2.4$ to $3.3V$, $AGND=DGND=0V$, $T_a=-30 \sim 70^{\circ}C$

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms

Table 8 Reset Timing

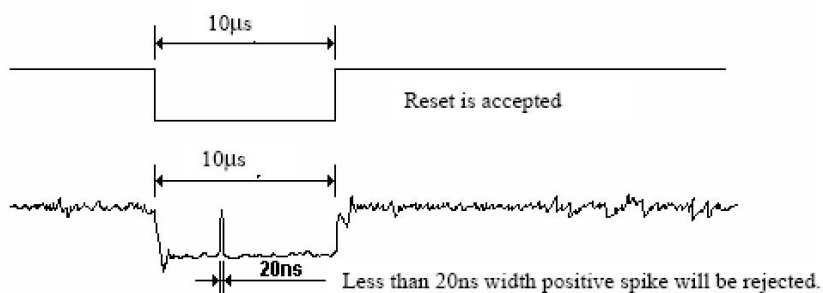
Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.

6. When Reset applied during Sleep Out Mode.

7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

6 Optical Characteristics

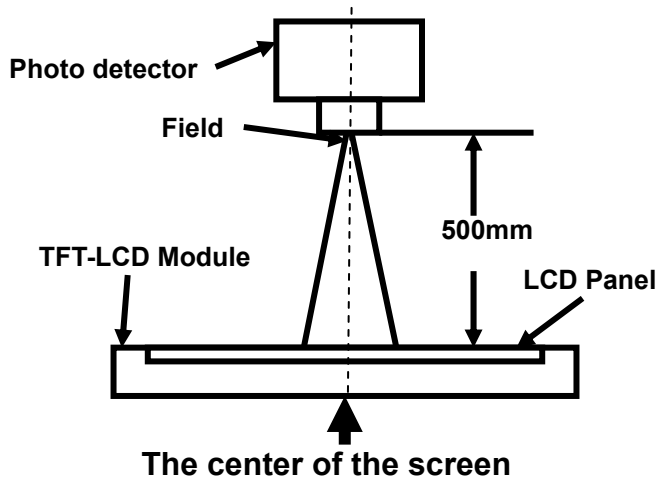
Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≧10	-	80	-	Degree	Note2,3
		θB		-	80	-		
		θL		-	80	-		
		θR		-	80	-		
Contrast Ratio		CR	θ=0°	-	500	-		Note 3
Response Time		T _{ON}	25℃	-	30	40	ms	Note 4
		T _{OFF}						
Chromaticity	White	x	Backlight is on	0.237	0.287	0.337		Note 1,5
		y		0.272	0.332	0.372		
	Red	x						Note 1,5
		y						
	Green	x						Note 1,5
		y						
	Blue	x						Note 1,5
		y						
Uniformity		U		75	80	--	%	Note 6
Luminance		L		-	150	-	cd/m ²	Note 7
Reflectance				5.0	7.0		%	

Test Conditions:

1. $I_F = 15$ mA, and the ambient temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

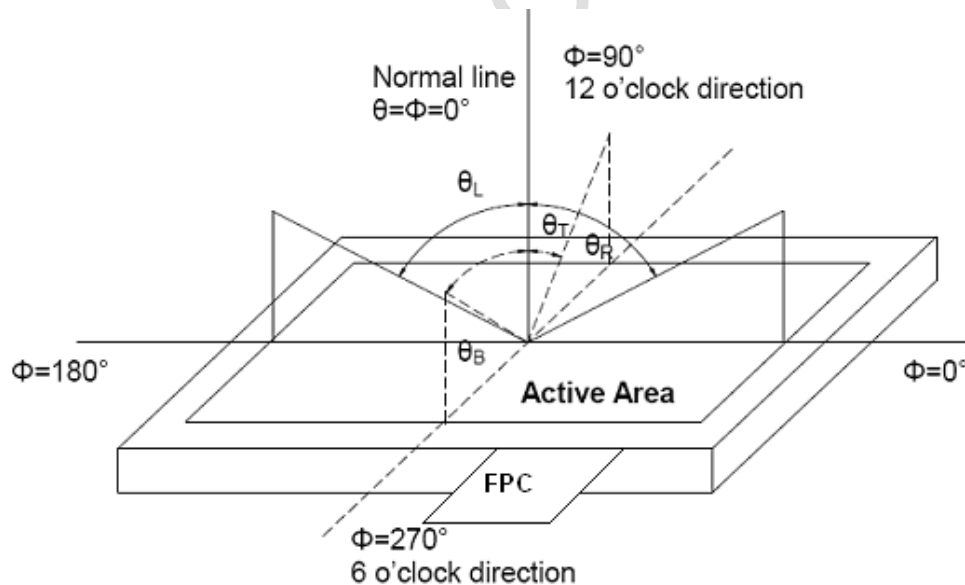
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD.



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

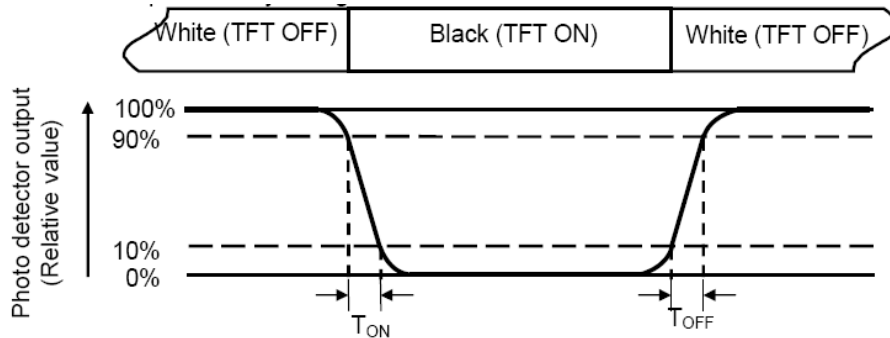
"White state ": The state is that the LCD should drive by V_{white} .

"Black state": The state is that the LCD should drive by V_{black} .

V_{white} : To be determined V_{black} : To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

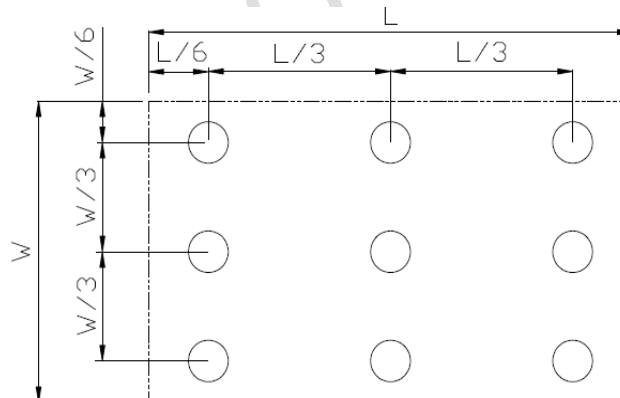
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width



$L_{\max}$: The measured Maximum luminance of all measurement position.

$L_{\min}$: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	70℃, 240 Hours	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	-20℃, 240 Hours	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	80℃, 240 Hours	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	-30℃, 240 Hours	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	60℃, 90%RH, 240 Hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-30℃, 30min.<=> 80℃, 30min. 10 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,G B2423.22-2002
7	ESD	Voltage:±8KV R:330 ohm,C:150pF Air discharge,10 times	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency:10~55Hz Amplitude: 1.5mm Sweep Time: 11Mins Test Period: 6 Cycles For Each Direction Of X, Y, Z (Packing Condition)	IEC60068-2-6:1982 GB/T2423.10—1995
10	Package Drop Test	Packed, 100CM free fall 6 sides, 1 corner, 3edges	IEC60068-2-32:1990 GB/T2423.8—1995

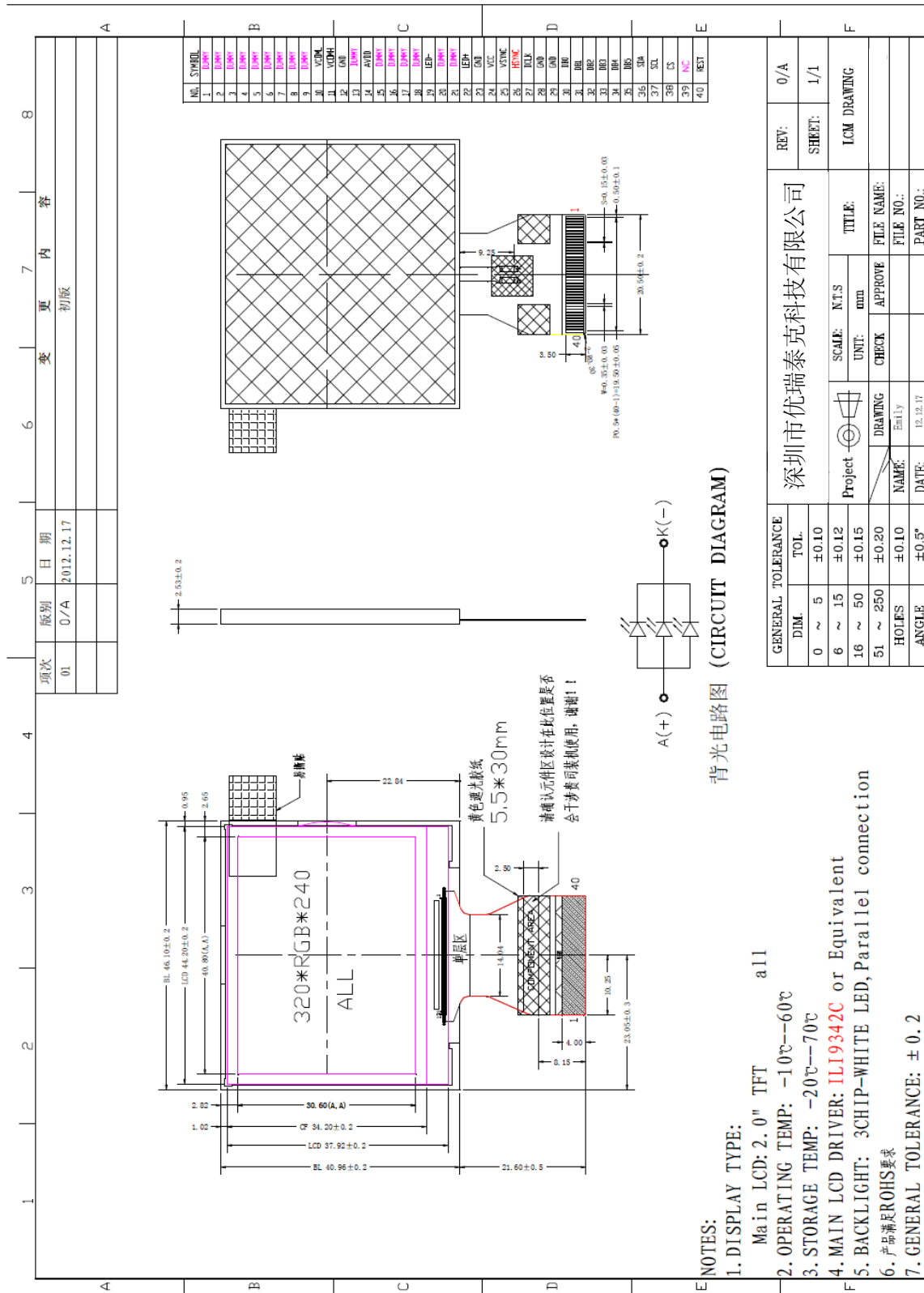
Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

8.Outline Dimension



9 Packing Drawing

TBD

10 Precautions for Use of LCD Modules

a) Handling Precautions

- i. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- ii. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- iii. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- iv. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- v. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
 - Ketone
 - Aromatic solvents
- vi. Do not attempt to disassemble the LCD Module.
 - vii. If the logic circuit power is off, do not apply the input signals.
 - viii. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

b) Storage precautions

- i. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

- ii. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

- iii. The LCD modules should be stored in the room without acid, alkali and harmful gas.

c) Transportation Precautions

- i. The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.