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深圳市优瑞泰克科技有限公司
SHENZHEN YOURITECH TECHNOLOGY CO.,LTD

LCD MODULE SPECIFICATION

Customer: _____

Module No.: ET035WV30-G

Date: 2022-10-11

Version: 1.0

For Customer's Acceptance:

Approved by	Comment

Approved by	Checked by	Prepared by
Kanglin.Zhong		

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Record of Revision

Rev.	Date	Description	Editor
1.0	2022-10-11	First release	Zaiping.Yang

1 General Specifications

No.	Item	Specification	Remark
1	LCD Size	3.5 inch (Diagonal)	
2	Driver Element	a-Si TFT active matrix	
3	Resolution	340 (RGB) × 800	
4	Display Mode	Normally Black, Transmissive	
5	Pixel Pitch	0.0333 (H) × 0.10425 (V)	
6	Display Colors	262K	
7	Surface Treatment	/	
8	Color Arrangement	RGB-Stripe	
9	Interface	3W SPI+RGB18bit	
10	Viewing Direction	All	
11	Gray Scale Inversion Direction	/	Note 1
12	Outline Dimension (mm)	37.27(W) × 89.0 (H) × 1.52(T)	
13	Active Area (mm)	33.97 (W) × 81.0 (H)	
14	Touch Screen	Without CTP	
15	Display Driver IC	ST7701	
16	Touch Driver IC	/	

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180° shift.

Note 2: RoHS compliant.

2 Pin Assignment

2.1 LCD Pin assignment

Match connector: XF2M-4015-1A (OMRON) or equivalent.

PIN	Symbol	I/O	Description	Remark
1	LED A	P	LED ANODE	
2	LED K1	P	LED1 CATHODE	
3	LED K2	P	LED2 CATHODE	
4	GND	P	Ground	
5	VCI	P	Power supply	
6	RESET	I	Reset Signal ,Active Low	
7	NC	-	NC	
8	NC	-	NC	
9	SDA	I	SPI Data signal	
10	SCK	I	SPI Clock signal	
11	CS	I	SPI Chip select signal	
12	PCLK	I	RGB dot clock signal	
13	DE	I	RGB data enable signal	
14	VSYNC	I	RGB frame synchronizing signal	
15	HSYNC	I	RGB line synchronizing signal	
16~33	DB0~DB17	I	RGB data signal: DB0:BLUE LSB--DB5:BLUE MSB; DB6:GREEN LSB--DB11:GREEN,MSB; DB12:RED LSB--DB17:RED MSB.	
34	GND	P	Ground	
35	TP_INT(NC)	-	Touch Interrupt,Not connect.	
36	TP_SDA(NC)	-	Touch IIC Data signal,Not connect.	
37	TP_SCL(NC)	-	Touch IIC Clock signal,Not connect.	
38	TP_RESET(NC)	-	Touch Reset Signal,Not connect.	
39	TP_VCI(NC)	-	Touch Power supply,Not connect.	
40	GND	P	Ground	

I---Input, O---Output, P--- Power/Ground

3 Absolute Maximum Ratings

Ta = 25℃

Item	Symbol	Min.	Max.	Unit	Remark
Power Voltage	V _{CI}	-0.30	+4.6	V	
Operating Temperature	T _{op}	-20.0	70.0	℃	
Storage Temperature	T _{st}	-30.0	80.0	℃	
Operating and Storage Humidity	H _{stg}	10%	90%	%(RH)	

4. Electrical Characteristics

4.1 Recommended Operating Condition

VCI=3.3V, GND=0V, Ta = 25℃

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply Voltage	VCI	2.7	3.0	3.3	V	
Input Signal Voltage	Low Level	V _{IL}	0	-	0.3 x VCI	V
	High Level	V _{IH}	0.7 x VCI	-	VCI	V
Power supply Voltage	I _{VCI}	-	25	50	mA	VCI=3.3V, color bar pattern

4.2 Backlight Unit Driving Condition

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Forward Current	I _F	-	20	25	mA	6 LEDs (6 LED Serial, 1LED Parallel)
Forward Current Voltage	V _F	-	19.2	20.4	V	
Backlight Power Consumption	W _{BL}	-	384	510	mW	
Operating Life Time	--	30000	--	--	hrs	Note 2, Note 3

Note1: The LED driving condition is defined for each module (6 LED Serial, 1 LED Parallel).

Note2: When LCM is operated, the stable forward current should be inputted. And forward voltage is for reference only.

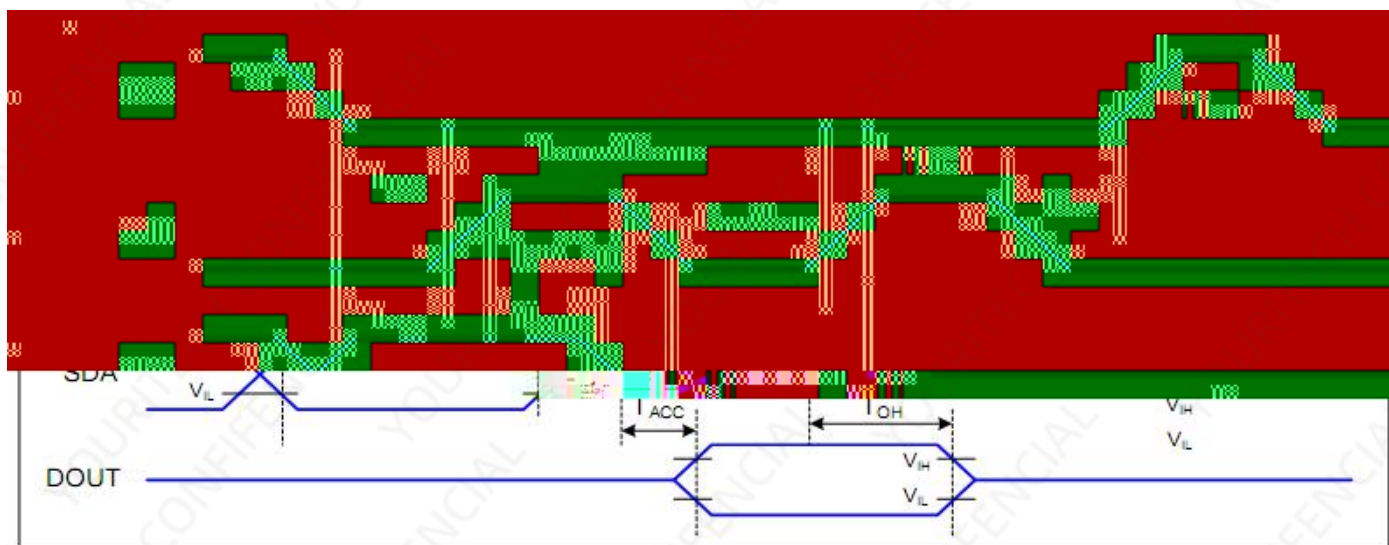
Note3: Optical performance should be evaluated at Ta=25℃ When LED is driven at high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

Note4: The LED driving condition is defined for each LED module.



5 Timing Chart

5.1 Serial Interface Characteristics (3-line serial):



3-line serial Interface Timing Characteristics

VDD=2.8, AGND=DGND=0V, Ta=25°C

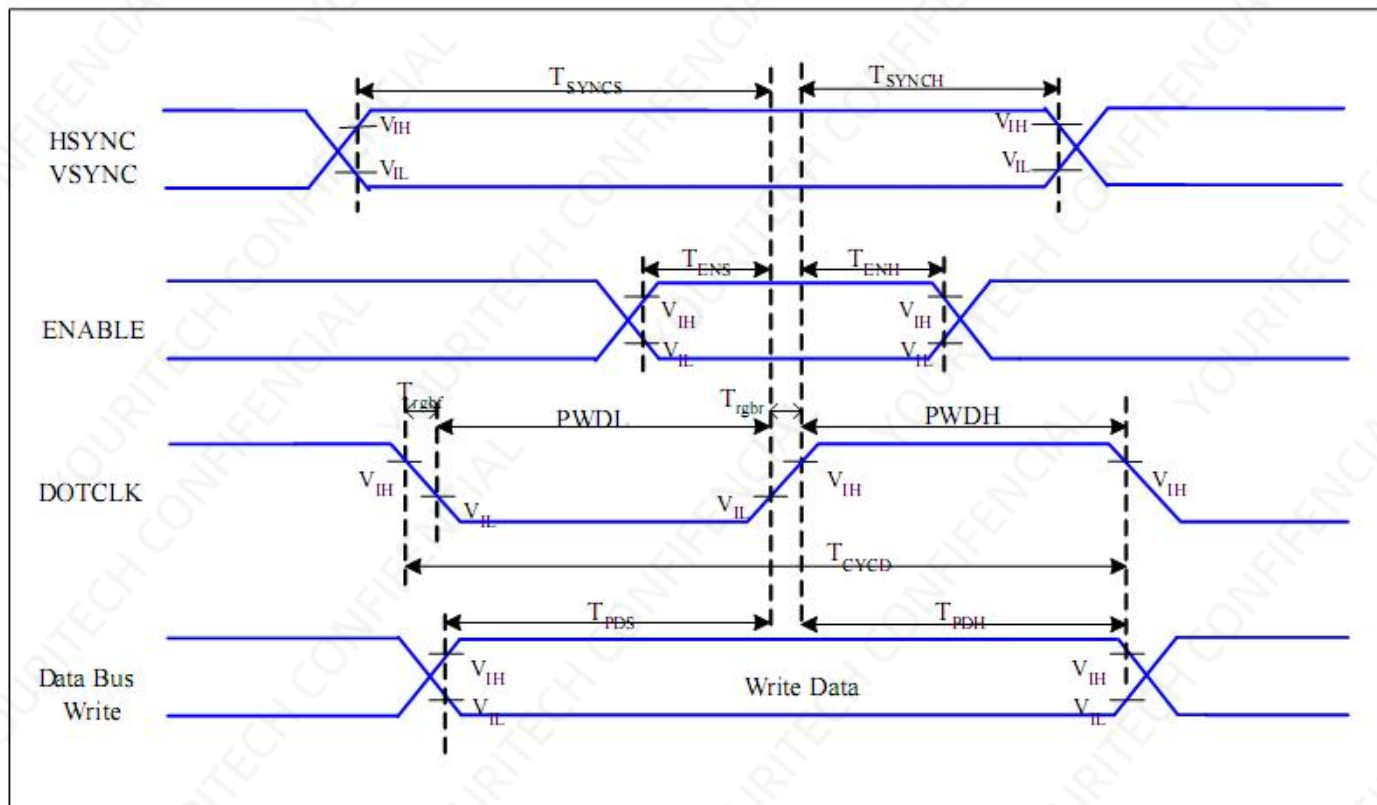
Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T _{CSS}	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	60		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	
	T _{SHW}	SCL "H" pulse width (Write)	15		ns	
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T _{SDS}	Data setup time	10		ns	
	T _{SDH}	Data hold time	10		ns	

3-line serial Interface Characteristics

Note : The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.

5.2 RGB Interface Characteristics :

5.2.1 TFT-LCD Input Clock and Data timing Diagram:

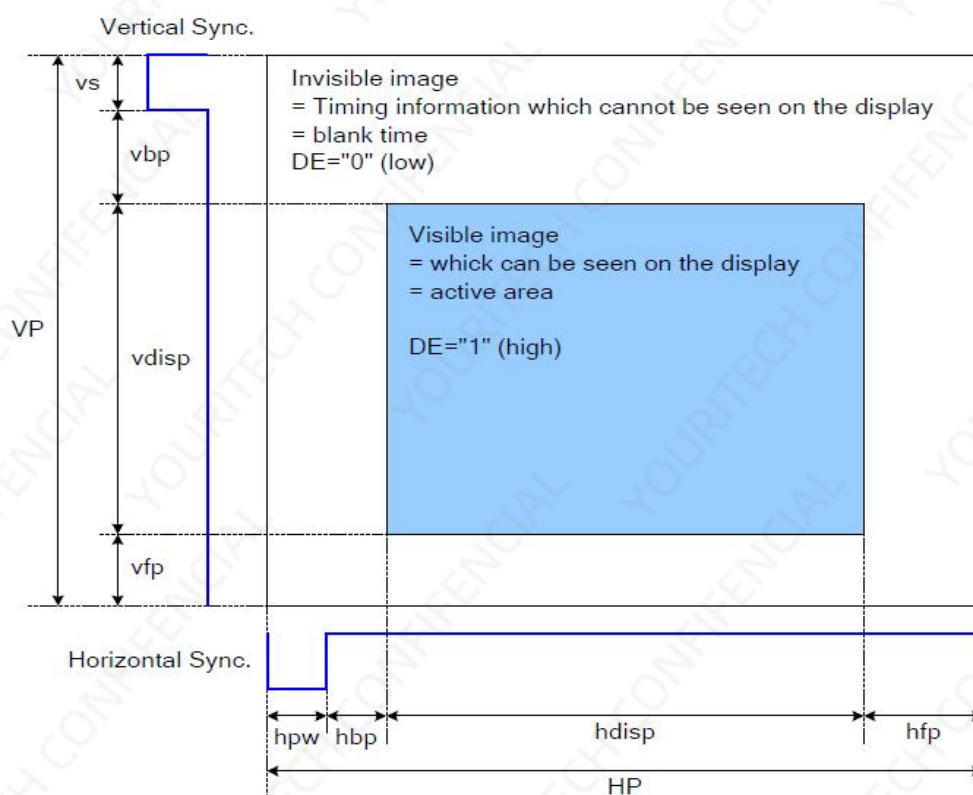


$VDD=2.8, AGND=DGND=0V, Ta=25^{\circ}C$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSNC	T_{SYNCs}	VSNC, HSYNC Setup Time	5	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	5	-	ns	
	T_{ENH}	Enable Hold Time	5	-	ns	
DOTCLK	$PWDH$	DOTCLK High-level Pulse Width	15	-	ns	
	$PWDL$	DOTCLK Low-level Pulse Width	15	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	33	-	ns	
	T_{rghr}, T_{rghf}	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	5	-	ns	
	T_{PDH}	PD Data Hold Time	5	-	ns	

18Bits RGB Interface Timing Characteristics

5.2.2 Recommended Timing Setting of TCON



TCON (Embedded in Source IC) Input Timing (DCLK, HS, VS, DE)

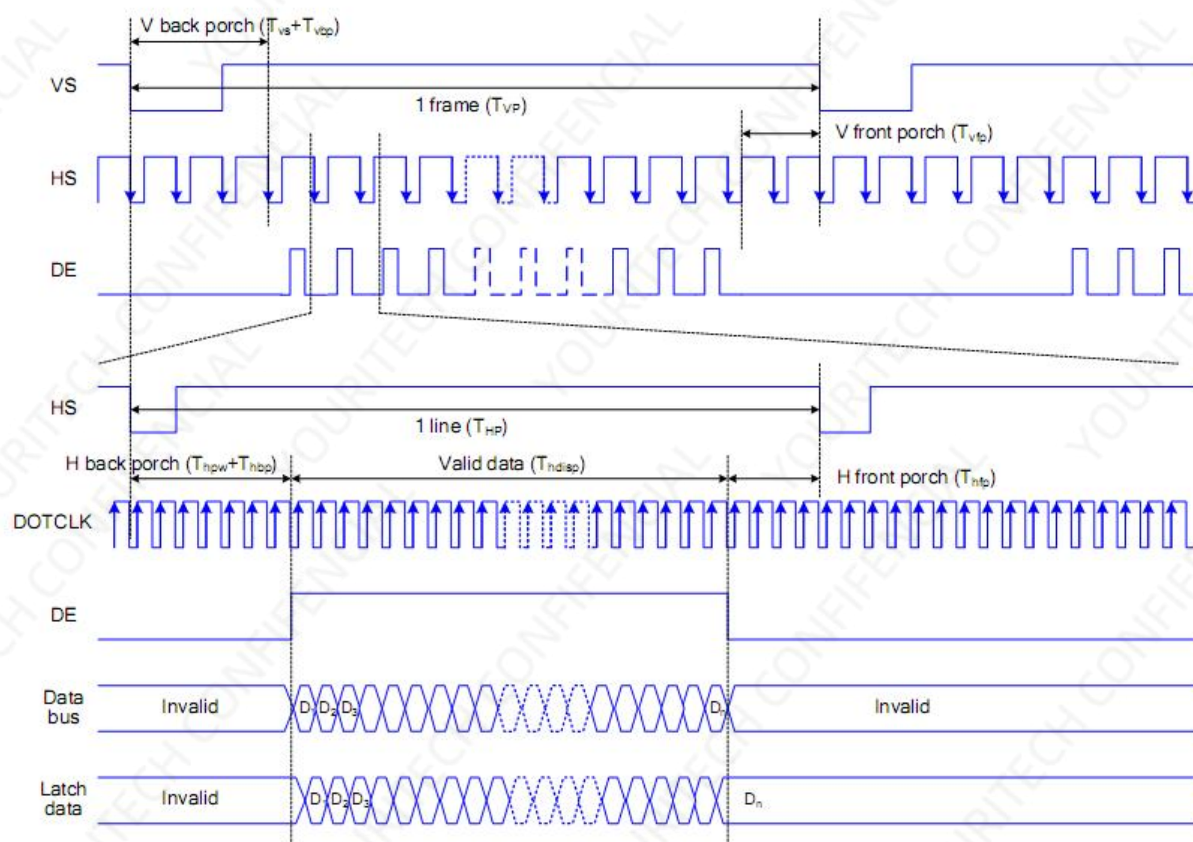
VCC=2.8V, GND=0V, Ta=25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK	Fclk	--	28	--	MHz	
	tclk	--	35.7	--	ns	
HSD	hdisp	--	480	--	tclk	
	hpw	--	10	--	tclk	
	hbp	--	30	--	tclk	
	hfp	--	30	--	tclk	
VSD	vdisp	--	800	--	th	
	vs	--	6	--	th	
	vbp	--	20	--	th	
	vfp	--	20	--	th	

Note: For reference only, it needs to be adjusted according to the actual display effect

TCON Vertical Input Timing Diagram HV

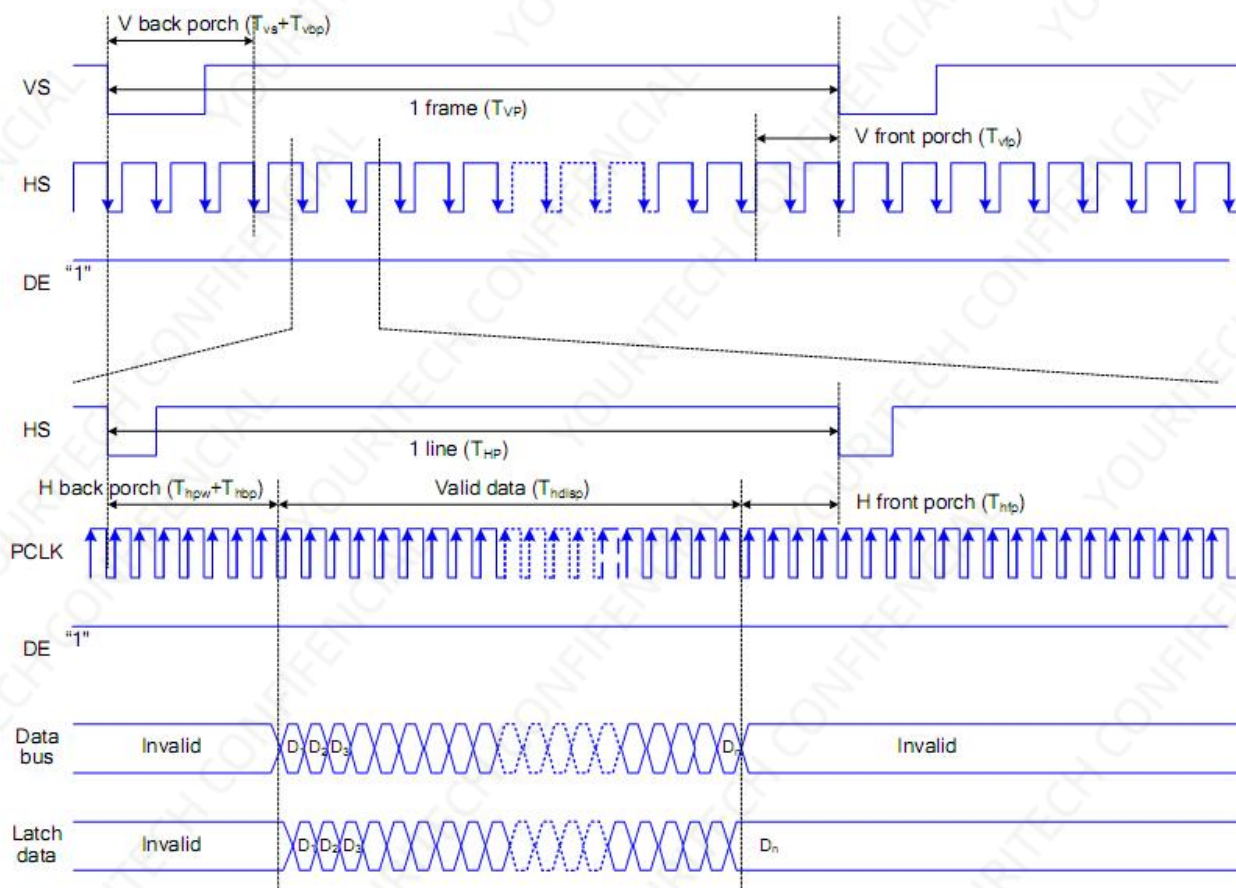
The timing chart of RGB interface DE mode is shown as follows.



Note: The setting of front porch and back porch in host must match that in IC as this mode.

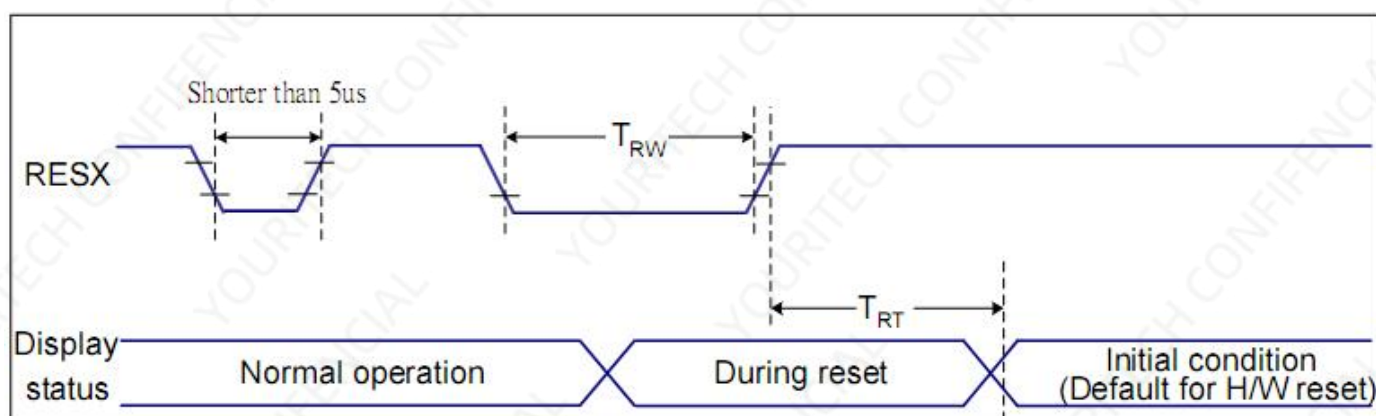
Timing Chart of Signals in RGB Interface DE Mode

The timing chart of RGB interface HV mode is shown as follows.



Timing chart of RGB interface HV mod

5.3 Reset input timing



Reset Timing

VDD=2.8, AGND=DGND=0V, Ta=25 °C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120(Note 1, 6, 7)	ms

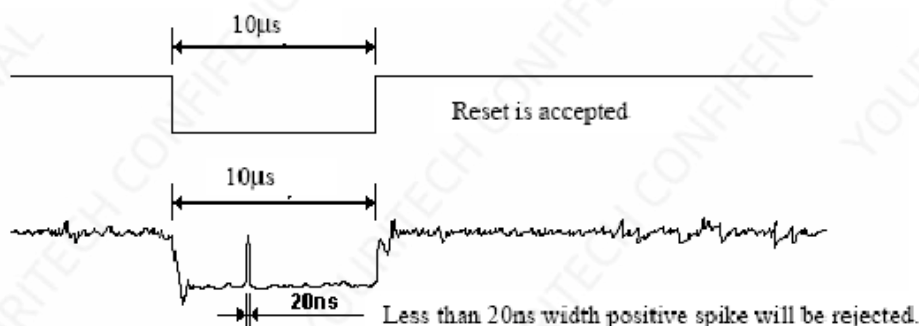
Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

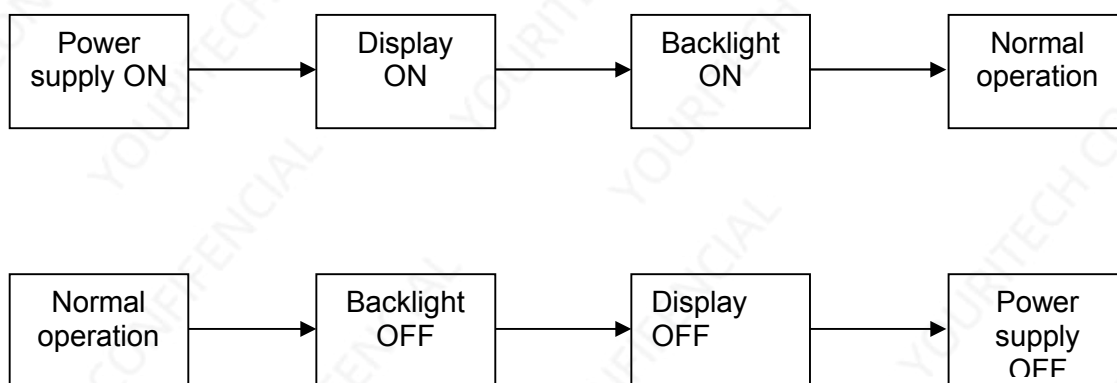
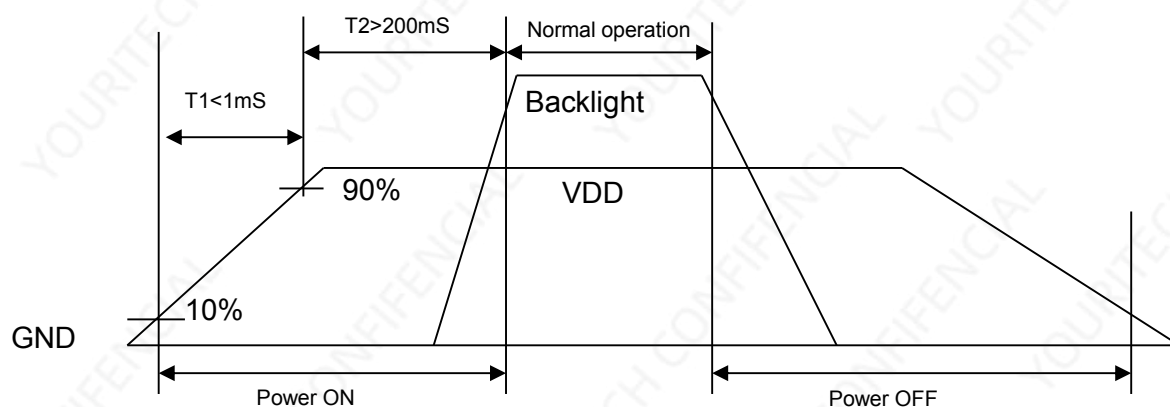
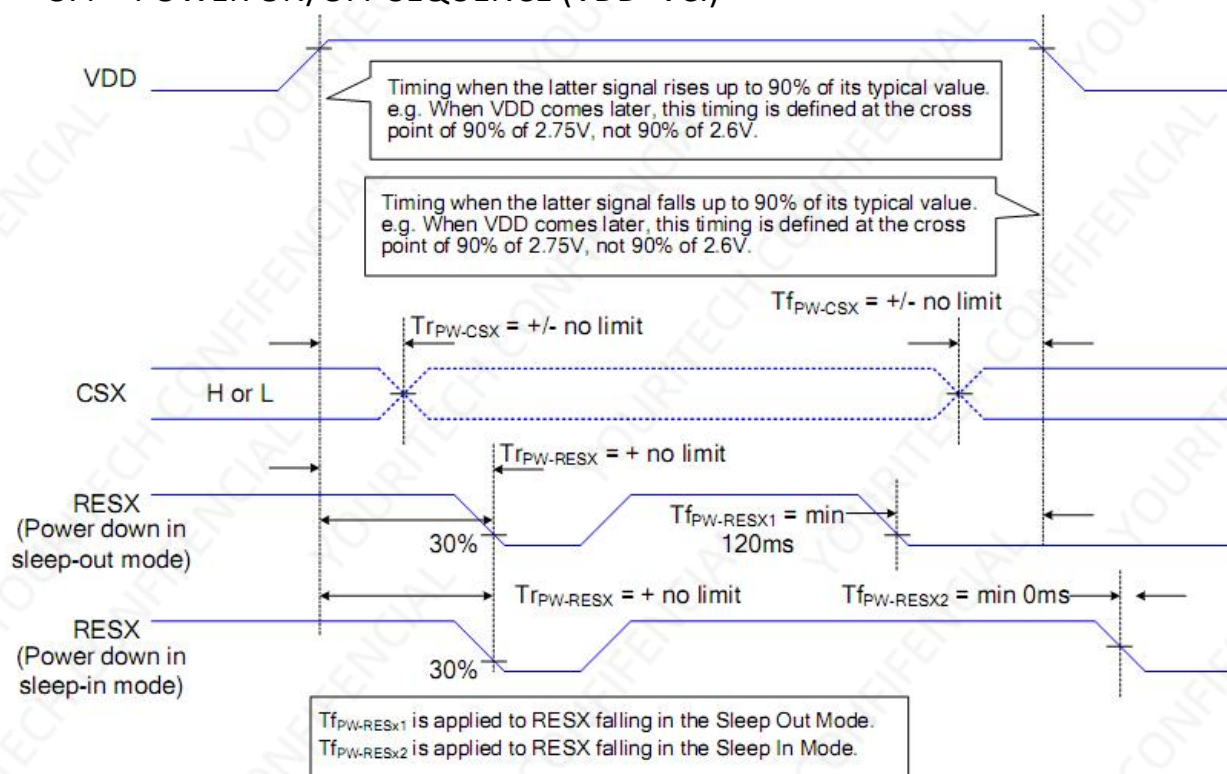
3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

5.4 POWER ON/OFF SEQUENCE (VDD=VCI)



6 Optical Characteristics

Ta=25℃

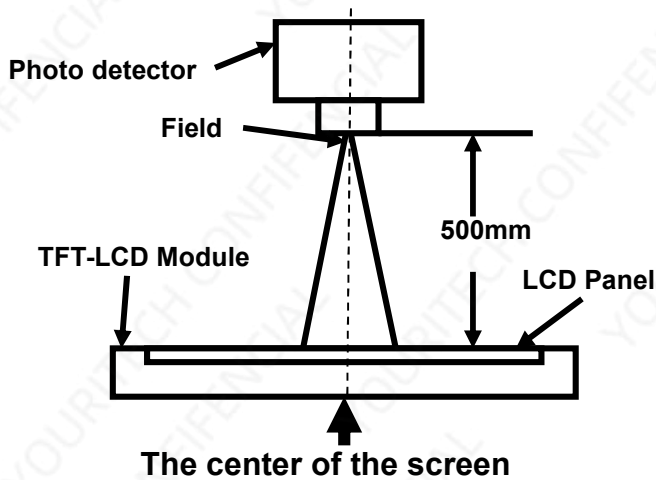
Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
View Angles	θT	$CR \geq 10$	80	85	-	Degree	Note 2
	θB		80	85	-		
	θL		80	85	-		
	θR		80	85	-		
Contrast Ratio	CR	$\theta=0^\circ$	700	900	-		Note1 Note3
Response Time	T_{ON}	25℃	--	25	35	ms	Note1 Note4
	T_{OFF}						
Chromaticity	White	x	Backlight is on	0.283	0.298	0.313	Note1 Note5
		y		0.322	0.337	0.352	
	Red	x		0.639	0.654	0.669	
		y		0.303	0.318	0.333	
	Green	x		0.265	0.280	0.295	
		y		0.541	0.556	0.571	
	Blue	x		0.132	0.147	0.162	
		y		0.116	0.131	0.146	
Uniformity	U		75	80	-	%	Note1 Note6
NTSC			55	60	-	%	Note 5
Luminance	L		330	360	-	cd/m ²	Note1 Note7

Test Conditions:

1. $I_F = 20 \text{ mA}$, $V_F = 19.2 \text{ V}$ and the ambient temperature is $25 \pm 2^\circ \text{C}$. humidity is $65 \pm 7\%$
2. The test systems refer to Note 1 and Note 2.

Note 1: Definition of optical measurement system.

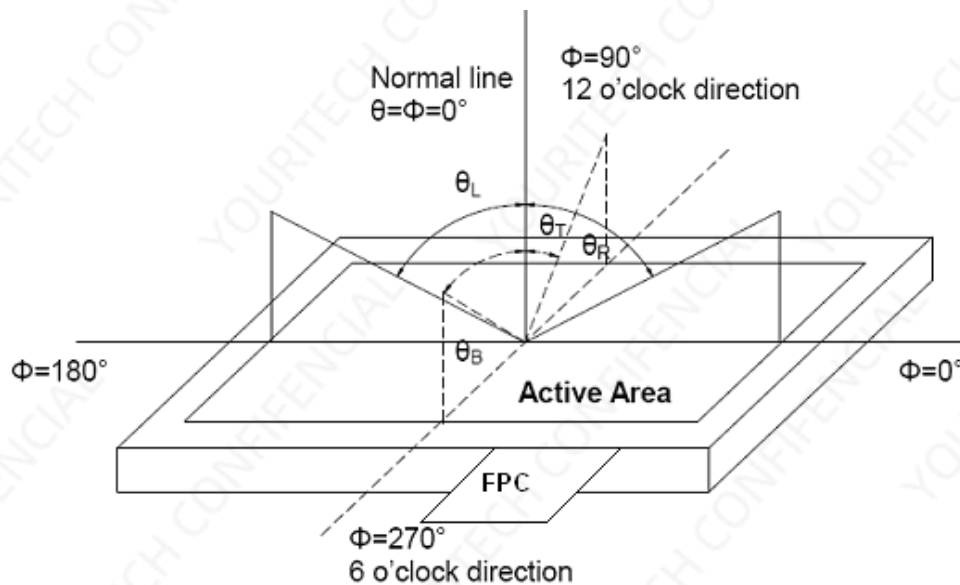
Properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

Viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80)。



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

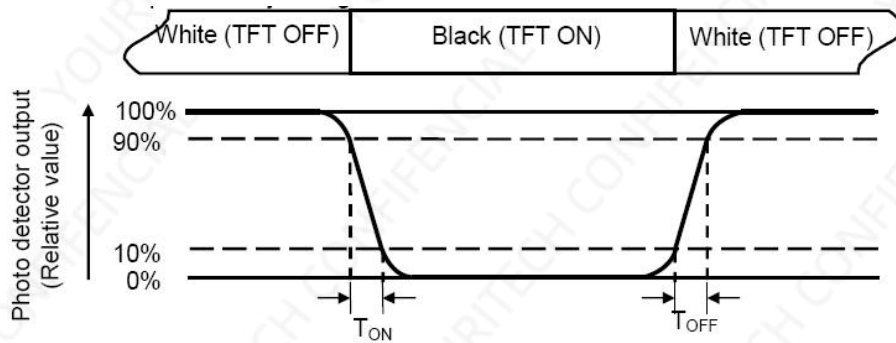
“White state”: The state is that the LCD should drive by V_{white}.

“Black state”: The state is that the LCD should drive by V_{black}.

V_{white}: To be determined V_{black}: To be determined.

Note 4: Definition of response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

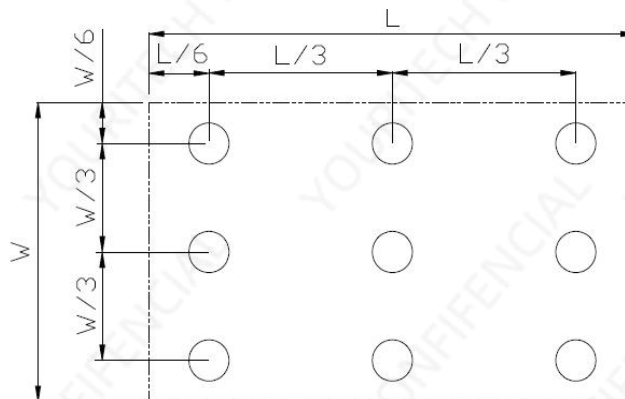
Color coordinates measured at center point of LCD.

Note 6: Definition of luminance uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width



$L_{\max}$: The measured Maximum luminance of all measurement position.

$L_{\min}$: The measured Minimum luminance of all measurement position.

Note 7: Definition of luminance:

Measure the luminance of white state at center point.

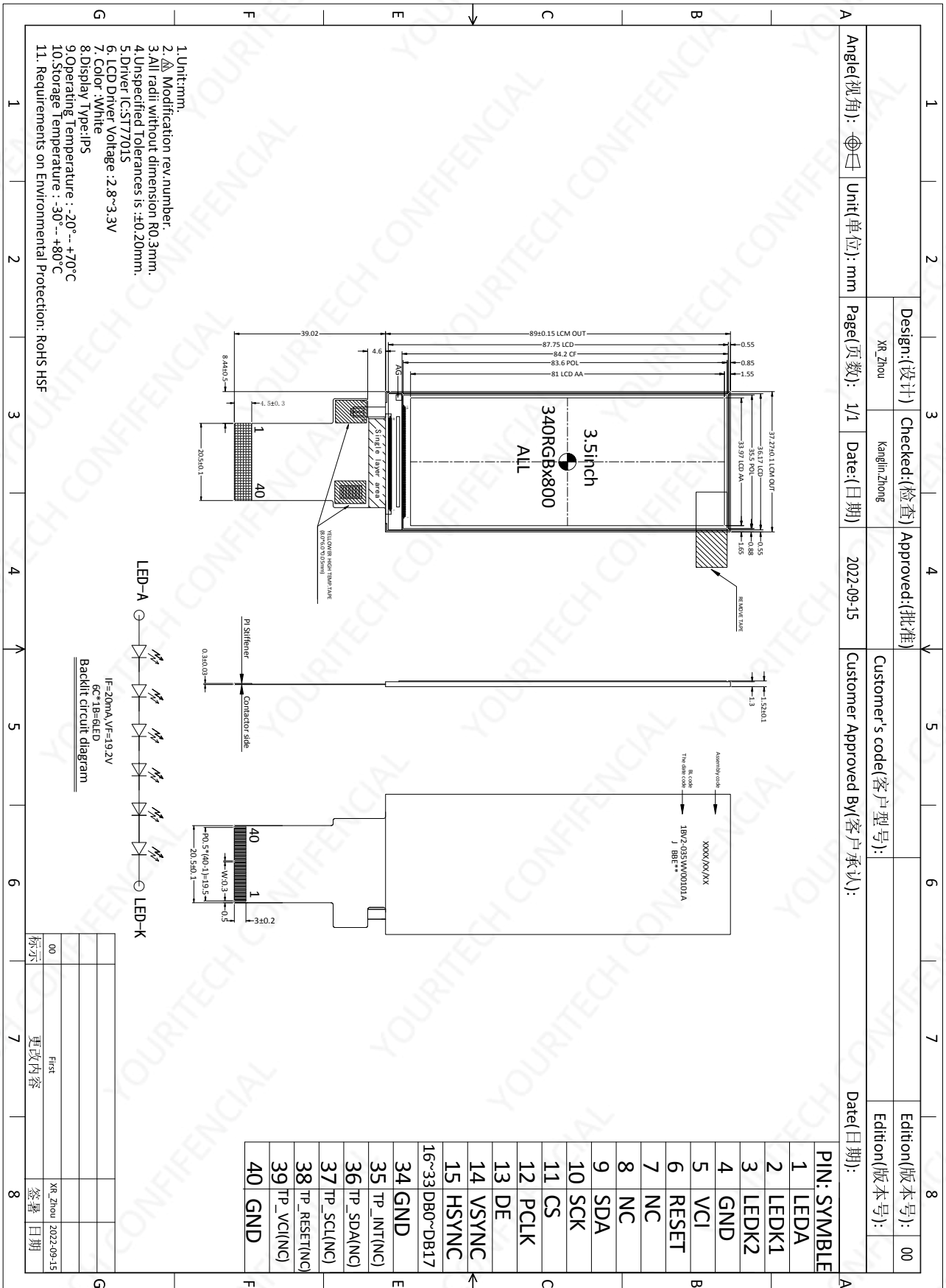
7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts = +70℃, 240 hours	No abnormalities in functions
2	Low Temperature Operation	Ta = -20℃, 240 hours	No abnormalities in functions
3	High Temperature Storage	Ta = +80℃, 240 hours	No abnormalities in functions
4	Low Temperature Storage	Ta = -30℃, 240 hours	No abnormalities in functions
5	Storage at High Temperature and Humidity	Ta = +60℃, 90% RH max, 240 hours	No abnormalities in functions
6	Thermal Shock (non-operating)	-30℃ 30 min ~ +70℃ 30 min, Change time: 0.5 hour @ 5 min @ 0.5 hour. 10 Cycle	Start with cold temperature, End with high temperature,
7	ESD	C=150pF, R=330Ω, 5point/panel Air: ±8Kv, 5times; Contact: ±4Kv, 5times (Environment: 15℃~35℃, 30%~60% RH, 86Kpa~106Kpa)	No abnormalities in functions

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of samples.

8 Mechanical Drawing



9 Precautions for Use of LCD Modules

Handling Precautions

9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

9.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

9.1.8.1 Be sure to ground the body when handling the LCD Modules.

9.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

9.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

9.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

Storage Precautions

9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is: Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

Transportation Precautions

9.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.