

# INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

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EMC tests

on-site service

Optical Bonding

In-house Manufacturing

Cover glass

System solutions

OEM Solutions

Custom Display

Production Custom designs Installation

Mechanical design



## 规格书

### Product Specification

|                                  |                                                                                                               |
|----------------------------------|---------------------------------------------------------------------------------------------------------------|
| 客户名称<br>Customer                 |                                                                                                               |
| 客户项目号<br>Part NO                 |                                                                                                               |
| 产品型号<br>Part NO                  | ET039BA02-H                                                                                                   |
| 产品内容<br>Product type             | Mode:Transmissive type .Normally black.<br>TFTLCD Module<br>LCD Module: 3.92"480RGB*128Dot 8bit/SPI/DSPI/QSPI |
| 客户确认签章<br>Signature by Customer: |                                                                                                               |

| PREPARED BY | CHECKED BY | APPROVED BY |
|-------------|------------|-------------|
|             |            |             |
|             |            |             |



## Records of Revision 修改记录

[illegible]

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## 1 General Description 规格简介

This display module is a transmissive type color active matrix TFT(Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This module is composed of a TFT LCD module, a driver circuit, and a back-light unit. The resolution of a 3.92" contains 480RGB x 128 dots and can display up to 16.7M colors.

该显示模块是一种采用非晶硅 TFT 作为开关器件的透射型彩色有源矩阵 TFT(薄膜晶体管)液晶显示器。该模块由 TFT 液晶显示模块、驱动电路和背光单元组成。3.92 英寸的分辨率包含 480RGB x 128 点，可显示高达 16.7M 的颜色。

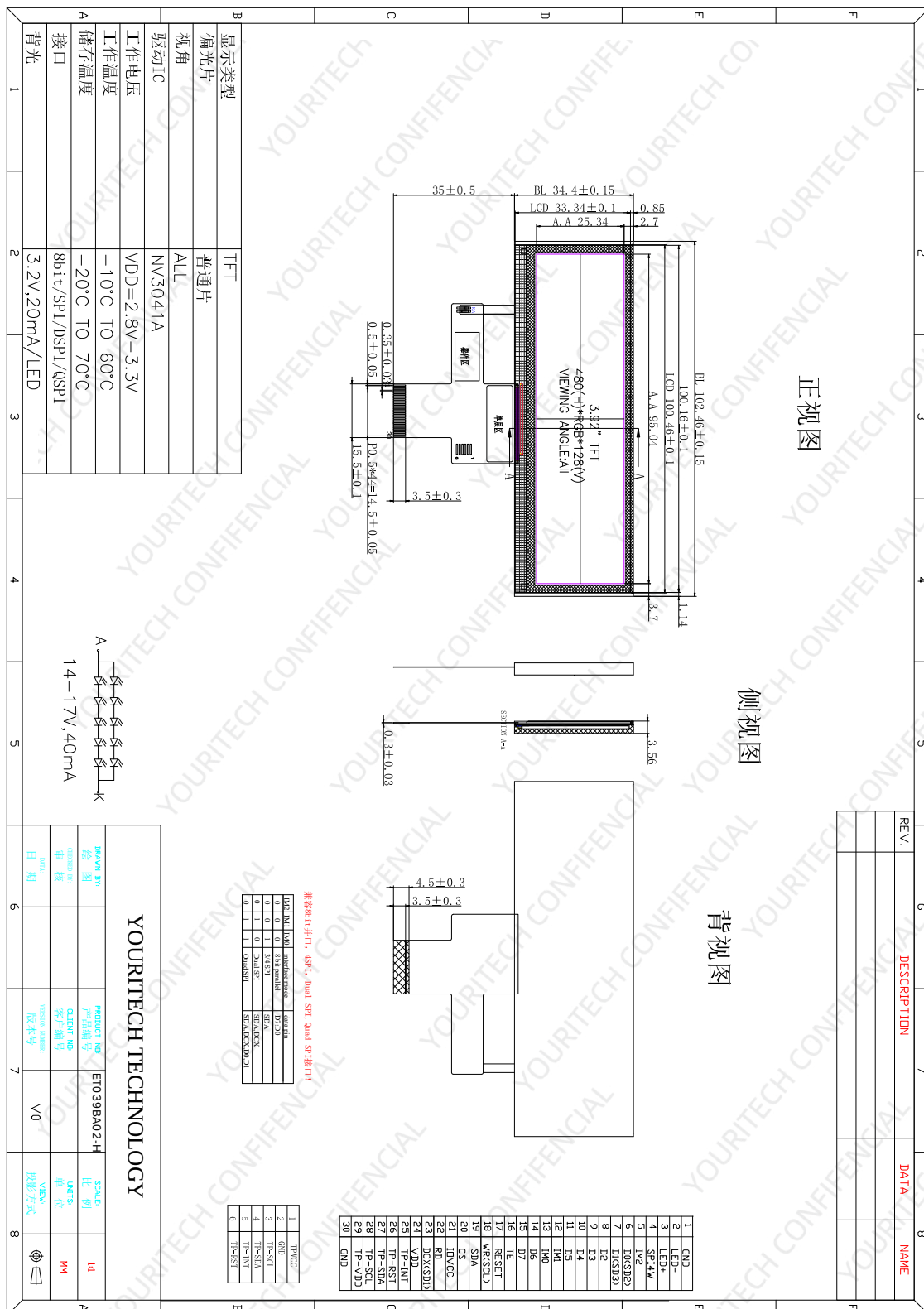
## 2 Module Parameter 模组参数

| Features                          | Details                                     | Unit              |
|-----------------------------------|---------------------------------------------|-------------------|
| Display Size(Diagonal) 显示尺寸(对角线)  | 3.92                                        | inch              |
| LCD type 液晶显示屏类型                  | $\alpha$ -Si TFT                            | -                 |
| Display Mode 显示模式                 | IPS / Transmissive / Normally Black         | -                 |
| Resolution 分辨率                    | 480RGB x 128                                | -                 |
| Active Area 显示区                   | 95.04(H) $\times$ 25.34(V)                  | mm                |
| Module Outline 模组外形               | 102.46(H) $\times$ 34.4(V) $\times$ 3.56(T) | mm                |
| Display Colors 显示颜色               | 16.7M                                       | -                 |
| Interface 接口                      | 8bit/SPI/DSPI/QSPI                          | -                 |
| Driver IC 驱动 IC                   | NV3041A                                     | -                 |
| TP Viewing Area TP 视窗             | /                                           | mm                |
| TP Outline(assembly) TP 外形        | /                                           | mm                |
| Luminance on surface 亮度           | 400                                         | cd/m <sup>2</sup> |
| View Direction 视角方向               | All                                         | Best image        |
| Contrast ratio 对比度                | 1200:1                                      |                   |
| Color gamut 色域                    | 50%                                         |                   |
| PPI 图像点密集度                        | 128                                         | -                 |
| Window effect 视窗效果                | 无一体黑                                        | -                 |
| Cover plate surface effect 盖板表面效果 | 无 AF/AG                                     | -                 |
| Operating Temperature 工作温度        | -20 $\sim$ 70                               | °C                |
| Storage Temperature 储存温度          | -30 $\sim$ 80                               | °C                |
| Weight 重量                         | TBD                                         | g                 |

Note 1: Excluding hooks, posts , FPC/FPC tail etc.



### 3 Mechanical Drawings.



## 4 Module Interface 模组接口定义

| NO  | SYMBOL   | FUNCTION                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
|-----|----------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|---------------|-----|----------------|----------|---|---|---|----------------|-------|---|---|---|---------|-----|---|---|---|----------|---------|---|---|---|----------|---------------|
| 1   | GND      | Power Ground                                                                                                                                                                                                                                                                                                                                                                                                                                  |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 2   | LED-     | LED Cathode                                                                                                                                                                                                                                                                                                                                                                                                                                   |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 3   | LED+     | LED Anode                                                                                                                                                                                                                                                                                                                                                                                                                                     |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 4   | SPI4W    | Std SPI 3/4 wire selection.<br>SPI4W=“H”, 4 wire SPI;<br>SPI4W=“L”, 3wire SPI.                                                                                                                                                                                                                                                                                                                                                                |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 5   | IM2      | 兼容8bit并口，4SPI，Dual SPI, Quad SPI接口！ <table><tr><td>IM2</td><td>IM1</td><td>IM0</td><td>interface mode</td><td>data pin</td></tr><tr><td>0</td><td>0</td><td>0</td><td>8 bit parallel</td><td>D7:D0</td></tr><tr><td>0</td><td>0</td><td>1</td><td>3/4 SPI</td><td>SDA</td></tr><tr><td>0</td><td>1</td><td>0</td><td>Dual SPI</td><td>SDA,DCX</td></tr><tr><td>0</td><td>1</td><td>1</td><td>Quad SPI</td><td>SDA,DCX,D0,D1</td></tr></table> | IM2            | IM1           | IM0 | interface mode | data pin | 0 | 0 | 0 | 8 bit parallel | D7:D0 | 0 | 0 | 1 | 3/4 SPI | SDA | 0 | 1 | 0 | Dual SPI | SDA,DCX | 0 | 1 | 1 | Quad SPI | SDA,DCX,D0,D1 |
| IM2 | IM1      | IM0                                                                                                                                                                                                                                                                                                                                                                                                                                           | interface mode | data pin      |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 0        | 0                                                                                                                                                                                                                                                                                                                                                                                                                                             | 8 bit parallel | D7:D0         |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 0        | 1                                                                                                                                                                                                                                                                                                                                                                                                                                             | 3/4 SPI        | SDA           |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 1        | 0                                                                                                                                                                                                                                                                                                                                                                                                                                             | Dual SPI       | SDA,DCX       |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 1        | 1                                                                                                                                                                                                                                                                                                                                                                                                                                             | Quad SPI       | SDA,DCX,D0,D1 |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 6   | D0(SD2)  | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 7   | D1(SD3)  | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 8   | D2       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 9   | D3       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 10  | D4       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 11  | D5       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 12  | IM1      | 兼容8bit并口，4SPI，Dual SPI, Quad SPI接口！                                                                                                                                                                                                                                                                                                                                                                                                           |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 13  | IM0      | <table><tr><td>IM2</td><td>IM1</td><td>IM0</td><td>interface mode</td><td>data pin</td></tr><tr><td>0</td><td>0</td><td>0</td><td>8 bit parallel</td><td>D7:D0</td></tr><tr><td>0</td><td>0</td><td>1</td><td>3/4 SPI</td><td>SDA</td></tr><tr><td>0</td><td>1</td><td>0</td><td>Dual SPI</td><td>SDA,DCX</td></tr><tr><td>0</td><td>1</td><td>1</td><td>Quad SPI</td><td>SDA,DCX,D0,D1</td></tr></table>                                     | IM2            | IM1           | IM0 | interface mode | data pin | 0 | 0 | 0 | 8 bit parallel | D7:D0 | 0 | 0 | 1 | 3/4 SPI | SDA | 0 | 1 | 0 | Dual SPI | SDA,DCX | 0 | 1 | 1 | Quad SPI | SDA,DCX,D0,D1 |
| IM2 | IM1      | IM0                                                                                                                                                                                                                                                                                                                                                                                                                                           | interface mode | data pin      |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 0        | 0                                                                                                                                                                                                                                                                                                                                                                                                                                             | 8 bit parallel | D7:D0         |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 0        | 1                                                                                                                                                                                                                                                                                                                                                                                                                                             | 3/4 SPI        | SDA           |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 1        | 0                                                                                                                                                                                                                                                                                                                                                                                                                                             | Dual SPI       | SDA,DCX       |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 0   | 1        | 1                                                                                                                                                                                                                                                                                                                                                                                                                                             | Quad SPI       | SDA,DCX,D0,D1 |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 14  | D6       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 15  | D7       | Data input                                                                                                                                                                                                                                                                                                                                                                                                                                    |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 16  | TE       | Tearing effect output pin is used to synchronize MCU frame writing, activated by S/W command. When this pin is not activated (TE function OFF), this pin is GND level.                                                                                                                                                                                                                                                                        |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 17  | RESET    | This signal will reset the device and it must be applied to properly initialize the chip.Signal is active low                                                                                                                                                                                                                                                                                                                                 |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 18  | WR(SCL)  | Clock input, internal pull high.                                                                                                                                                                                                                                                                                                                                                                                                              |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 19  | SDA      | Serial communication data input and output, internal pull low.                                                                                                                                                                                                                                                                                                                                                                                |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 20  | CS       | Chip select, internal pull high.                                                                                                                                                                                                                                                                                                                                                                                                              |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 21  | IOVCC    | Low voltage power supply for interface logic circuits(1.65~3.3V)                                                                                                                                                                                                                                                                                                                                                                              |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 22  | RD       | MCU interface read enable input, internal pull high.                                                                                                                                                                                                                                                                                                                                                                                          |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 23  | DCX(SD1) | Data or Command flag                                                                                                                                                                                                                                                                                                                                                                                                                          |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 24  | VDD      | Power Supply for Analog, VDD=2.4V~3.3V.                                                                                                                                                                                                                                                                                                                                                                                                       |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 25  | TP-INT   | Touch panel interrupt output                                                                                                                                                                                                                                                                                                                                                                                                                  |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |
| 26  | TP-RST   | Touch panel reset                                                                                                                                                                                                                                                                                                                                                                                                                             |                |               |     |                |          |   |   |   |                |       |   |   |   |         |     |   |   |   |          |         |   |   |   |          |               |

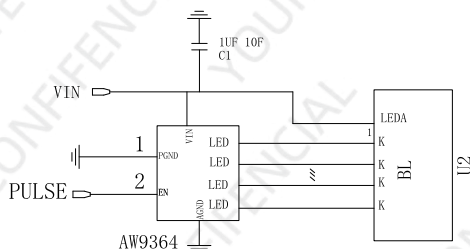


|    |        |                                                                  |
|----|--------|------------------------------------------------------------------|
| 27 | TP-SDA | Touch panel I2C data                                             |
| 28 | TP-SCL | Touch panel I2C clock                                            |
| 29 | TP-VDD | Touch panel Power Supply for Analog.If not used, please open it. |
| 30 | GND    | Power Ground                                                     |

## 5 Application Circuit 应用电路

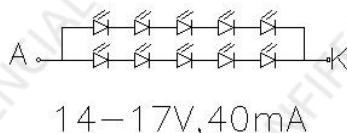
### 5.1 Backlight recommended circuit 背光电路参考

Motherboard driver backlight is need constant current circuit, if the rated voltage screen after light brightness difference.Current and power consumption of the machine are inconsistent, so recommend a backlight driving circuit is best rated current.It is recommended to use IC (AW9364). The reference circuit is as follows:



### 5.2 Backlight recommended circuit 背光电路参数推荐

Motherboard driver backlight is need constant current circuit:



10 灯并联

Note: constant current circuit for every LED, and though LED lamp current is less than 20mA. Recommend between 15mA and 20 mA for every LED.

### 5.3 Application Circuit 应用电路 ( )

## 6 Absolute Maximum Ratings 绝对最大额定值

VSS=0V, Ta=25°C

| Item 项目             | Symbol | Min.最小 | Max.最大 | Unit 单位 |
|---------------------|--------|--------|--------|---------|
| Supply Voltage 电源电压 | VDD    | -0.3   | +4.6   | V       |



|                            |           |       |           |      |   |
|----------------------------|-----------|-------|-----------|------|---|
|                            | Analog 模拟 | -     | -         | -    | V |
|                            | IO        | IOVDD | -0.3      | +4.6 | V |
| Input Voltage 输入电压         | $V_i$     | -0.3  | IOVDD+0.3 | V    |   |
| Storage temperature 储存温度   | $T_{stg}$ | -30   | +70       | °C   |   |
| Operating temperature 工作温度 | $T_{op}$  | -20   | +60       | °C   |   |
| Storage humidity 存储湿度      | $H_{stg}$ | 10    | Note 1    | %RH  |   |
| Operating humidity 操作湿度    | $H_{op}$  | 10    | Note 1    | %RH  |   |

Note 1: 90%RH max, If Ta is below 50°C; 60%RH max, If Ta is over 60°C.

## 7 Electrical Specification 电性规格

DC Characteristics 直流特性

| Item 项目                         |                         | Symbol          | Min.最小    | Typ.中间  | Max.最大   | Unit 单位 |
|---------------------------------|-------------------------|-----------------|-----------|---------|----------|---------|
| Supply Voltage 电源电压             | Powersupply<br>电力供应     | VDD             | 2.4       | 2.8     | 3.3      | V       |
|                                 | Analog                  | VCI             | 2.4       | 2.8     | 3.3      | V       |
|                                 | IO                      | IOVDD           | 1.65      | 1.8/2.8 | 3.3      | V       |
| Logic Low input voltage 输入电压低   |                         | V <sub>IL</sub> | -0.3IOVDD | -       | 0.3IOVDD | V       |
| Logic High input voltage 输入电压高  |                         | V <sub>IH</sub> | 0.7IOVDD  | -       | IOVDD    | V       |
| Logic Low output voltage 输出电压低  |                         | V <sub>OL</sub> | -         | -       | 0.2IOVDD | V       |
| Logic High output voltage 输出电压高 |                         | V <sub>OH</sub> | 0.8IOVDD  | -       | -        | V       |
| Current Consumption 电<br>流消耗    | Normal display<br>正常的显示 | Ivdd            | -         | 30      | -        | mA      |
|                                 | Standby mode<br>待机模式    | Ivdd            | -         | 60      | -        | uA      |
| Frame Frequency 帧频              |                         | f <sub>FR</sub> | -         | 60      | -        | Hz      |

## 8 Initialization Code 初始化代码

```

//-----
void LCD_Init(void)
{
    //CS0=0;
    RST=1;
    Delay(200);
    RST=0;
    Delay(800);
    RST=1;
    Delay(800);
    WriteComm(0xff);WriteData(0xa5);
    WriteComm(0xE7);WriteData(0x10);
    WriteComm(0x35);WriteData(0x00);
    WriteComm(0x36);WriteData(0x00);
    WriteComm(0x55);WriteData(0x

```



01);  
WriteComm(0x3A);WriteData(0x01);  
WriteComm(0x40);WriteData(0x01);  
WriteComm(0x41);WriteData(0x01);  
WriteComm(0x44);WriteData(0x15);  
WriteComm(0x45);WriteData(0x15);  
WriteComm(0x7d);WriteData(0x03);  
WriteComm(0xc1);WriteData(0xbb);  
WriteComm(0xc2);WriteData(0x05);  
WriteComm(0xc3);WriteData(0x10);  
WriteComm(0xc6);WriteData(0x3e);  
WriteComm(0xc7);WriteData(0x25);  
WriteComm(0xc8);WriteData(0x21);  
WriteComm(0x7a);WriteData(0x51);  
WriteComm(0x6f);WriteData(0x49);  
WriteComm(0x78);WriteData(0x57);  
WriteComm(0xc9);WriteData(0x00);  
WriteComm(0x67);WriteData(0x11);  
WriteComm(0x51);WriteData(0x0a);  
WriteComm(0x52);WriteData(0x7D);  
WriteComm(0x53);WriteData(0x0a);

WriteComm(0x54);WriteData(0x7D);  
WriteComm(0x46);WriteData(0x0a);  
WriteComm(0x47);WriteData(0x2a);  
WriteComm(0x48);WriteData(0x0a);  
WriteComm(0x49);WriteData(0x1a);  
WriteComm(0x44);WriteData(0x15);  
WriteComm(0x45);WriteData(0x15);  
WriteComm(0x73);WriteData(0x08);  
WriteComm(0x74);WriteData(0x10);  
WriteComm(0x56);WriteData(0x43);  
WriteComm(0x57);WriteData(0x42);  
WriteComm(0x58);WriteData(0x3c);  
WriteComm(0x59);WriteData(0x64);  
WriteComm(0x5a);WriteData(0x41);  
WriteComm(0x5b);WriteData(0x3C);  
WriteComm(0x5c);WriteData(0x02);  
WriteComm(0x5d);WriteData(0x3c);  
WriteComm(0x5e);WriteData(0x1f);  
WriteComm(0x60);WriteData(0x80);  
WriteComm(0x61);WriteData(0x3f);  
WriteComm(0x62);WriteData(0x



21);  
WriteComm(0x63);WriteData(0x  
07);  
WriteComm(0x64);WriteData(0x  
e0);  
WriteComm(0x65);WriteData(0x  
02);  
WriteComm(0x6e);WriteData(0x  
14);  
WriteComm(0xe5);WriteData(0x  
06);  
WriteComm(0xe6);WriteData(0x  
00);  
WriteComm(0xca);WriteData(0x  
20);  
WriteComm(0xcb);WriteData(0x  
52);  
WriteComm(0xcc);WriteData(0x  
10);  
WriteComm(0xcD);WriteData(0  
x42);  
WriteComm(0xD0);WriteData(0  
x20);  
WriteComm(0xD1);WriteData(0  
x52);  
WriteComm(0xD2);WriteData(0  
x10);  
WriteComm(0xD3);WriteData(0  
x42);  
WriteComm(0xD4);WriteData(0  
x0a);  
WriteComm(0xD5);WriteData(0  
x32);  
WriteComm(0x80);WriteData(0x  
00);  
WriteComm(0xA0);WriteData(0  
x00);  
WriteComm(0x81);WriteData(0x  
07);  
WriteComm(0xA1);WriteData(0  
x09);

WriteComm(0x82);WriteData(0x  
05);  
WriteComm(0xA2);WriteData(0  
x04);  
WriteComm(0x86);WriteData(0x  
1a);  
WriteComm(0xA6);WriteData(0  
x19);  
WriteComm(0x87);WriteData(0x  
2a);  
WriteComm(0xA7);WriteData(0  
x24);  
WriteComm(0x83);WriteData(0x  
37);  
WriteComm(0xA3);WriteData(0  
x37);  
WriteComm(0x84);WriteData(0x  
35);  
WriteComm(0xA4);WriteData(0  
x35);  
WriteComm(0x85);WriteData(0x  
3f);  
WriteComm(0xA5);WriteData(0  
x3f);  
WriteComm(0x88);WriteData(0x  
0A);  
WriteComm(0xA8);WriteData(0  
x0A);  
WriteComm(0x89);WriteData(0x  
13);  
WriteComm(0xA9);WriteData(0  
x12);  
WriteComm(0x8a);WriteData(0x  
18);  
WriteComm(0xAa);WriteData(0  
x19);  
WriteComm(0x8b);WriteData(0x  
0a);  
WriteComm(0xAb);WriteData(0  
x0a);  
WriteComm(0x8c);WriteData(0x



```
17);  
WriteComm(0xAc);WriteData(0  
x0B);  
WriteComm(0x8d);WriteData(0x  
1A);  
WriteComm(0xAd);WriteData(0  
x09);  
WriteComm(0x8e);WriteData(0x  
1A);  
WriteComm(0xAe);WriteData(0  
x08);  
WriteComm(0x8f);WriteData(0x  
1F);  
WriteComm(0xAf);WriteData(0x  
00);  
WriteComm(0x90);WriteData(0x  
08);  
WriteComm(0xB0);WriteData(0  
x00);  
WriteComm(0x91);WriteData(0x  
0f);  
WriteComm(0xB1);WriteData(0  
x05);  
WriteComm(0x92);WriteData(0x  
17);  
WriteComm(0xB2);WriteData(0  
x13);  
WriteComm(0xff);WriteData(0x  
00);  
WriteComm(0x11);Delay(120);  
WriteComm(0x29);Delay(20);  
    //WriteComm(0x2c);  
}
```



## 9 Optical Specifications 光学规格

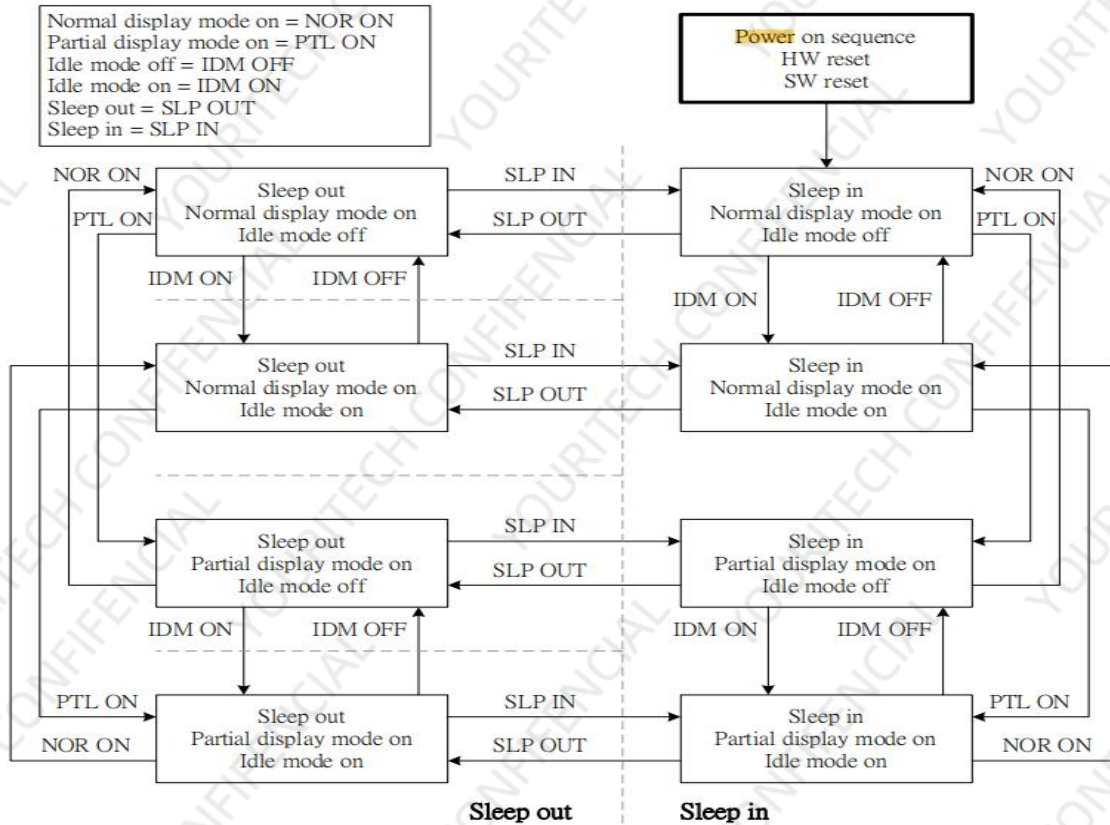
### 9.1 Optical Specifications 光学规格

Ta=25°C, VDD=2.8V, TN LC+ Polarizer

| Backlight On (Transmissive Mode) | Item<br>项目                              |             | Symbol<br>标志 | Condition<br>条件                                       | Specification 规范 |            |            | Unit<br>单位        |   |
|----------------------------------|-----------------------------------------|-------------|--------------|-------------------------------------------------------|------------------|------------|------------|-------------------|---|
|                                  |                                         |             |              |                                                       | Min.<br>最小       | Typ.<br>中间 | Max.<br>最大 |                   |   |
|                                  | Luminance on surface( $I_f$ =20mA) 表面亮度 |             | $L_v$        | Normally viewing angle<br>$\theta_x=\theta_y=0^\circ$ |                  | 400        | -          | cd/m <sup>2</sup> |   |
|                                  | Contrast ratio 对比度                      |             | $CR$         |                                                       | 1000             | 1200       | -          | -                 |   |
|                                  | Response time 响应时间                      |             | $TR$         |                                                       | -                | 10         | 15         | ms                |   |
|                                  |                                         |             | $TF$         | -                                                     | 20               | 20         |            |                   |   |
|                                  | Chromaticity Transmissive 色度            |             | Red 红        | $XR$                                                  | -                | 0.598      | 0.618      | 0.638             | - |
|                                  |                                         |             |              | $YR$                                                  |                  | 0.314      | 0.334      | 0.354             | - |
|                                  |                                         |             | Green 绿      | $XG$                                                  |                  | 0.267      | 0.287      | 0.307             | - |
|                                  |                                         |             |              | $YG$                                                  |                  | 0.527      | 0.547      | 0.567             | - |
| Blue 蓝                           |                                         |             | $XB$         | 0.121                                                 |                  | 0.141      | 0.161      | -                 |   |
|                                  |                                         |             | $YB$         | 0.144                                                 |                  | 0.164      | 0.184      | -                 |   |
| White 白                          |                                         |             | $XW$         | 0.287                                                 |                  | 0.307      | 0.327      | -                 |   |
|                                  |                                         |             | $YW$         | 0.319                                                 |                  | 0.339      | 0.359      | -                 |   |
| Viewing Angle 视角                 |                                         | Horizo ntal | $\theta X+$  | Center<br>$CR\geq 10$                                 | 80               | 85         | -          | Deg.              |   |
|                                  |                                         |             | $\theta X-$  |                                                       | 80               | 85         | -          |                   |   |
|                                  |                                         | Vertical    | $\theta Y+$  |                                                       | 80               | 85         | -          |                   |   |
|                                  |                                         |             | $\theta Y-$  |                                                       | 80               | 85         | -          |                   |   |
| NTSC Ratio(Gamut)                |                                         |             | -            | -                                                     | 45               | 50         | -          | %                 |   |



## 9.2 The power on/off sequence is illustrated below 电源启动/关闭顺序



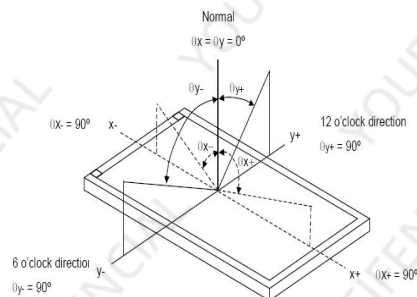
## 9.3 Definition of Contrast Ratio 对比度的定义

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

| Measuring Equipment 测量设备      | BM-7 or EQUI             |
|-------------------------------|--------------------------|
| Measuring Point Diameter 测点直径 | 3mm//1mm                 |
| Measuring Point Location 测点位置 | Active Area centre point |
| Test pattern 测试模式             | A: All Pixels white      |
|                               | B: All Pixel black       |
| Contrast setting              | Maximum                  |

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

## 9.4 Definition of Viewing Angles 视角的定义



Measuring machine: LCD-5100 or EQUI

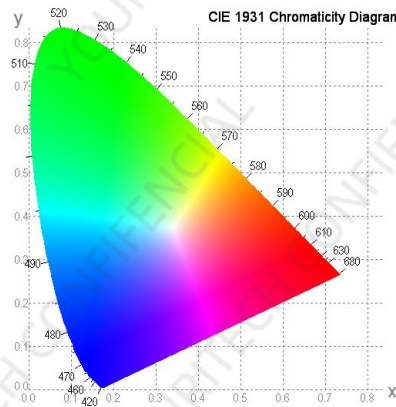
## 9.5 Definition of Color Appearance 色域的定义



R,G,B and W are defined by (x, y) on the IE chromaticity diagram

NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)



## 9.6 Definition of Surface Luminance, Uniformity and Transmittance

表面亮度、均匀性和透光率的定义

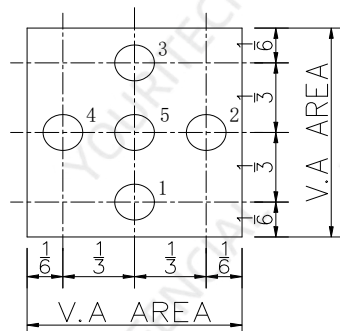
Using the transmissive mode measurement approach, measure the white screen luminance of the display panel and backlight.

9.6.1 Surface Luminance:  $LV = \text{average (LP1:LP5)}$

9.6.2 Uniformity =  $\text{Minimal (LP1:LP5)} / \text{Maximal (LP1:LP5)} * 100\%$

9.6.3 Transmittance =  $LV \text{ on LCD} / LV \text{ on Backlight} * 100\%$

Note :Measuring machine:BM-7



## 10 Quality Assurance 质量标准

### 10.1 Purpose 目的

This standard for Quality Assurance assures the quality of LCD module products supplied to customer by YOURITECH.

### 10.2 Agreement Items 协议项目

YOURITECH and customer shall negotiate if the following situation occurs:

10.2.1 Discrepancies between YOURITECH's QA standards and customer's QA standards.

10.2.2 Additional requirement to be added in product specification.

10.2.3 Any other special problem.

### 10.3 Standard of the Product Visual Inspection 产品外观检验标准

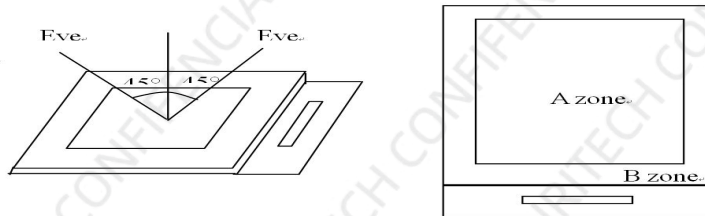


### 10.3.1 Appearance inspection:

10.3.1.1 The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.3.1.2 The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.3.1.3 Definition of area: A Zone: Active Area, B Zone: Viewing Area.



10.3.2 Basic principle: A set of sample to indicate the limit of acceptable quality level must be discussed by both YOURITECH and customer when there is any dispute happened.

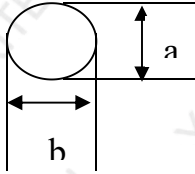
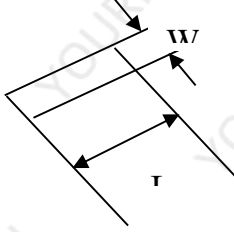
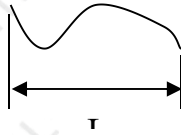
## 10.4 Inspection Specification 检验标准

Sampling plan according to GB/T2828.1-2012/ISO 2859-1: 1999 and ANSI/ASQC

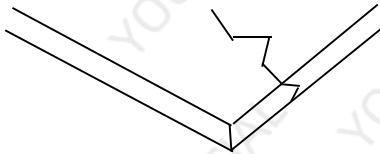
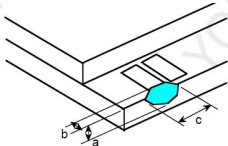
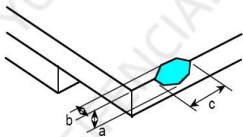
Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.4

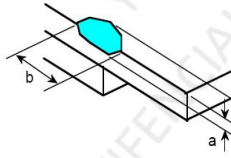
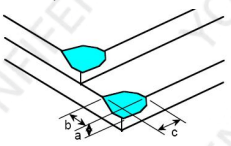
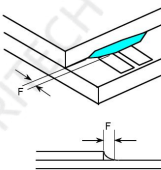
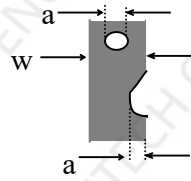
Minor defect: AQL 1.0

| No.                    | Item 项目                                                                                                                                               | Criteria (Unit: mm) 标准                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
|------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------|------|----------|------------------|--|--------|------------------------|--|---|--------------|--|---|-------|--|----------------------------------------------|
| 01                     | Black / White spot<br>Foreign material<br>(Round type)<br>Pinholes Stain<br>Particles inside cell.<br>(Minor defect) 黑/白斑/异物<br>(圆类型)细胞内的针孔染色颗粒。(小瑕疵) |  <table border="1" data-bbox="944 1137 1407 1518"> <thead> <tr> <th>Size</th><th>Area</th><th>Acc. Qty</th></tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.10</math></td><td></td><td>Ignore</td></tr> <tr> <td><math>0.10 &lt; \phi \leq 0.2</math></td><td></td><td>2</td></tr> <tr> <td><math>0.2 &lt; \phi</math></td><td></td><td>0</td></tr> <tr> <td>Total</td><td></td><td><math>N \leq 3</math><br/>NO include<br/><math>\phi \leq 0.10</math></td></tr> </tbody> </table> <p><math>\phi = (a + b) / 2</math><br/>Distance between 2 defects should more than 10mm apart.</p> | Size | Area | Acc. Qty | $\phi \leq 0.10$ |  | Ignore | $0.10 < \phi \leq 0.2$ |  | 2 | $0.2 < \phi$ |  | 0 | Total |  | $N \leq 3$<br>NO include<br>$\phi \leq 0.10$ |
| Size                   | Area                                                                                                                                                  | Acc. Qty                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
| $\phi \leq 0.10$       |                                                                                                                                                       | Ignore                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
| $0.10 < \phi \leq 0.2$ |                                                                                                                                                       | 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
| $0.2 < \phi$           |                                                                                                                                                       | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
| Total                  |                                                                                                                                                       | $N \leq 3$<br>NO include<br>$\phi \leq 0.10$                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |
| 02                     | Black and White line<br>Scratch<br>Foreign material<br>(Line type)<br>(Minor defect) 黑白线刮伤异物(类型)行(小瑕疵)                                                |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |      |      |          |                  |  |        |                        |  |   |              |  |   |       |  |                                              |



| No.                          | Item 项目                                                            | Criteria (Unit: mm) 标准                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
|------------------------------|--------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------|----------|--------------------|--------|--------------------|--------|--------------------|----------------------|------------------------------|---|------------|---|-------|--|------------|
|                              |                                                                    | <table><tr><th>Length</th><th>Width</th><th>Acc. Qty</th></tr><tr><td>/</td><td><math>W \leq 0.03</math></td><td>Ignore</td></tr><tr><td><math>L \leq 3</math></td><td><math>0.05 &lt; W \leq 0.08</math></td><td>2</td></tr><tr><td>/</td><td><math>0.08 &lt; W</math></td><td>0</td></tr><tr><td colspan="2">Total</td><td><math>N \leq 2</math></td></tr></table> <p>Distance between 2 defects should more than 10mm apart.<br/>Scratches not viewable through the back of the display are acceptable.</p> | Length           | Width    | Acc. Qty           | /      | $W \leq 0.03$      | Ignore | $L \leq 3$         | $0.05 < W \leq 0.08$ | 2                            | / | $0.08 < W$ | 0 | Total |  | $N \leq 2$ |
| Length                       | Width                                                              | Acc. Qty                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| /                            | $W \leq 0.03$                                                      | Ignore                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $L \leq 3$                   | $0.05 < W \leq 0.08$                                               | 2                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| /                            | $0.08 < W$                                                         | 0                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| Total                        |                                                                    | $N \leq 2$                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| 03                           | Glass Crack<br>(Minor defect) 玻璃裂纹(小瑕疵)                            |  <p>LCD with extensible crack line is unacceptable(When press the cracked LCD area, the line will expand, we define it is extensible crack line)</p>                                                                                                                                                                                                                                                                         |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| 04                           | Glass Chipping Pad Area: (Minor defect)<br>玻璃碎片面积:(轻微缺陷)           | <table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td><math>c &lt; 5.0, b &lt; 0.4</math></td><td>Ignore</td></tr></table>                                                                                                                                                                                                                                                                                      | Length and Width | Acc. Qty | $c < 5.0, b < 0.4$ | Ignore |                    |        |                    |                      |                              |   |            |   |       |  |            |
| Length and Width             | Acc. Qty                                                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $c < 5.0, b < 0.4$           | Ignore                                                             |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| 05                           | Glass Chipping Rear of PadArea:(Minor defect)<br>) 玻璃切屑垫区后方: (小瑕疵) | <table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td><math>c &gt; 3.0, b &lt; 1.0</math></td><td>1</td></tr><tr><td><math>c &lt; 3.0, b &lt; 1.0</math></td><td>2</td></tr><tr><td><math>c &lt; 3.0, b &lt; 0.5</math></td><td>4</td></tr><tr><td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td></tr></table>                                                                                     | Length and Width | Acc. Qty | $c > 3.0, b < 1.0$ | 1      | $c < 3.0, b < 1.0$ | 2      | $c < 3.0, b < 0.5$ | 4                    | $a < \text{Glass Thickness}$ |   |            |   |       |  |            |
| Length and Width             | Acc. Qty                                                           |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $c > 3.0, b < 1.0$           | 1                                                                  |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $c < 3.0, b < 1.0$           | 2                                                                  |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $c < 3.0, b < 0.5$           | 4                                                                  |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |
| $a < \text{Glass Thickness}$ |                                                                    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                  |          |                    |        |                    |        |                    |                      |                              |   |            |   |       |  |            |



| No.                   | Item 项目                                                                                                                                             | Criteria (Unit: mm) 标准                                                                                                                                                                                                                                     |                  |          |                       |        |                  |        |                   |  |
|-----------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------|----------|-----------------------|--------|------------------|--------|-------------------|--|
| 06                    | Glass Chipping Except Pad Area: (Minor defect) 除垫区外的玻璃切屑:(小瑕疵)<br> | <table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td><math>c \leq 0.6, b &lt; 5.0</math></td><td>Ignore</td></tr><tr><td colspan="2">aGlass Thickness</td></tr></table>                                                                       | Length and Width | Acc. Qty | $c \leq 0.6, b < 5.0$ | Ignore | aGlass Thickness |        |                   |  |
| Length and Width      | Acc. Qty                                                                                                                                            |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| $c \leq 0.6, b < 5.0$ | Ignore                                                                                                                                              |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| aGlass Thickness      |                                                                                                                                                     |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| 07                    | Glass Corner Chipping: (Minor defect) 玻璃切角:(小瑕疵)<br>               | <table><tr><th>Length and Width</th><th>Acc. Qty</th></tr><tr><td><math>c &lt; 2.0, b &lt; 1.5</math></td><td>Ignore</td></tr><tr><td><math>c &lt; 1.5, b &lt; 2</math></td><td>Ignore</td></tr><tr><td colspan="2">a&lt;Glass Thickness</td></tr></table> | Length and Width | Acc. Qty | $c < 2.0, b < 1.5$    | Ignore | $c < 1.5, b < 2$ | Ignore | a<Glass Thickness |  |
| Length and Width      | Acc. Qty                                                                                                                                            |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| $c < 2.0, b < 1.5$    | Ignore                                                                                                                                              |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| $c < 1.5, b < 2$      | Ignore                                                                                                                                              |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| a<Glass Thickness     |                                                                                                                                                     |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| 08                    | Glass Burr: (Minor defect) 玻璃磨:(小瑕疵)<br>                         | Glass burr don't affect assemble and module dimension.<br><table><tr><th>Length</th><th>Acc. Qty</th></tr><tr><td><math>F &lt; 0.5</math></td><td>Ignore</td></tr></table>                                                                                 | Length           | Acc. Qty | $F < 0.5$             | Ignore |                  |        |                   |  |
| Length                | Acc. Qty                                                                                                                                            |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| $F < 0.5$             | Ignore                                                                                                                                              |                                                                                                                                                                                                                                                            |                  |          |                       |        |                  |        |                   |  |
| 09                    | FPC Defect: (Minor defect) FPC 缺陷:(小瑕疵)<br>                      | 9.1 Dent, pinhole width $a < w/3$ .<br>(w: circuitry width.)<br>9.2 Open circuit is unacceptable.<br>9.3 No oxidation, contamination and distortion.                                                                                                       |                  |          |                       |        |                  |        |                   |  |
| 10                    | Screen deformation 屏幕上的变形<br>                                    | Test for insertion of plug gauge at highest warping point:<br>(3.1-6.0inches)<br>$H \leq 0.3MM$<br>The client has special requirements,according to drawing                                                                                                |                  |          |                       |        |                  |        |                   |  |



| No. | Item 项目                                            | Criteria (Unit: mm) 标准                                                                                                                                                                                    |          |
|-----|----------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|
| 11  | Bubble on Polarizer<br>(Minor defect) 偏光片上的气泡(小瑕疵) | Diameter                                                                                                                                                                                                  | Acc. Qty |
|     |                                                    | $\varphi \leq 0.15$                                                                                                                                                                                       | Ignore   |
|     |                                                    | $0.15 < \varphi \leq 0.25$                                                                                                                                                                                | 2        |
|     |                                                    | $0.25 < \varphi \leq 0.3$                                                                                                                                                                                 | 1        |
|     |                                                    | $0.3 < \varphi$                                                                                                                                                                                           | 0        |
| 12  | Dent on Polarizer<br>(Minor defect) 偏光片上的凹痕(小瑕疵)   | Diameter                                                                                                                                                                                                  | Acc. Qty |
|     |                                                    | $\varphi \leq 0.15$                                                                                                                                                                                       | Ignore   |
|     |                                                    | $0.15 < \varphi \leq 0.25$                                                                                                                                                                                | 2        |
|     |                                                    | $0.25 < \varphi \leq 0.30$                                                                                                                                                                                | 1        |
|     |                                                    | $0.3 < \varphi$                                                                                                                                                                                           | 0        |
| 13  | Bezel 边框                                           | 13.1 No rust, distortion on the Bezel.                                                                                                                                                                    |          |
| 14  | Touch Panel 触控面板                                   | D: Diameter W: width L: length                                                                                                                                                                            |          |
|     |                                                    | 14.1 Spot: $D \leq 0.20$ is acceptable<br>$0.20 < D \leq 0.3$ , acceptable QTY, 3<br>$D > 0.3$ is unacceptable                                                                                            |          |
|     |                                                    | 14.2 Dent (dot):<br>$D \leq 0.20$ is acceptable<br>$0.20 < D \leq 0.3$ , acceptable QTY, 3<br>$D > 0.30$ is unacceptable<br>2dots are acceptable and the distance between defects should more than 10 mm. |          |
|     |                                                    | Dent (line) According to the limit sample                                                                                                                                                                 |          |
|     |                                                    | 14.3 Scratch: $W \leq 0.03$ , $L \leq 10$ is acceptable,<br>$0.03 < W \leq 0.10$ , $L \leq 10$ , acceptable QTY, 3<br>$W > 0.10$ is unacceptable.<br>Distance between 2 defects should more than 10 mm.   |          |
| 15  | PCB                                                | 15.1 No distortion or contamination on PCB terminals.                                                                                                                                                     |          |
|     |                                                    | 15.2 All components on PCB must same as documented on the BOM/component layout.                                                                                                                           |          |
|     |                                                    | 15.3 Follow IPC-A-600F.                                                                                                                                                                                   |          |
| 16  | Soldering 焊接                                       | Follow IPC-A-610C standard                                                                                                                                                                                |          |



| No. | Item 项目                                        | Criteria (Unit: mm) 标准                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|-----|------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 17  | Electrical Defect<br>(Major defect) 电气缺陷(主要缺陷) | <p>The below defects must be rejected.</p> <p>17.1 Missing vertical / horizontal segment,</p> <p>17.2 Abnormal Display.</p> <p>17.3 No function or no display.</p> <p>17.4 Current exceeds product specifications.</p> <p>17.5 LCD viewing angle defect.</p> <p>17.6 No Backlight.</p> <p>17.7 Dark Backlight.</p> <p>17.8 Touch Panel no function.</p> <p>17.9 Dark Dot –one Allowed.</p> <p>17.10 Bright Dot – one Allowed.</p> <p>Remark:</p> <p>1. A pixel defect is acceptable if one color is none functional and causes a bright dot. The display may have one case where one color is out and cause a dark dot.</p> <p>2. Bright dot caused by scratch and foreign object accords to item1.</p> |
| 18  | Light leak 漏光                                  | Yellow light OK; White light,According to the limit sample                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |

Remark: Visual and cosmetic defects are rejectable only if these fall within the LCD viewing area.

## 10.5 Classification of Defects 缺陷的分类

Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.

## 10.6 Identification/marketing criteria 识别/评分标准

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

## 10.7 Packing 包装

10.7.1 There should be no damage of the outside carton box, each packaging box should has label in the correct location per packing drawing requirement.

10.7.2 All direct package materials shall offer ESD protection.

## 11 Reliability Specification 可靠性规范

| Item<br>项目                                                   | Condition<br>条件     | Cycle Time<br>周期时间 | Quantity<br>数量 | Remark<br>备注 |
|--------------------------------------------------------------|---------------------|--------------------|----------------|--------------|
| Constant Temp. and Constant Humidity Operation Test 恒温恒湿运行试验 | +40 ± 3°C,90 ± 3%RH | 96hrs              | --             | *1           |
| High Temp. Operation Test 高温操作试验                             | +70 ± 3°C           | 96hrs              | --             |              |
| Low Temp. Operation Test 低温操作试验                              | -20 ± 3°C           | 96hrs              | --             |              |
| Thermal Shock Test 热冲击试验                                     | -20 ± 3°C (30min)   | 10cycles           | --             |              |



|                                            |                                                                             |         |                     |        |
|--------------------------------------------|-----------------------------------------------------------------------------|---------|---------------------|--------|
|                                            | +70 ± 3°C (30min)                                                           |         |                     |        |
| ESD Test(end product) ESD 测试<br>(最终产品)     | 150pF, 330Ω, ±2KV, Contact                                                  | 10times | --                  | *2, *3 |
|                                            | 150pF, 330Ω, ±6KV, Air                                                      |         |                     |        |
| Vibration Test(for packaging) 振动<br>测试(包装) | Frequency: 10Hz to 55Hz<br>to 10Hz, Swing: 1.5mm, time:<br>X, Y, Z each 2H. | 6hrs    | One inner<br>carton | *4     |

Note 1. For humidity test, DI water should be used.

Inspection Standard: Inspect after 1-2hrs storage at room temperature, the sample shall be free from the following defects:

- Air bubble in the LCD
- Seal Leakage
- Non-display
- Missing Segment
- Glass Crack
- IDD is greater than twice initial value.
- Others as per QA Inspection Criteria

Note 2. No defect is allowed after testing

The End Product ESD value is only indicative and depends on customer ESD protection design for the whole system.

Note 3. ESD should be applied to LCD glass panel, not other areas (such as on IC and so on)

IDD should be within twice initial value.

In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

Note 4. Only upon request.

## 12 Precautions and Warranty 注意事项和保证

### 12.1 Safety 安全

12.1.1 The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

12.1.2 Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

### 12.2 Handling 处理

12.2.1 Reverse and use within ratings in order to keep performance and prevent damage.

12.2.2 Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

### 12.3 Operation 操作

12.3.1 Do not drive LCD with DC voltage

12.3.2 Response time will increase below lower temperature

12.3.3 Display may change color with different temperature

12.3.4 Mechanical disturbance during operation, such as pressing on the display area, may



cause the segments to appear “fractured”.

#### 12.4 Static Electricity 静电

12.4.1 CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.

12.4.2 The normal static prevention measures should be observed for work clothes and benches.

12.4.3 The module should be kept into anti-static bags or other containers resistant to static for storage.

#### 12.5 Limited Warranty 有限质量保证

12.5.1 Unless otherwise agreed between YOURITECH and customer, YOURITECH will replace or repair any of its LCD and LCM which YOURITECH found to be defective electrically and visually when inspected in accordance with YOURITECH Quality Standards, for a period of one year from date of shipment.

12.5.2 The warranty liability of YOURITECH is limited to repair and/or replacement. YOURITECH will not be responsible for any consequential loss.

12.5.3 If possible, we suggest you use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used.

### 13 Packaging 包装

TBD

### 14 Prior Consult Matter 免责声明

1. For YOURITECH standard products, we keep the right to change material, process for improving the product property without prior notice to our customer.

2. For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.

3. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

#### Reference 参考

| Item 项目              | Description 描述       | Revision 修订 |
|----------------------|----------------------|-------------|
| NV3041A              | IC Data sheet        | V1.0        |
| Panel 3.92 寸 480X128 | LCM assembly drawing | V0          |

