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Custom Display

Production Custom designs Installation

Mechanical design



SPECIFICATION FOR LCM Module

MODULE No:	ET062VG20-K
CUSTOMER:	

YOURITECH	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		



[illegible]

Contents

1. Block Diagram	5
2. Outline dimension	6
3. Input terminal Pin Assignment	7
4. LCD Optical Characteristics	9
4.1 Optical specification	9
5. Electrical Characteristics	12
5.1 Absolute Maximum Rating	12
5.2 DC Electrical Characteristics	12
5.3 LED Backlight Characteristics	13
6. AC Characteristics	15
6.1 CMOS AC Characteristics	15
6.2 CMOS INTERFACE TIMING	16
6.3 RGB timing	17
7. LCD Module Out-Going Quality Level	18
7.1 VISUAL & FUNCTION INSPECTION STANDARD	18
7.1.1 Inspection conditions	18
7.1.2 Definition	18
7.1.3 Sampling Plan	19
7.1.4 Criteria (Visual)	20
8. Reliability Test Result	24
9. Cautions and Handling Precautions	25
9.1 Handling and Operating the Module	25
9.2 Storage and Transportation	25



* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 6.2" TFT-LCD contains 640x240 pixels, and can display up to 65K/262K/16.7M colors.

* Features

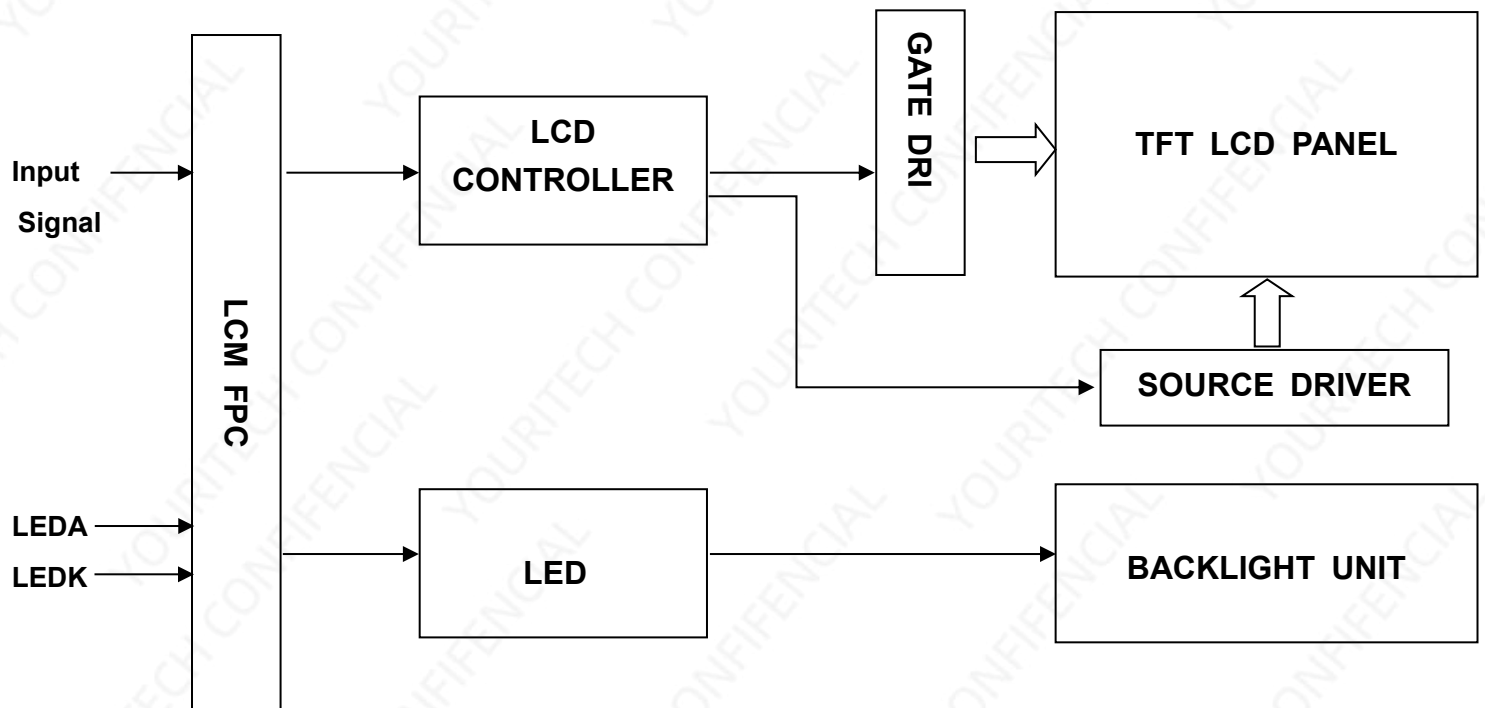
General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	148.8(H)*53.76(V) (6.2 inch)	mm	
Driver element	TFT active matrix	-	
Display colors	65K/262K/16.7M	colors	
Number of pixels	640(RGB)*240	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.2325(H)*0.224(V)	mm	
Viewing angle	ALL	o'clock	
Controller IC	ST5043AA(Gate)+ST5894AA(Source)	-	
LCM Interface	16/18/24BIT RGB	-	
Display mode	Transmissive /Normally Black	-	
Operating temperature	-30~+85	°C	
Storage temperature	-35~+85	°C	

* Mechanical Information

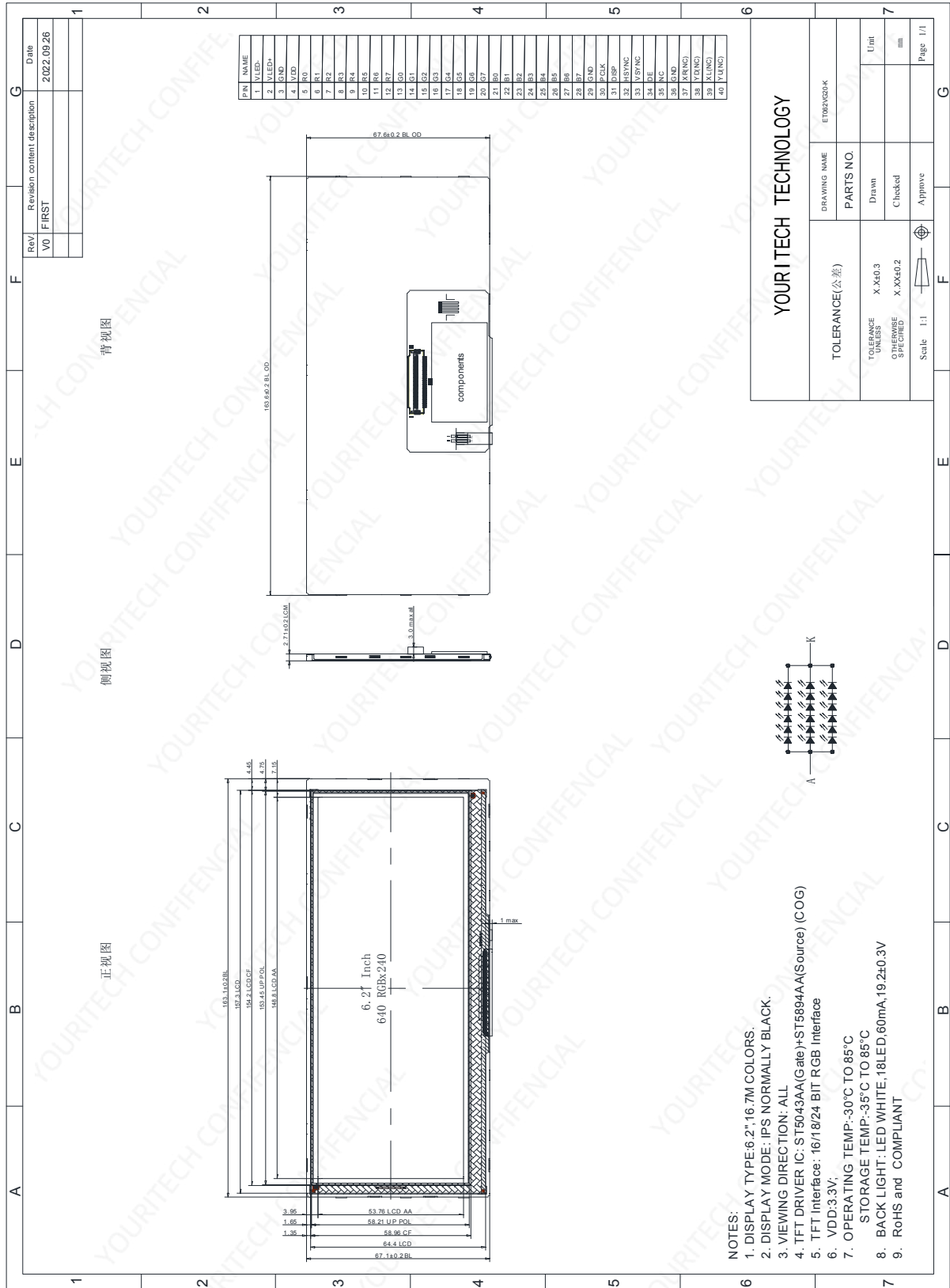
Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	163.1	-	mm	
	Vertical(V)	-	67.1	-	mm	
	Depth(D)	-	2.71	-	mm	
Weight		-	57	-	g	



1. Block Diagram



2. Outline dimension



3. Input terminal Pin Assignment

NO.	SYMBOL	DISCRIPTION	I/O
1	LEDK	Cathode pin OF backlight	P
2	LEDA	Anode pin of backlight	P
3	GND	Ground.	P
4	VDD	Supply voltage(3.3V).	P
5	R0	Red data input.	I
6	R1	Red data input.	I
7	R2	Red data input.	I
8	R3	Red data input.	I
9	R4	Red data input.	I
10	R5	Red data input.	I
11	R6	Red data input.	I
12	R7	Red data input.	I
13	G0	Green data input.	I
14	G1	Green data input.	I
15	G2	Green data input.	I
16	G3	Green data input.	I
17	G4	Green data input.	I
18	G5	Green data input.	I
19	G6	Green data input.	I
20	G7	Green data input.	I
21	B0	Blue data input.	I
22	B1	Blue data input.	I
23	B2	Blue data input.	I
24	B3	Blue data input.	I
25	B4	Blue data input.	I
26	B5	Blue data input.	I
27	B6	Blue data input.	I
28	B7	Blue data input.	I
29	GND	Ground.	P



30	PCLK	Clock signal. Latching data at the rising edge	I
31	DISP	Standby setting for testing, it should be connected to VDD in normal operation mode. If connected to GND, the IC is in standby mode.	I
32	HSYNC	Horizontal Sync input. Negative polarity.	I
33	VSNC	Vertical Sync input. Negative polarity.	I
34	DE	Data input Enable. Active High to enable the data input Bus under "DE Mode".	I
35	NC	--	--
36	GND	Ground.	P
37	XR(NC)	--	--
38	YD(NC)	--	--
39	XL(NC)	--	--
40	YU(NC)	--	--



4. LCD Optical Characteristics

4.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio		CR	$\Theta=0$ Normal viewing angle	1000	1200	--		(1)(2)
Response time	Rising	T_R+T_F		--	25	35	msec	(1)(3)
	Falling							
Color Gamut		S(%)		57	62	--	%	
Color Filter Chromaticity	White	W_X		0.254	0.294	0.334		(1)(4) CA-310
		W_Y		0.287	0.327	0.367		
	Red	R_X		0.595	0.635	0.675		
		R_Y		0.313	0.353	0.396		
	Green	G_X		0.283	0.323	0.363		
		G_Y		0.542	0.582	0.622		
	Blue	B_X		0.106	0.146	0.186		
		B_Y		0.044	0.084	0.124		
Viewing angle	Hor.	Θ_L	CR>10	80	85	--		(1)(4)
		Θ_R		80	85	--		
	Ver.	Θ_U		80	85	--		
		Θ_D		80	85	--		
Option View Direction		ALL						

*The data comes from the LCD specification.

Measuring Condition

Measuring surrounding : dark room

Ambient temperature : $25\pm 2^\circ\text{C}$

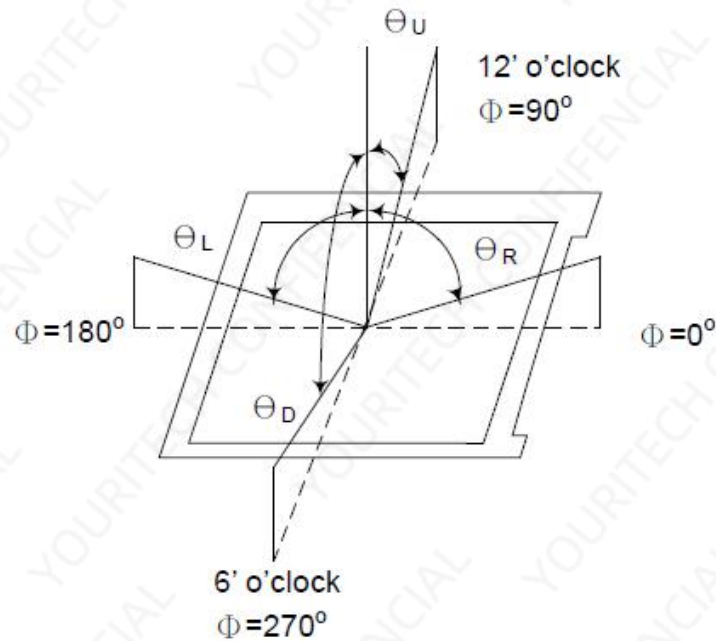
15min. warm-up time.

Measuring Equipment

FPM520 , which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.



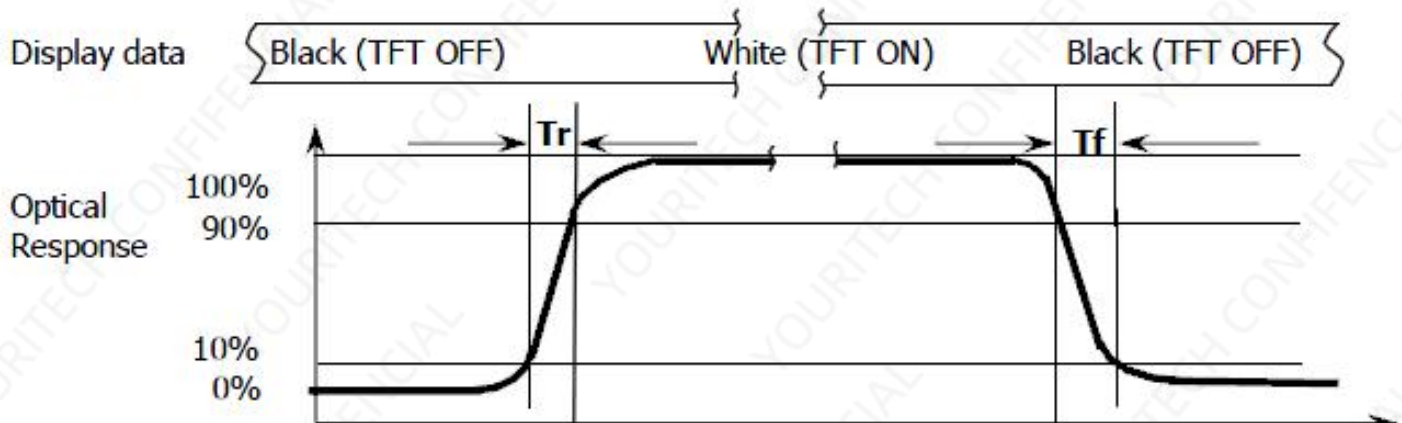
Note (1): Definition of Viewing Angle :



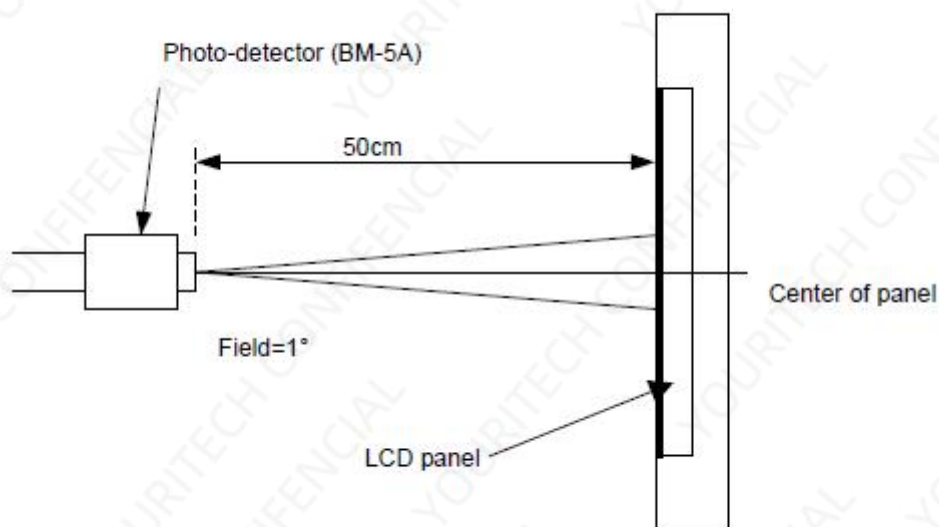
Note (2): Definition of Contrast Ratio(CR) :measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3): Response Time



Note (4): Definition of optical measurement setup



5. Electrical Characteristics

5.1 Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	VDD	-0.3	4.0	V	Note1
Operating temperature	T _{OP}	-30	+85	°C	
Storage temperature	T _{ST}	-35	+85	°C	

NOTE1: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	
Normal mode Current	IDD	--	15	30	mA	
Level input voltage	V _{IH}	0.7*VDD	--	VDD	V	
	V _{IL}	0	--	0.3*VDD	V	
Level output voltage	V _{OH}	VDD-0.4	--	--	V	
	V _{OL}	--	--	GND+0.4	V	



5.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 18 chips LED

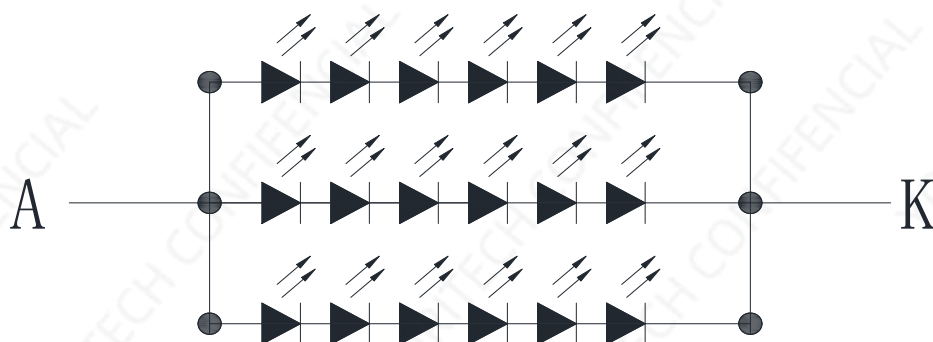
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I_F	50	60	--	mA	
Forward Voltage	V_F	--	19.2	--	V	
LCM Luminance	LV	800	900	--	cd/m2	
LED life time	Hr	50000	--	--	Hour	Note1,2
Uniformity	Avg	80	--	--	%	Note3

Note1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition:

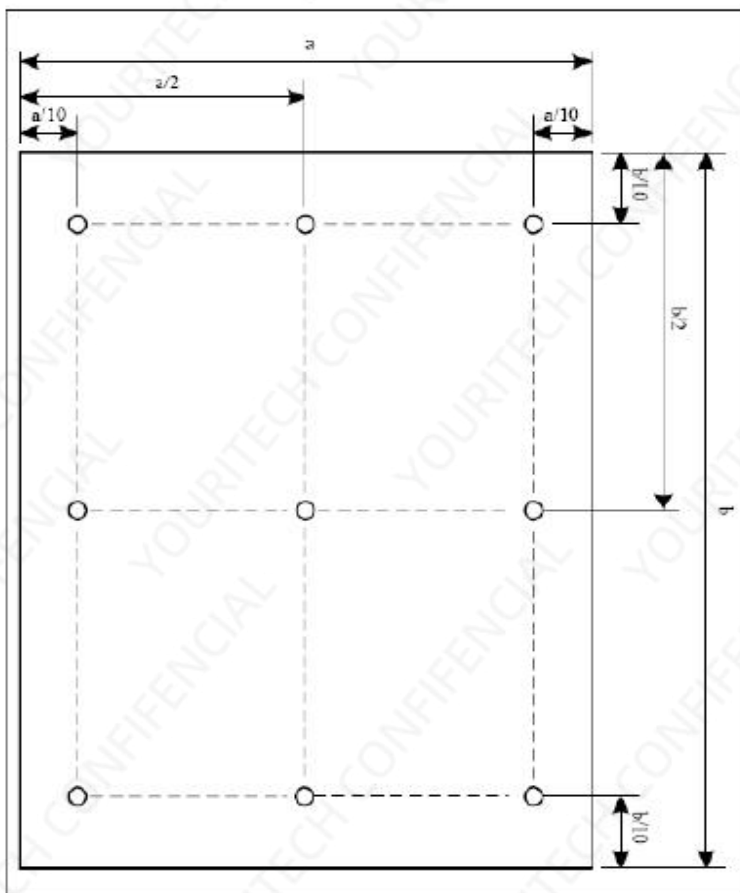
$T_a = 25 \pm 3^\circ \text{C}$, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

$T_a = 25^\circ \text{C}$ and $I_L = 60 \text{mA}$. The LED lifetime could be decreased if operating I_L is larger than 60mA. The constant current driving method is suggested.



Note (3) Luminance Uniformity of these 9 points is defined as below:



$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$$

$$\text{Luminance} = \frac{\text{Total Luminance of 9 points}}{9}$$

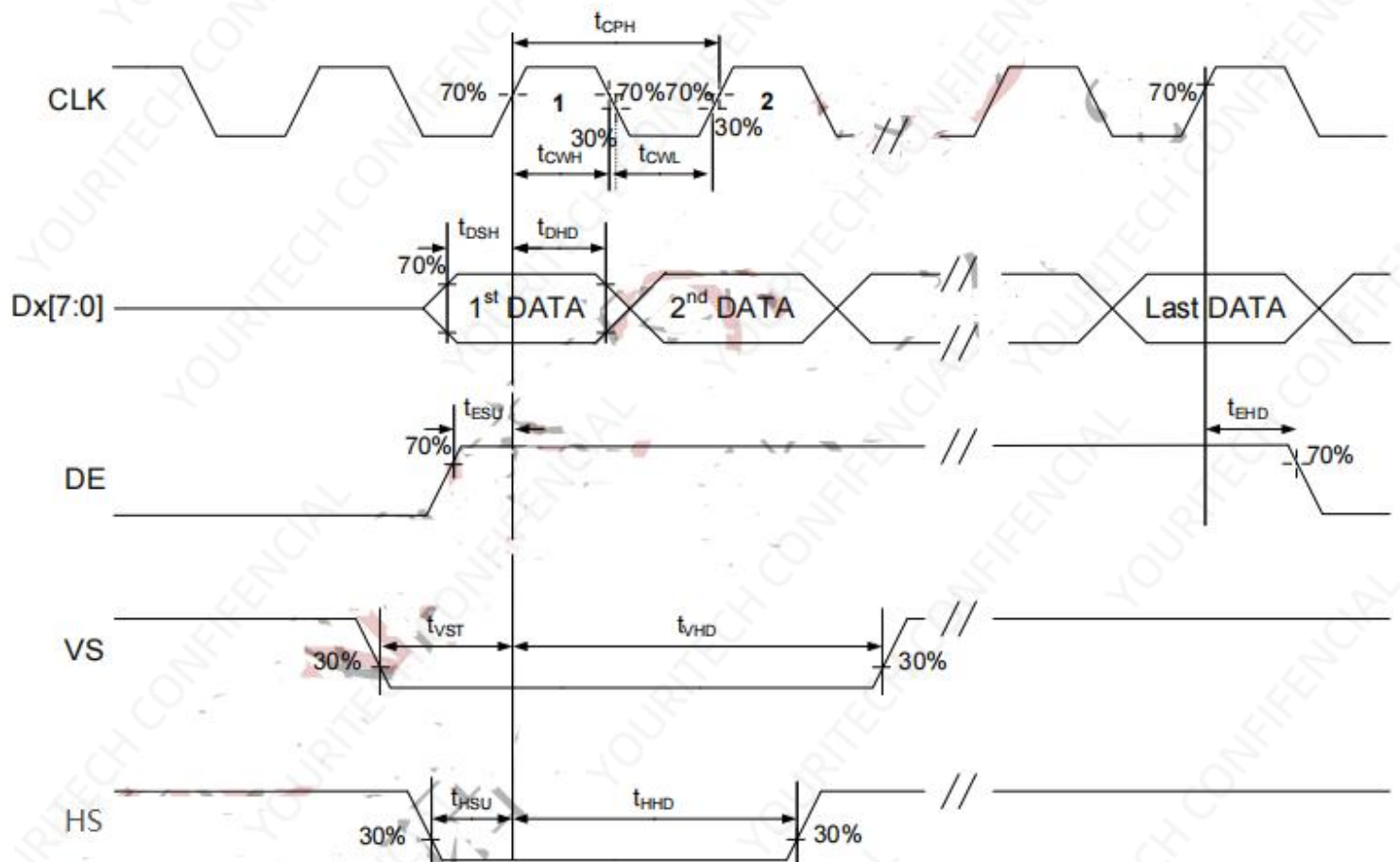


6. AC Characteristics

6.1 CMOS AC Characteristics

(VSSI=VSSRX=VSSP=VSS PFM=VSSA=0V, VDDI=VDDRX=VDDP=VDD PFM=3.0~3.6V)

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
CLK cycle time	t_{CPH}	16.7			ns	
CLK pulse high duty	t_{CWH}	40	50	60	%	
CLK pulse low duty	t_{CWL}	40	50	60	%	
VS setup time	t_{VST}	4	-	-	ns	
VS hold time	t_{VHD}	2	-	-	ns	
HS setup time	t_{HST}	4	-	-	ns	
HS hold time	t_{HHD}	2	-	-	ns	
Data setup time	t_{DSH}	4	-	-	ns	D0[7:0], D1[7:0], D2[7:0], D3[7:0] to CLK
Date hold time	t_{DHD}	2	-	-	ns	D0[7:0], D1[7:0], D2[7:0], D3[7:0] to CLK
DE setup time	t_{ESU}	4	-	-	ns	
DE hold time	t_{EHD}	2	-	-	ns	

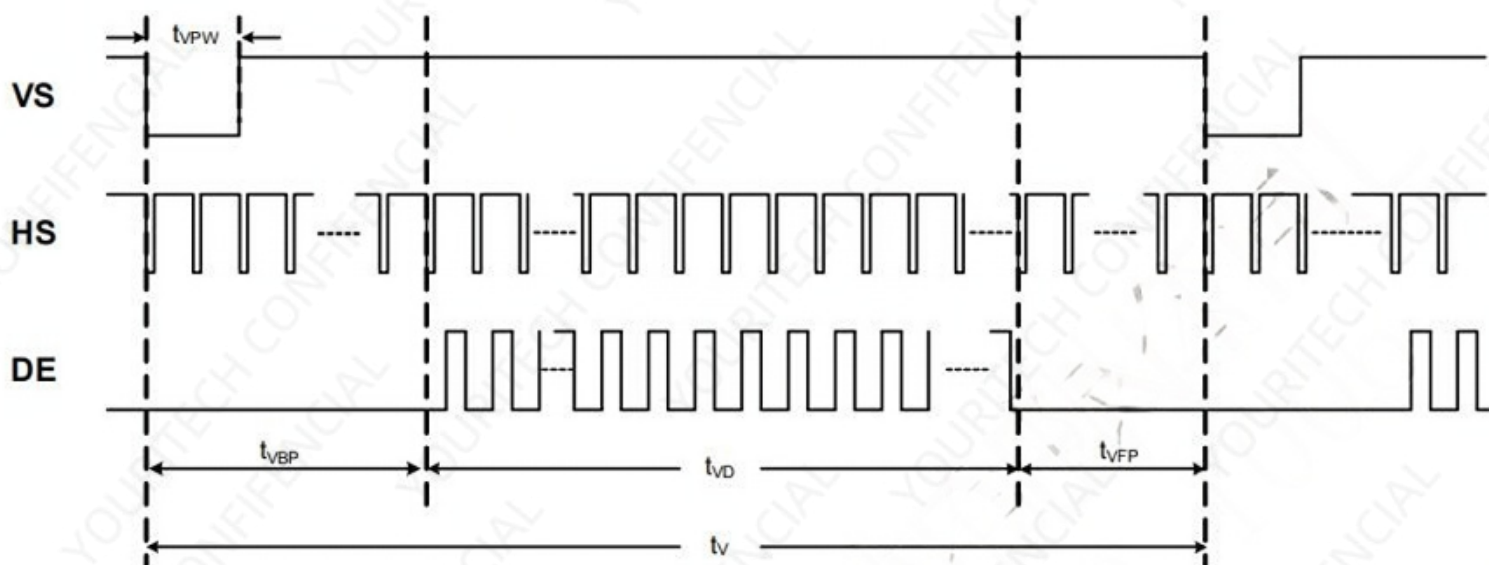


Note: This reference timing diagram set CK_POL=H, VS_POL=L and HS_POL=L

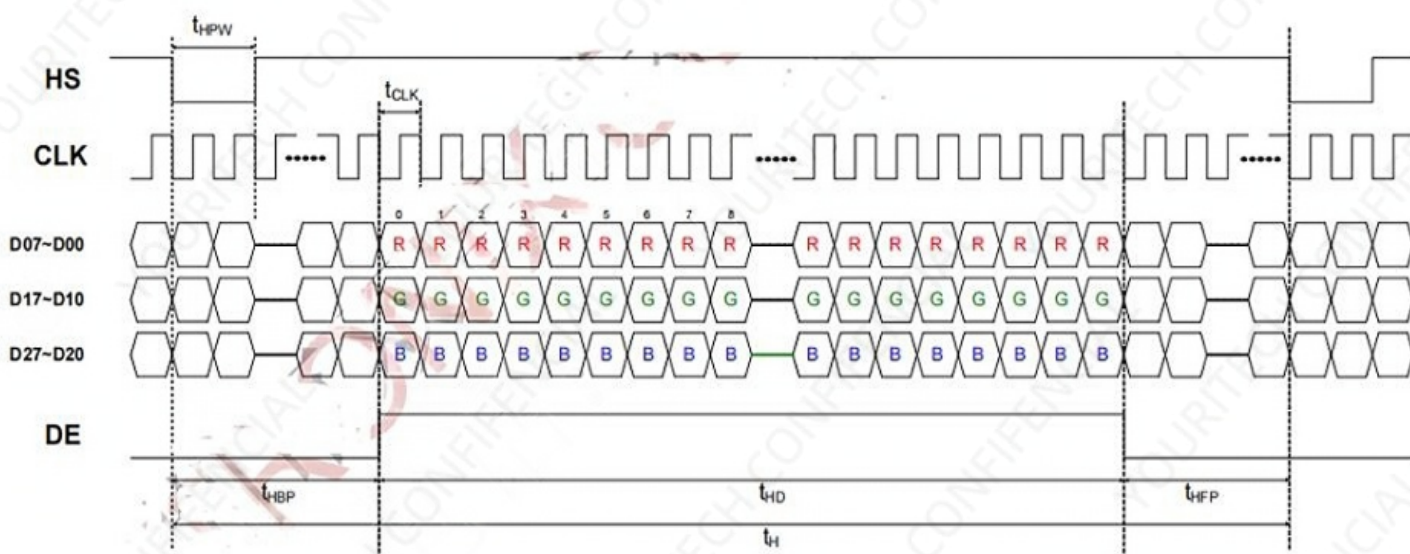


6.2 CMOS INTERFACE TIMING

Vertical input timing



Horizontal input timing

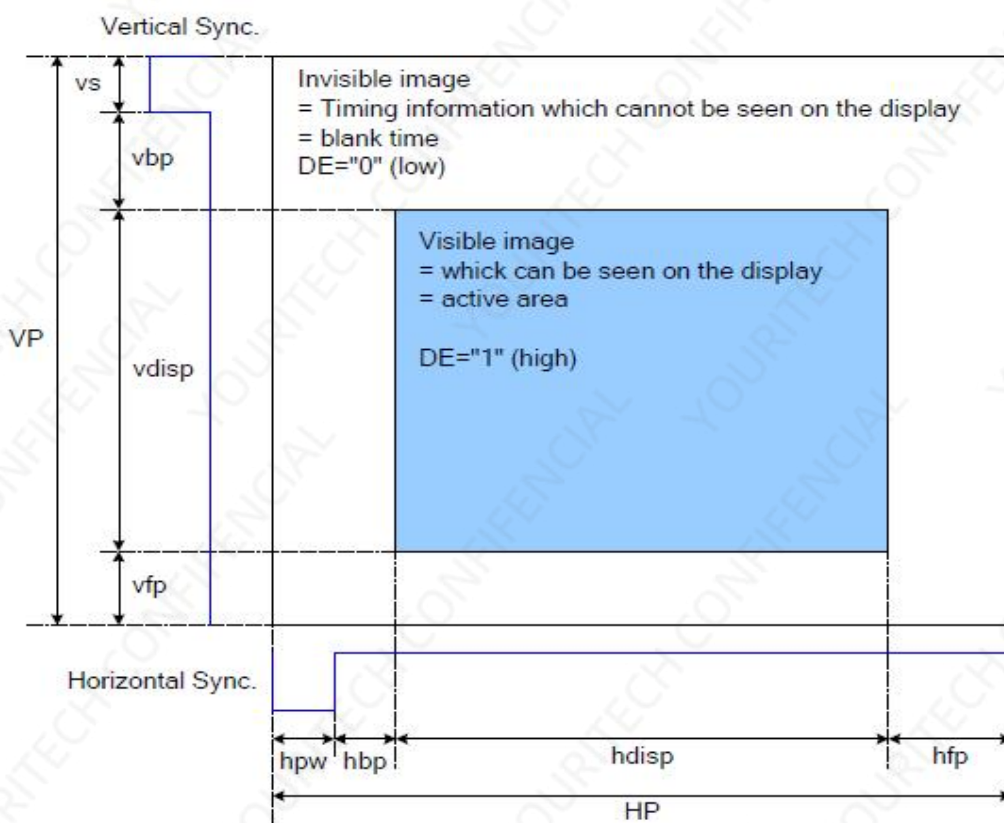


Note: This horizontal timing diagram is based on (Reg) CK_POL=H (Default), and user can select input CLK polarity by software register



6.3 RGB timing

The display operation via the RGB interface is synchronized with the VSYNC, HSYNC, and DOTCLK signals. The data can be written only within the specified area with low power consumption by using window address function. The back porch and front porch are used to set the RGB interface timing.



DRAM Access Area by RGB Interface

Please refer to the following table for the setting limitation of RGB interface signals.

Parameter	Symbol	Min.	Typ.	Max.	Unit
DCLK frequency	FCLK	--	14	--	MHz
Horizontal Active pixels	thd	--	640	--	Clock
Horizontal Sync. Width	hpw	1	4	255	Clock
Horizontal Sync. Back Porch	hbp	1	20	255	Clock
Horizontal Sync. Front Porch	hfp	1	150	--	Clock
Vertical Active pixels	tdv	--	240	--	Line
Vertical Sync. Width	vs	1	4	254	Line
Vertical Sync. Back Porch	vbp	1	20	254	Line
Vertical Sync. Front Porch	vfp	1	20	--	Line
Frame-Rate		--	60	--	Hz

Note: 1. Typical value are related to the setting frame rate is 60Hz..



7. LCD Module Out-Going Quality Level

7.1 VISUAL & FUNCTION INSPECTION STANDARD

7.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

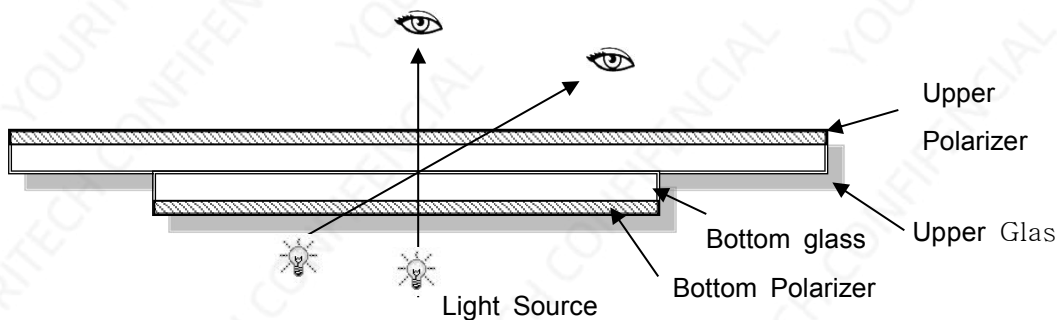
Temperature : $25\pm 5^{\circ}\text{C}$

Humidity : $65\%\pm 10\%\text{RH}$

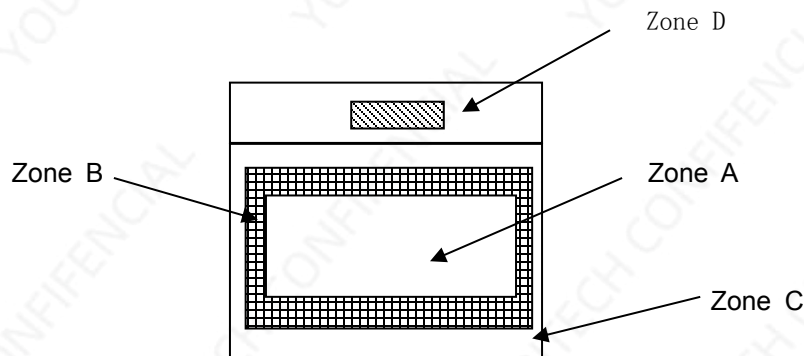
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



7.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D : IC Bonding Area

Note:As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer



7.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class II

AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , LCM: Liquid Crystal Module

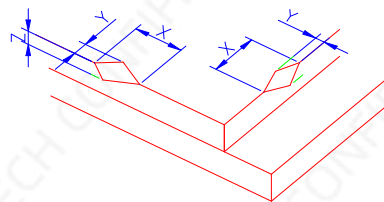
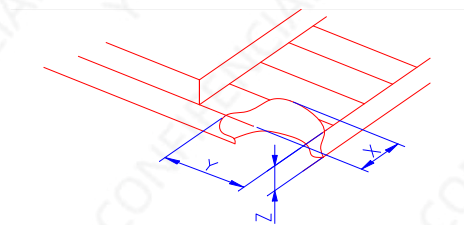
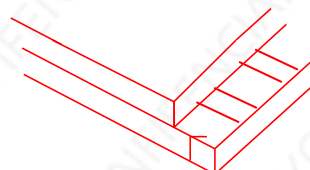
No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. etc...	Major
2	Missing	Missing components and etc...	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed, deformation and etc...	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot/Line defect	Light dot, Dim spot, (Note1) Polarizer Air Bubble, Polarizer accidented spot and etc...	
6	Soldering appearance	Good soldering , Peeling off is not allowed and etc...	
7	LCD/Polarizer	Black/White spot/line, scratch, crack, etc.	

Note1: a) Light dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.

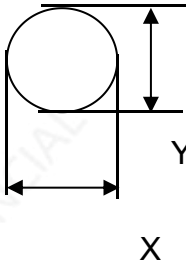
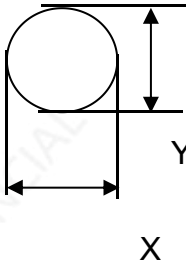
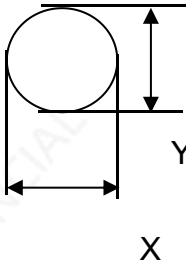
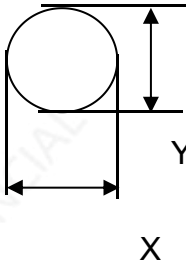
b) Dim dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.



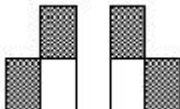
7.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of IT O, T: Height of LCD	(1) The edge of LCD broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td><Inner border line of the seal</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
	X	Y	Z					
	≤3.0mm	<Inner border line of the seal	≤T					
(2)LCD corner broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td>≤L</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	≤L	≤T	
X	Y	Z						
≤3.0mm	≤L	≤T						
(3) LCD crack	 Crack Not allowed							



2.0	Spot defect	① light dot (black/white spot , pinhole, stain, etc.)			
	 $\Phi=(X+Y)/2$	Acceptable Qty			
		A		B	C
		Ignore		Ignore	
		3(distance $\geq 10\text{mm}$)			
		2(distance $\geq 10\text{mm}$)			
	$\Phi \leq 0.15$	0			
	$0.15 < \Phi \leq 0.25$				
	$0.25 < \Phi \leq 0.4$				
	$\Phi > 0.4$				
② Dim spot (light leakage、dent、dark spot, etc)		Acceptable Qty			
	 $\Phi=(X+Y)/2$	A		B	C
		Ignore		Ignore	
		3(distance $\geq 10\text{mm}$)			
		2(distance $\geq 10\text{mm}$)			
		$\Phi \leq 0.15$	0		
$0.15 < \Phi \leq 0.25$					
$0.25 < \Phi \leq 0.4$					
$\Phi > 0.4$					
③ Polarizer accidented spot		Acceptable Qty			
	 $\Phi=(X+Y)/2$	A		B	C
		Ignore		Ignore	
		2(distance $\geq 10\text{mm}$)			
		0			
		$\Phi \leq 0.2$			
$0.2 < \Phi \leq 0.5$					
$\Phi > 0.5$					
④Polarizer Bubble		Acceptable Qty			
	 $\Phi=(X+Y)/2$	A		B	C
		Ignore		Ignore	
		2(distance $\geq 10\text{mm}$)			
		1			
		$\Phi \leq 0.2$	0		
$0.2 < \Phi \leq 0.4$					
$0.4 < \Phi \leq 0.5$					
$\Phi > 0.5$					



3.0	LCD Pixel defect	Pixel bad points																							
		<table> <tr> <th>Item</th><th>Zone A</th><th>Acceptable Qt</th></tr> <tr> <td rowspan="3">Bright dot</td><td>Random</td><td>$N \leq 2$</td></tr> <tr> <td>2 dots adjacent</td><td>$N \leq 0$</td></tr> <tr> <td>3 dots adjacent</td><td>$N \leq 0$</td></tr> <tr> <td rowspan="3">Dark dot</td><td>Random</td><td>$N \leq 3$</td></tr> <tr> <td>2 dots adjacent</td><td>$N \leq 0$</td></tr> <tr> <td>3 dots adjacent</td><td>$N \leq 0$</td></tr> <tr> <td>Distance</td><td> 1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot. </td><td>5mm</td></tr> <tr> <td colspan="2">Total bright and dark dot</td><td>$N \leq 4$</td></tr> </table> Note: A) Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. B) Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture. C) 2 dot adjacent = 1 pair = 2 dots Picture:   2 dot adjacent 2 dot adjacent (vertical)   2 dot adjacent 2 dot adjacent (slant)	Item	Zone A	Acceptable Qt	Bright dot	Random	$N \leq 2$	2 dots adjacent	$N \leq 0$	3 dots adjacent	$N \leq 0$	Dark dot	Random	$N \leq 3$	2 dots adjacent	$N \leq 0$	3 dots adjacent	$N \leq 0$	Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm	Total bright and dark dot		$N \leq 4$
Item	Zone A	Acceptable Qt																							
Bright dot	Random	$N \leq 2$																							
	2 dots adjacent	$N \leq 0$																							
	3 dots adjacent	$N \leq 0$																							
Dark dot	Random	$N \leq 3$																							
	2 dots adjacent	$N \leq 0$																							
	3 dots adjacent	$N \leq 0$																							
Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm																							
Total bright and dark dot		$N \leq 4$																							



4.0	Line defect (LCD /Polarizer backlight/white line, scratch, stain)  W: width, L : length N : Count	Width(mm)	Length(m)	Acceptable Qty		
				A	B	C
		$\Phi \leq 0.05$	Ignore	Ignore		Ignore
		$0.05 < W \leq 0.06$	$L \leq 5.0$	$N \leq 3$		
		$0.06 < W \leq 0.08$	$L \leq 4.0$	$N \leq 2$		
$W > 0.08$	Define as spot defect					
5.0	Electronic Components SMT.	Not allow missing parts, solderless connection, cold solder joint, mismatch, The positive and negative polarity opposite				
6.0	Display color& Brightness.	1. Color: Measuring the color coordinates, The measurement standard according to the datasheet or samples. 2. Brightness: Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.				
7.0	LCD Mura/Waving/ Hot spot	Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary.				

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed



8. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	85°C,96H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature Operating	-30°C, 96HR	
High Temperature Storage	85°C, 96HR	
Low Temperature Storage	-35°C, 96HR	
High Temperature & High Humidity Operating	+60°C, 90% RH ,96 hours.	
Thermal Shock (Non-operation)	-10°C,30 min ↔ +60°C,30 min, Change time:5min 20CYC.	
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15°C~35°C, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
6. The color fading mura of polarizing filter should not care.



9. Cautions and Handling Precautions

9.1 Handling and Operating the Module

- (1) When the module is assembled, it should be attached to the system firmly.
Do not warp or twist the module during assembly work.
- (2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
- (4) Do not allow drops of water or chemicals to remain on the display surface.
If you have the droplets for a long time, staining and discoloration may occur.
- (5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.
Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static; it may cause damage to the CMOS ICs.
- (9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (10) Do not disassemble the module.
- (11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (12) Pins of I/F connector shall not be touched directly with bare hands.
- (13) Do not connect, disconnect the module in the "Power ON" condition.

9.2 Storage and Transportation.

- (1) Do not leave the panel in high temperature, and high humidity for a long time.
It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%
- (2) Do not store the TFT-LCD module in direct sunlight.
- (3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
- (4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.
In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
- (5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.