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Custom Display

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To :
Date :

TFT LCD

ET062WS01-GL

ACCEPTED BY :

APPROVED BY	CHECKED BY	PREPARED BY



REVISION STATUS

[illegible]

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1.OVERVIEW

ET062WS01-GL is 6.2" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) Module composed of LCD Panel, driver ICs, control circuit and LED backlight. By applying 1024× 600 resolution images are displayed on the 6.2" diagonal screen, Display 16.7M color by R.G.B signal input.

General specifications are summarized in the following table :

ITEM	SPECIFICATION			
Panel Size	6.2" inch			
Display Area (mm)	137.47(H) × 77.23 (V)			
Number of Pixels	1024(H) x 600(V)			
Pixel Pitch (mm)	0.134(W)x 0.128(H)			
Color Pixel Arrangement	RGB vertical stripe			
Display Mode	Normally Black			
Number of Colors	16.7M			
Brightness(cd/m ²)	1000nits(typ)			
Response Time (ms)	30ms(typ)			
Color Gamut	60%			
Viewing Direction	Free			
Contrast ratio	1000:1			
Interface connection	LVDS, 40pin			
Module Size(mm)		Min.	Typ.	Max
	Horizontal (H)	148.4	148.7	150.0
	Vertical (V)	87.1	87.4	87.7
	Depth (D)	4.5	4.8	5.1
Module Weight (g)	TBD			
Backlight Unit	LED			
Surface Treatment				

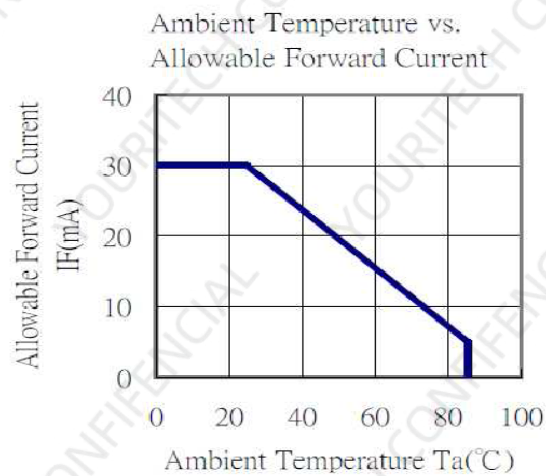


2. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remark
Digital Supply Voltage	DVDD	-0.3	+3.96	V	
Operating Ambient Temperature	T _{OP}	-30	+85	°C	
Operating Ambient Humidity	H _{OP}	60	90	% (RH)	
Storage Temperature	T _{STG}	-30	+85	°C	
Storage Humidity	H _{STG}	60	90	% (RH)	

【Note:】

- *1) If the product were used out of the operation and storage, it will have quality issue.
- *2) Ifp Conditions: Pulse Width $\leq 10\text{msec}$, Duty $\leq 1/10$
- *3) Each one of LED operation must be follow diagram of Ambient Temperature and Allowable Forward



- *4) If users use the product out off the environmenntal operation range (temperature and humidity), it will have visual quality concens.



3. ELECTRICAL CHARACTERISTICS

3.1 The LCD Module Electrical Interface Connection

Pin No.	SYMBOL	FUNCTION	Note
1	NC	No connection	
2	VDD	Power Voltage for digital circuit	
3	VDD	Power Voltage for digital circuit	
4	NC	No connection	
5	Reset	Global reset pin	
6	U/D	Vertical inversion	
7	L/R	Horizontal inversion	
8	STBYB	Standby mode, Normally pulled high	
9	GND	Ground	
10	RXCLKIN-	- LVDS differential clock input	
11	RXCLKIN+	+ LVDS differential clock input	
12	GND	Ground	
13	RXIN0-	- LVDS differential data input	
14	RXIN0+	+ LVDS differential data input	
15	GND	Ground	
16	RXIN1-	- LVDS differential data input	
17	RXIN1+	+ LVDS differential data input	
18	GND	Ground	
19	RXIN2-	- LVDS differential data input	
20	RXIN2+	+ LVDS differential data input	
21	GND	Ground	
22	RXIN3-	- LVDS differential data input	
23	RXIN3+	+ LVDS differential data input	
24	GND	Ground	
25	NC	No connection	
26	GND	Ground	
27	NC	No connection	
28	GND	Ground	
29	NC	No connection	
30	NC	No connection	
31	NC	No connection	
32	NC	No connection	
33	GND	Ground	
34	NC	No connection	
35	LEDK	Backlight LED Cathode	
36	LEDK	Backlight LED Cathode	
37	NC	No connection	
38	NC	No connection	
39	LEDA	Backlight LED Anode	
40	LEDA	Backlight LED Anode	

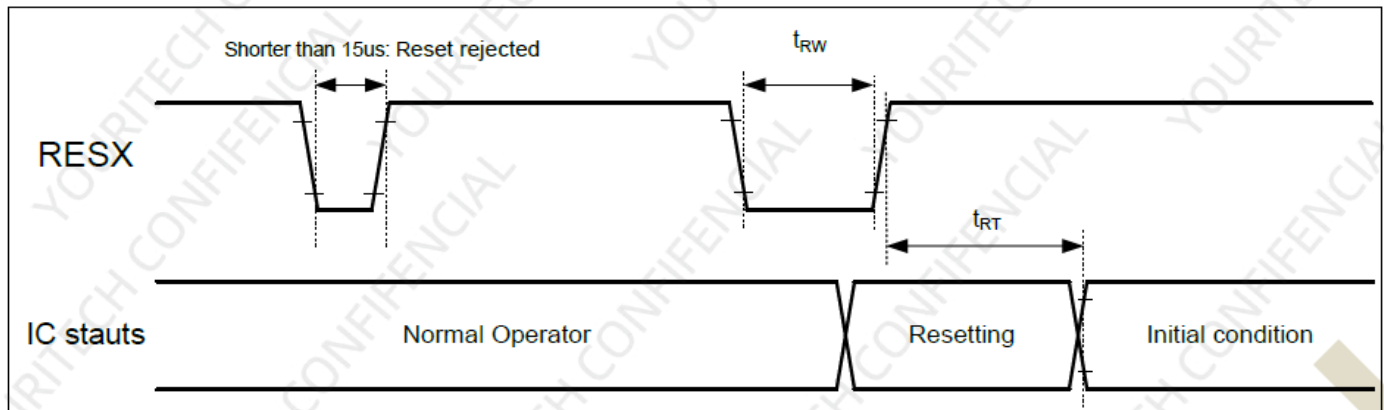


3.2 Backlight Specification

Item	Symbol	CONDITION	Min.	TYP.	Max.	Unit	Remark
LED current	IL	Ta=25°C Each serial=20mA	-	150	-	mA	
LED voltage	VL	Ta=25°C Each serial=20mA	-	16	-	V	
Power consumption	WL	Ta=25°C Each serial=20mA	-	2.4	-	W	
LED Lifetime	-	Ta=25°C Each serial=20mA	30000	-	-	Hr	

3.3 AC Characteristics

3.3.1 Reset input timings



Symbol	Parameter	Related pins	Min.	Max.	Unit
t_{RW}	Reset "L" pulse width ⁽²⁾	RESX	20	-	μs
t_{RT}	Reset complete time ⁽³⁾	-	-	5 ⁽⁵⁾	ms
		-	-	120 ⁽⁶⁾ (7) (8)	ms

Note:

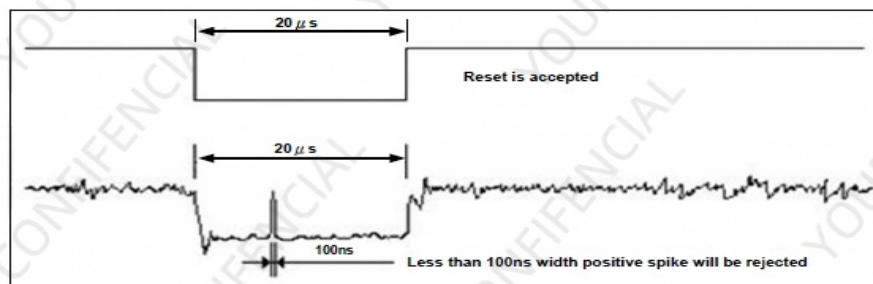
(1) The reset complete time also required time for loading ID bytes from OTP to registers. This loading is done every time when there is HW reset complete time (t_{RT}) within 5 ms after a rising edge of RESX.

(2) Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below.

RESX Pulse	Action
Shorter than 15 μs	Reset Rejected
Longer than 20 μs	Reset
Between 15 μs and 20 μs	Reset Start

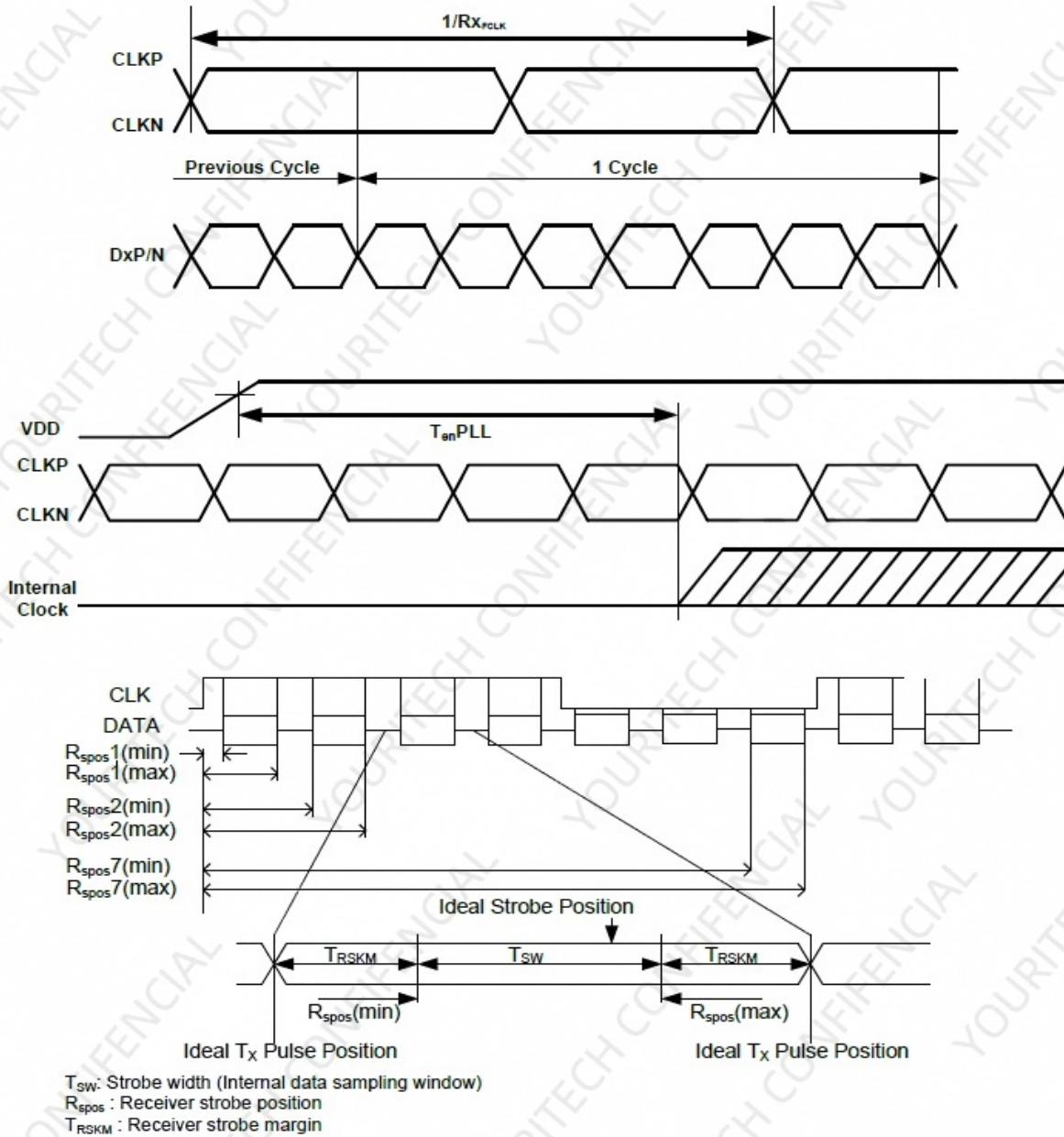
(3) During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out -mode. The display remains the blank state in Sleep In -mode) and then returns to Default condition for HW reset.

(4) Spike Rejection also applies during a valid reset pulse as shown below:



- (5) When Reset is applied during Sleep In Mode.
- (6) When Reset is applied during Sleep Out Mode.
- (7) It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

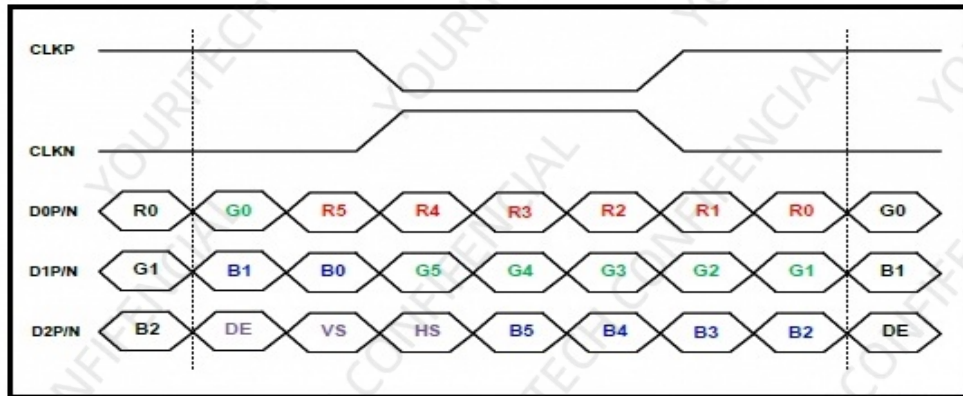
3.3.2 LVDS electronic characteristics



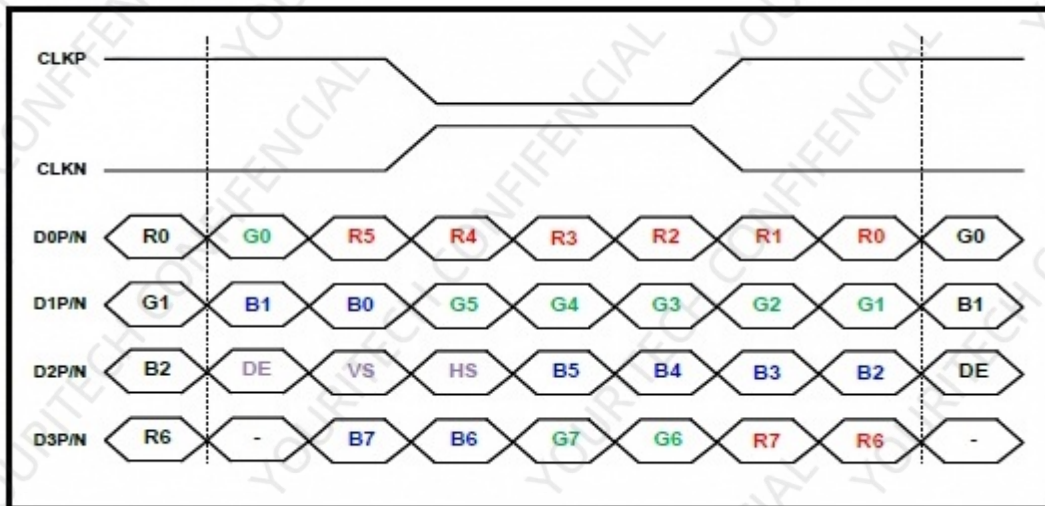
Signal	Symbol	Min.	Typ	Max.	Unit	Description
Clock frequency	Rx_FCLK	30	-	75	MHz	-
Input data skew margin	T_{RSKM}	500	-	-	ps	$ VID = 200mV$ $RxVCM = 1.2V$ $@Rx_FCLK = 75MHz$
Clock high time	T_{LVCH}	-	$4/(7 \times Rx_FCLK)$	-	ns	-
Clock low time	T_{LVCL}	-	$3/(7 \times Rx_FCLK)$	-	ns	-
PLL wake-up time	T_{enPLL}	-	-	150	us	-

3.3.3 LVDS Data format





6-bit LVDS input (IM[1:0]=01, LANSEL[1:0]=10, LVFMT=Don't care)



8-bit LVDS input (IM[1:0]=01, LANSEL[1:0]=11, LVFMT=0(VESA)

3.4 DC Characteristics

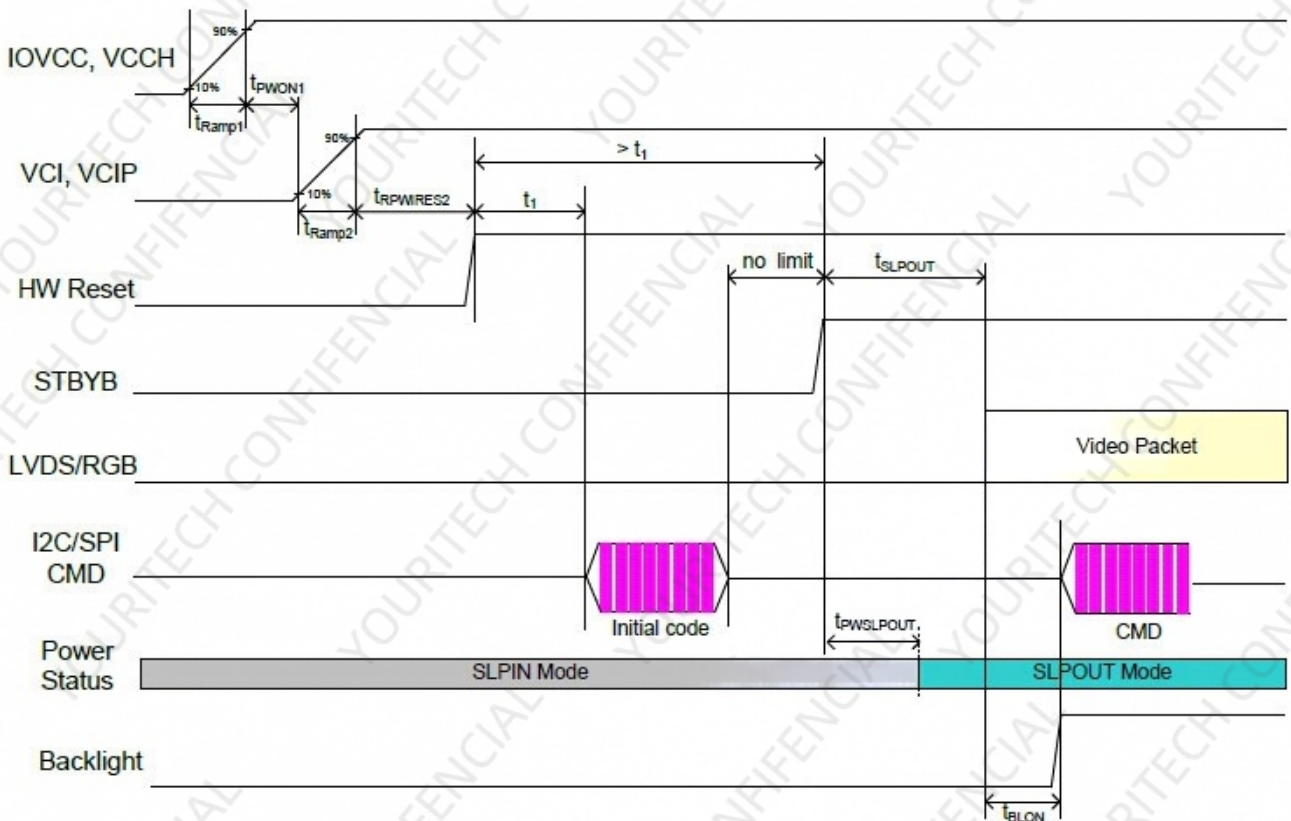
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Differential input high threshold voltage	R_{XVTH}	$R_{XVCM}=1.2V$	+0.1	+0.2	+0.3	V
Differential input low threshold voltage	R_{XVTL}		-0.3	-0.2	-0.1	V
Input voltage range (singled-end)	R_{XVIN}		0.7	-	1.7	V
Differential input common mode voltage	R_{XVCM}	$ VID =0.2$	1	1.2	1.4	V
Differential input impedance	ZID		80	100	125	ohm
Differential input voltage	$ VID $		0.2	-	0.6	V
Differential input leakage current	I_{LCLVDS}		-10	-	+10	μA
LVDS Digital Stand-by Current	I_{STLVDS}	Clock & all Functions are stopped	-	TBD	-	μA



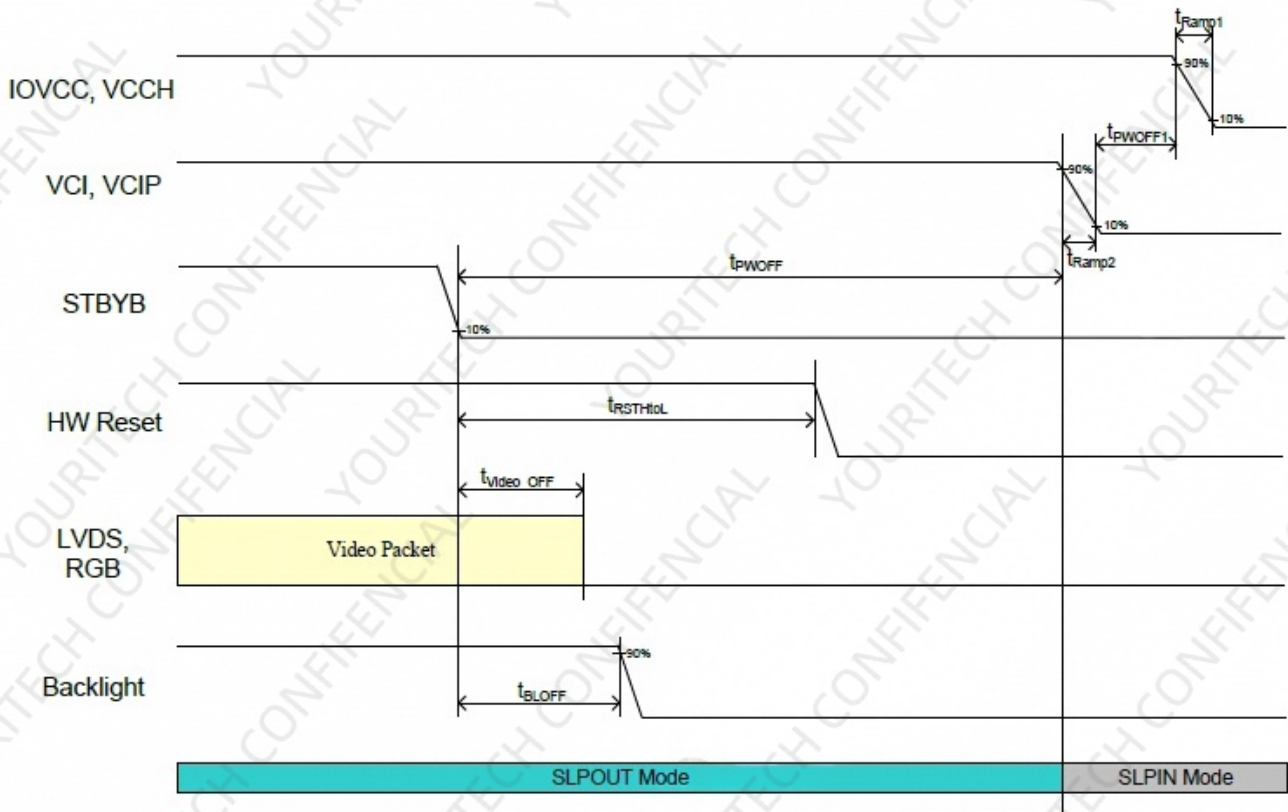
Single-End Signals



3.5 Power on sequence

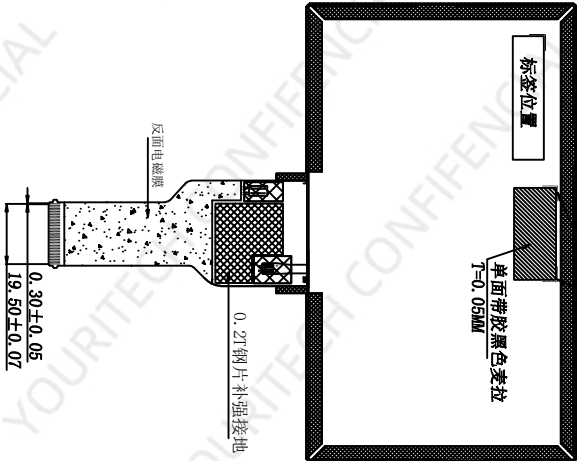
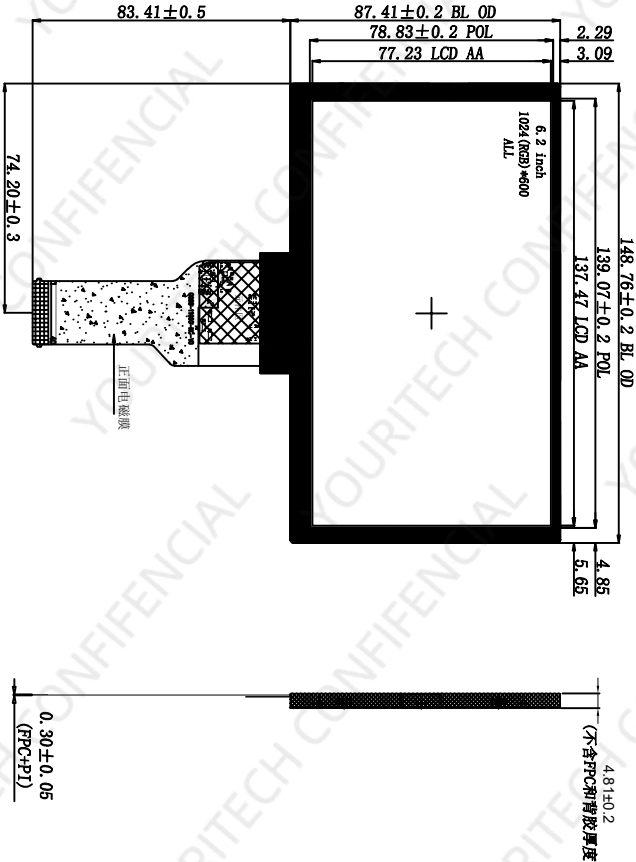


3.5 Power off sequence

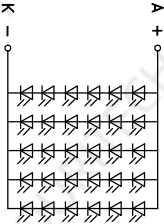


TFT PIN

PIN	SYMBOL
1	NC
2	VDD
3	VDD
4	CS
5	RESET
6	UD
7	LR
8	DISP
9	GND
10	RXCLKIN-
11	RXCLKIN+
12	GND
13	RXINO-
14	RXINO+
15	GND
16	RXIN1-
17	RXIN1+
18	GND
19	RXIN2-
20	RXIN2+
21	GND
22	RXIN3-
23	RXIN3+
24	GND
25	NC
26	GND
27	NC
28	GND
29	NC
30	SCL
31	SDA
32	NC
33	GND
34	NC
35	LED-
36	LED-
37	NC
38	NC
39	LED+
40	LED+



CIRCUIT DIAGRAM (LED 6*5=30 dies)



项目 Item		符号 Symbol	最小值 min.	典型值 typ.	最大值 max.	单位 Unit	测定条件 Condition
主屏 Main screen	亮度 Lumiance	Lv	12000	12500		cd/m ²	If= 150 mA (20mA/LED) (恒定电流测试)
	模组亮度 Lumiance	Lv	900	1000	--	cd/m ²	
	均匀性 Uniformity	Avg	80			%	
	色度坐标 Colour Coordinate	X	---	---	---	客户	
		Y	---	---	---		
		X					
正向电压 Forward Voltage		Vf	--	16	17	V	
背光源电流使用范围		I		150		mA	

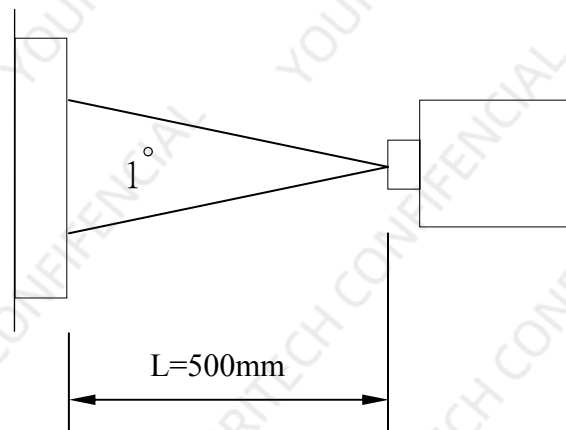
YOURITECH TECHNOLOGY				DATE	2026. 06. 10
TECHTRON MODEL		ET062WS01-GL		APPROVED	
SCALE	1 : 1	UNIT	mm	CHECKED	
PROJECTION:				DESIGNED	

5. OPTICAL CHARACTERISTICS

Ta = 25°C, VCC=3.3V

ITEM		SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	NOTE
Constrast Ratio		CR	Point-5	800	1000			1.2.3
Luminance(CEN)		Lw	Point-5	900	1000		cd/m ²	1.3
Luminance Uniformity		L		75	80		%	1.3
Response Time (White - Black)		Tr +Tf	Point-5	-	30	40	ms	1.3.5
NTSC		-	Point-5	51	60	-	%	1.4
Viewing Angle	Vertical	Upper(θ)	CR ≥ 10 Point-5	-	85	-	Deg	1.4
		Down(θ)		-	85	-	Deg	1.4
	Horizontal	Left(θ)		-	85	-	Deg	1.4
		Right (θ)		-	85	-	Deg	1.4
Color Coordinate	White	Wx	Point-5				-	1.3
		Wy						
	Red	Rx						
		Ry						
	Green	Gx						
		Gy						
	Blue	Bx						
		By						

Note1: Measure condition: 25 °C ±2 °C , 60±10%RH , under 10 Lux in the dark room.BM-5A (TOPCON) .



Note2: Definition of contrast ratio:

Contrast Ratio (CR)= (White) Luminance of ON ÷ (Black) Luminance of OFF

Definition of luminance: Measure white luminance on the point 5 as figure.7-1

$\Delta L = [L(\text{MIN})/L(\text{MAX})] \times 100$



Note3: Definition of luminance: Measure white luminance on the point 5 as figure.7-1

Definition of Luminance Uniformity: Measure white luminance on the point1~9 as figure.7-1

$$\Delta L = [L(\text{MIN})/L(\text{MAX})] \times 100$$

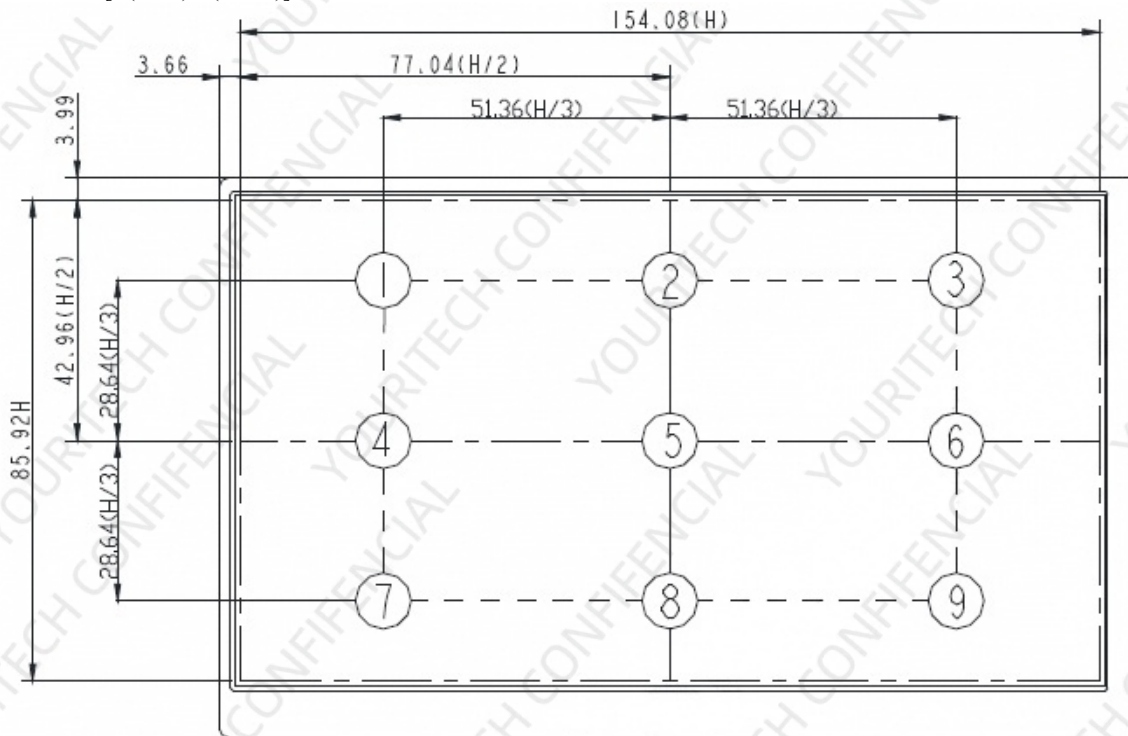


Fig.5-1 Measuring point

Note 4: Definition of Viewing Angle(θ) ,refer to Fig.7-2 as below:

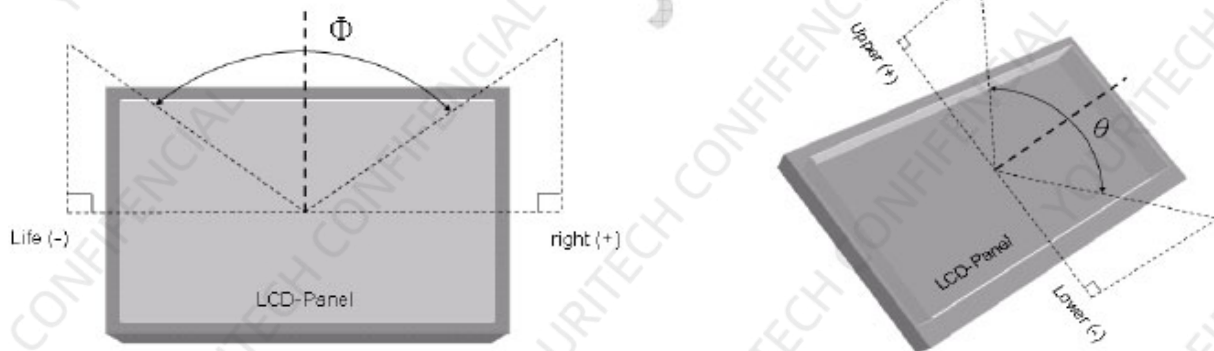


Fig.5-2 Definition of Viewing Angle

Note5: Definition of Response Time.(White-Black)



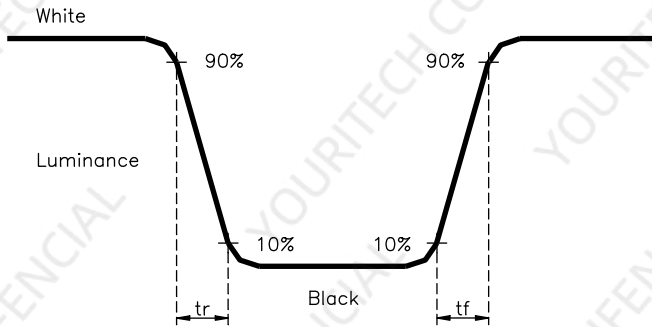


Fig.5-3 Definition of Response Time(White-Black)

6. RELIABILITY TEST ITEM

6.1 TEMPERATURE AND HUMIDITY

TEST ITEMS	CONDITIONS	NOTE
High Temperature Operation	85℃ ; 240hrs	
High Temperature Storage	85℃ ; 240hrs	
High Temperature High Humidity Operation	60℃ ; 90%RH ; 240hrs (No condensation)	
Low Temperature Operation	-30℃ ; 240hrs	
Low Temperature Storage	-30℃ ; 240hrs	
Thermal Shock	-20℃(0.5hr)~70℃(0.5hr); 100Cycles	Non-Operating
Image Sticking	25℃; 4hrs	Note 2
MTBF	30000 Hrs	

NOTE1:

All judgement of display are performed after temperature of panel return to room temperature.
Display function should be no change under normal operating condition.
Under no condensation of dew.
GTECH only guarantee the above 5 test items, and without guarantee the others.

NOTE1:

Condition of Image Sticking test: 25℃±2℃
Operation with test pattern sustained for 4 hrs, then change to mid-gray pattern immediately.
After 5 mins, the mura must be disappeared completely .

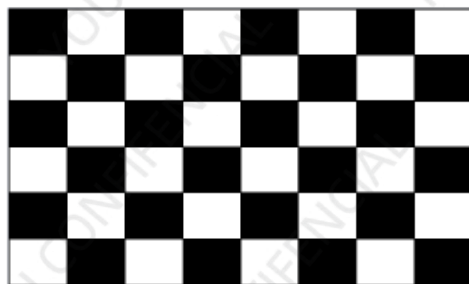
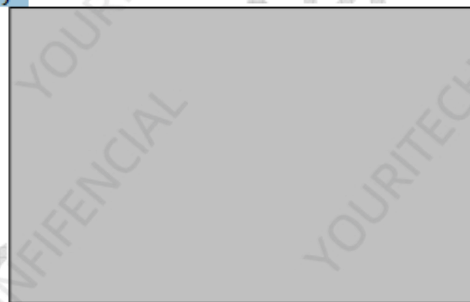


Image Sticking -pattern



Mid-Gray pattern



6.2 Shock and Vibration

ITEMS	CONDITIONS
Shock (Non-Operation)	- Shock level: 980m/s² (equal to 100G). - Waveform: 1/2 Sine wave, 6msec ±X; ±Y; ±Z; each axis 1 times
Vibration (Non-Operation)	- Frequency range: 8~33.3Hz - Stroke: 1.3mm - Vibration: sinusoidal wave, perpendicular axis (both x, z axis: 2Hrs, y axis 4Hrs). - Sweep : 2.9G, 33.3 Hz -400 Hz - Cycle: 15 min

6.3 Electrostatic Discharge

TEST ITEM	Conditions	NOTE
ESD test (operating)	Air Voltage: ±8KV Contact Voltage: ±4KV R: 330Ω C: 150pF 5 time, Surface of AA area 9 point ClassB	

Note

Class B, 有异常而可恢复, 比如闪屏, 因为整机ESD水平不仅与模组相关, 也与系统相关。此处承诺配合客户整机达到要求, 如需要将进行ESD改善。

6.4 Judgment Standard

The Judgment of the above test should be made as follow:

Pass: Normal display image and no line defect.

Partial transformation of the module parts should be ignored.

Fail: No display image, function NG, or line defects.

7. PACKING

TBD



A: Incoming Inspection Specification

1.0 Introduction

1.1. Scope

This incoming Inspection Standard is limited to the TFT-LCD LCD which supplied by YOURITECH. (hereinafter called the "Supplier") to its Customer.

1.2. Incoming inspection Right

The buyer (customer) shall inspect the LCD within twenty days from receiving as inspection period at its own cost. The results of the inspection, acceptance or rejection shall be notified to Supplier .

The buyer may, under commercially reasonable reject procedures, reject an entire lot within inspection period, define unacceptable LCD number in accordance with incoming inspection standard. Should the buyer fail to notify the result of the inspection to supplier within the inspection period, the buyer's right to reject the LCD shall lapse and whole lot shall be deemed to have been accepted by the buyer.

1.3. Operation Instruction

1.3.1 Mounting Method

- As the panel of LCD which consists of two thin glasses with polarizers was easily get Damaged, please handling LCD cautiously.
- Excessive stress or pressure on the glass of the LCD should be avoided. Please insure that no torsional or compressive forces are applied to the LCD unit when it is mounted.
- Abnormal display may occur under press setting problem from customer, which does not mean the malfunction of the LCD and should be verified by both party.
- Optimum mounting angle was determined based on specified viewing angle range.
- Please assemble LCD module in accordance with the specification.
- Please mark condition of humiture.

1.3.2 Caution of LCD Handling and Cleaning

- Since the LCD is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass may be broken.
- The polarizers on the surface of panel are made from organic substances. Be very careful for chemicals that not to touch the polarizers or it may leads the polarizers to be deteriorated.
- If the use of a chemical is unavoidable, use soft cloth with solvent (recommended below) to clean the LCD's surface with wipe lightly.
-IPA(Isopropyl Alcohol),Ethyl Alcohol, Trichlorotrifluoroethane
- Do not wipe the LCD's surface with dry or hard materials that will damage the polarizers

and others. Do not use the following solvent.

-Water, Ketone, Aromatics

- It is recommended that the LCD be handled with soft material during assembly, etc. The polarizers on the LCD's surface are vulnerable to scratch and thus to be damaged by sharp particles.
- Do not drop water or any chemicals onto the LCD's surface.
- A protective film is supplied on the LCD and should be left in place until the LCD is required for operation.
- The ITO pad area needs special careful caution because it could be easily corroded. Do not contact the ITO pad area with HCFC, Soldering flux, Chlorine, Sulfur, saliva or fingerprint. To prevent the ITO corrosion, customers are recommended that the ITO area would be covered by UV or silicon.
LCD should be stored in static-protective & vacuum polythene bag, please assemble it When it expose to the air within 3 days to avoid ITO corrosion
- Please clean the LCD without ultrasonic to avoid line open.
- Temperature of clean and bake should be less than 80℃.

1.3.3 Caution Against Static Charge

- The LCD modules use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.
- Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
- In handling the LCD, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary.

1.3.4 Caution For operation

- It is indispensable to drive the LCD within the specified voltage limit since the higher Voltage than the limit causes the shorter LCD's life. An electro-chemical reaction due to DC causes undesirable deterioration of the LCD so that the use of DC drive should avoid.
- Do not connect or disconnect the LCD to or from the system when power is on.
- Never use the LCD under abnormal conditions of high temperature and high humidity.
- When expose to drastic fluctuation of temperature(hot to cold or cold to hot) ,the LCD may be affected; specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD's surface which may affect the operation of the polarizer and the LCD.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD may turn black at



temperature above its operational range. However those phenomena do not mean malfunction or out of order with the LCD. The LCD will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

- Do not display the fixed pattern for a long time because it may develop image sticking due to the LCD structure. If the screen is displayed with fixed pattern, use a screen saver.
- Static electricity (ESD) will damage the panel,. Please make sure that operators wear static-protective glove effectively and working tables & device are effectively grounded during operation and other ESD protective method
- Please place LCD on the tray provided by YOURITECH while moving it, in order to avoid mechanical damage.
- LCD should be stored in required humidity. Low humidity may add static, while high humidity may corrode the ITO circuit of LCD product.
- Before use the LCD. Please check the Engineering specification.
- Please keep the LCD in the specified, original packing boxes when storage.
- LCD contain a small amount of Liquid Crystal and Mercury. Please follow local ordinances or regulations for disposal.
- DO NOT press the area covered with PET or such materials. These are weak point of LCD since of TCPs (Driver ICs) and PWBs.
- Please DO NOT touch the surface of glass (Polarizer).

2.0 Generals

2.1. Sampling Method

Unless otherwise agreed upon in writing ,the sampling inspection shall be applied to the customer's Incoming inspection.

2.1.1. Lot Size: 1 pallet per same model;

2.1.2. Sampling type: Random sampling;

2.1.3. Inspection level: II

2.1.4. Sampling table: MIL-STD-105E

Major Defect: AQL=0.65

Minor Defect: AQL=1.5

2.2. Inspection Environment

2.2.1. Inspection environment conditions:

a. Room temperature: 23 ± 2 °C ;

b. Humidity: $60 \pm 10\%$ RH;

c. Inspection Ambient Illumination : 300~700 Lux (150~250 Lux for function test);

2.2.2. Viewing Distance

The distance between the panel and the inspector's eyes shall be at 30CM~50CM;

2.2.3. Viewing Angle

performing in front of the panel All directions for inspecting the sample should be:

ADS Production: within 45° to perpendicular line.;

TN Production: within 10° to perpendicular line.;

2.2.4. Inspection Area :

Display Area (Active Area)

2.3. Main Defect Definitions

2.3.1 Black / White Spots

Points on display which appear Black/ white at L0/L127/L255 .

2.3.2. Dark / Bright Lines

Lines on display which appear dark/bright at R/G/B. such as vertical, horizontal, or cross lines.

2.3.3. Bright Dot Defects

Dots(sub-pixels) on display which appear bright in the display area at R/G/B.

2.3.4. Dark Dot Defects

Dots(sub-pixels) on display which appear dark in the display area at R,G,B Color Pattern.

2.3.5. Mura

Mura on display which appears darker / brighter against background brightness on parts of display area at L0/L127/L255

2.3.6. Visual Inspection

Inspect PNL in operation

2.3.7. Appearance Inspection

External inspection for Panel in Non Operation



3.0 Inspection Criteria

3.1. Visual Inspection Criteria

Dimensional unit: mm

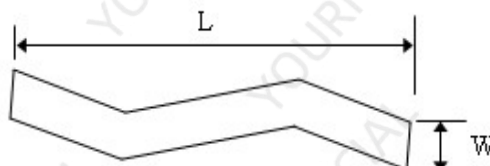
Items		Details	Inspection Criteria		Type
			A Area	B/C Area	
Visual (Function) Inspection	Foreign Material /Dent/ Bubble/ Spots//Extraneous Substances/Dot	Circular Type	$0.15 < D \leq 0.3, N \leq 4$	Ignore	Minor
		Linear Type	$0.05 < W \leq 0.10, L \leq 3, N \leq 3$		
	Pixel Defects	Bright Dot	$N \leq 0$	Ignore	Major
		Dark Dot	$N \leq 4$		
		Bright + Dark Dot	$N \leq 4$		
		2S	$N \leq 1$		
	Line Defects	Bright Line, Dark Line	Not Allowed		
	No Display		Not Allowed		
	Abnormal Display		Not Allowed		
	Mura		5%ND not visible, or reference limit samples		Minor

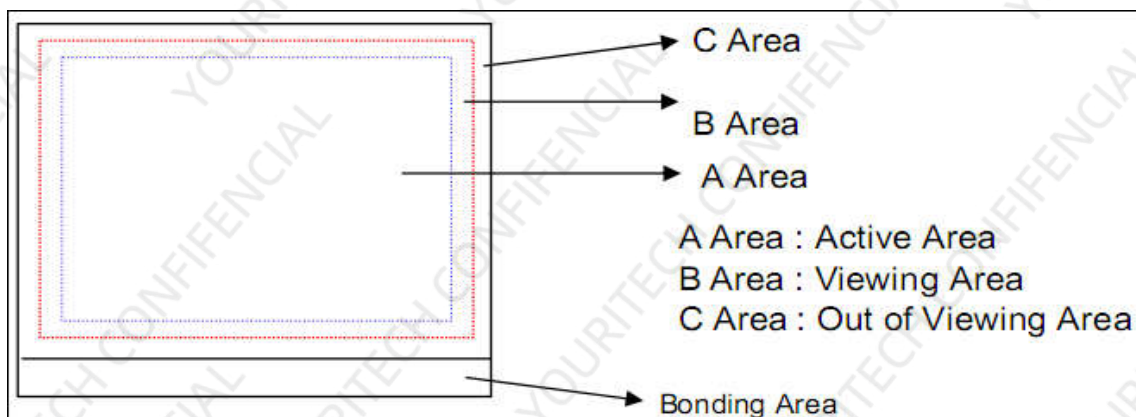
Remark: The determination of all defects is based on the panel with Polarizer.

※ Note 1) D = Diameter, L = Length, W = Width, N = Number

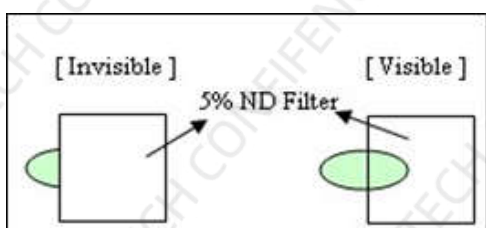
※ Note 2) Definition of the Area A Area: Display area B/C Area: No display area

$$D = (a + b) / 2$$



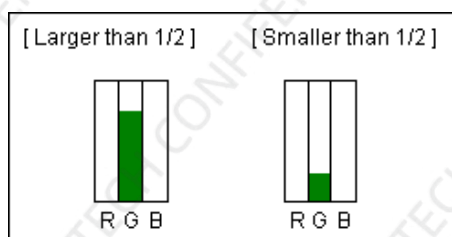


※Note 3) For pixel defect, dot means a sub-pixel. Dot defects should be larger than half size of a sub-pixel.
Dot which is invisible through 5% ND filter or smaller than 1/2 of sub-pixel size will not counted as "1 dot" defect.



"No dot defect"
(=ignored)

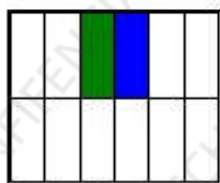
"1 dot defect"
(=counted)



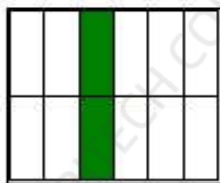
"1 dot defect"
(=counted)

"No dot defect"
(=ignored)

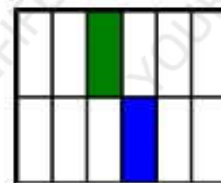
[2 adjacent dots defect]



Type 1



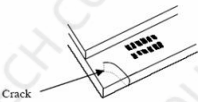
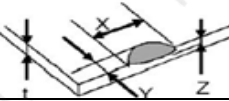
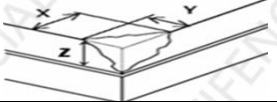
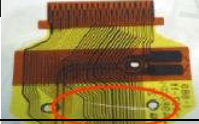
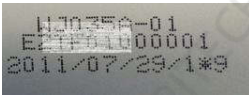
Type 2



Type 3



3.2. Appearance Inspection Criteria

location	Items	Criterion for Defects		Type	scope
All	Stain		Removable stain is OK	-	All
Be related to PNL	Crack		Not Allowed	Major	Shipment status: Single Cell/FOG/MDL Production
	Side Chipping		Function and assembly are not affected	Minor	
	Corner Chipping		Function and assembly are not affected	Minor	
	Burr		Function and assembly are not affected	Minor	
	Scratch		PNL with POL, based on point/line foreign (scratch) standard to determine,	Minor	
Be related to FPC/PCB	short circuit / open circuit		Not Allowed	Major	Shipment status: FOG/MDL Production
	components and parts		Component missing is not allowed	Minor	
Be related to Backlight	Code-spurting		Key information can be identified is OK	Minor	Shipment status: MDL Production
	Scratch		Limit Sample	Minor	
	Stain		Removable stain is OK	Minor	

