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MODEL NO : ET070WS02-X**MODEL VERSION:** 01**SPEC VERSION :** 1.0**ISSUED DATE:** 2017-12-11☒ **Preliminary Specification**☐ **Final Product Specification****Customer :** _____

Approved by	Notes

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Record of Revision

[illegible]

1 General Specifications

Feature		Spec
Display Spec.	Size	7.0 inch
	Resolution	1024(RGB) X 600
	Technology Type	a-Si
	Pixel Configuration	RGB Vertical Stripe
	Pixel pitch(mm)	0.1506(H)×0.1432(V)
	Display Mode	Normally Black
	Viewing Direction	ALL
Mechanical Characteristics	LCM (W x H x D) (mm)	164.00(W) x 97.2(H) x 2.8 (D)
	Active Area(mm)	154.21(H)x85.92(V)
	With /Without TSP	Without TSP
	Weight (g)	TBD
Electrical Characteristics	Interface	6bit/8bit LVDS
	Color Depth	262K

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: ± 5%

2 Input/Output Terminals

FPC connector is used for electronics interface. The recommended model is FH12A-40S-0.5SH manufactured by Hirose.

Pin No.	Symbol	Type	Function
1	VCOM	P	Common voltage
2	VDD	P	Digital power
3	VDD	P	Digital power
4	NC	-	Not connect
5	REST	I	Global reset pin. Active low to enter reset state. Suggest to connecting with an RC reset circuit for stability. Normally pull high. (R=10K Ω , C=0.1 μ F)
6	STBYB	I	Standby mode, normally pull high STBYB="1", normal operation STBYB="0", timing control, source driver will turn off, all output are high-Z
7	GND	P	Ground
8	RXIN0-	I	Negative LVDS differential data inputs
9	RXIN0+	I	Positive LVDS differential data inputs
10	GND	P	Ground
11	RXIN1-	I	Negative LVDS differential data inputs
12	RXIN1+	I	Positive LVDS differential data inputs
13	GND	P	Ground
14	RXIN2-	I	Negative LVDS differential data inputs
15	RXIN2+	I	Positive LVDS differential data inputs
16	GND	P	Ground
17	RXCLKIN-	I	Negative LVDS differential clock inputs
18	RXCLKIN+	I	Positive LVDS differential clock inputs
19	GND	P	Ground
20	RXIN3-	I	Negative LVDS differential data inputs
21	RXIN3+	I	Positive LVDS differential data inputs
22	GND	P	Ground
23	NC	-	Not connect
24	NC	-	Not connect
25	GND	P	Ground
26	NC	-	Not connect
27	DIMO	O	Backlight CABC controller signal output
28	SELB	I	6bit/8bit mode select H : 6bit / L : 8bit
29	AVDD	P	Power for Analog Circuit

30	GND	P	Ground
31	LED-	P	LED Cathode
32	LED-	P	LED Cathode
33	L/R	I	Horizontal inversion
34	U/D	I	Vertical inversion
35	VGL	P	Negative power for TFT
36	GND	P	Ground
37	GND	P	Ground
38	VGH	P	Positive power for TFT
39	LED+	P	LED Anode
40	LED+	P	LED Anode

I : input , O : output , P : Power

【Note】

When L/R="0" , set right to left scan direction

When L/R="1" , set left to right scan direction

When U/D="0" , set top to bottom scan direction

When U/D="1" , set bottom to top scan direction

3 Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	V_{DD}	-0.3	5.0	V	
	V_{GH}	-0.3	40.0	V	
	V_{GL}	-20.0	0.3	V	
	AV_{DD}	6.5	13.5	V	
	V_I	-0.3	$V_{DD} + 0.3$	V	
Logic Signal Input Level	V_{DD}	-0.3	5.0	V	

GND=0V

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	
Analog Supply Voltage	AVDD	8.8	9.0	9.2	V	
Gate On Voltage	VGH	20	21	22	V	
Gate Off Voltage	VGL	-8.5	-7.5	-6.5	V	
Common Voltage	VCOM	3.4	3.6	3.8	V	Note1

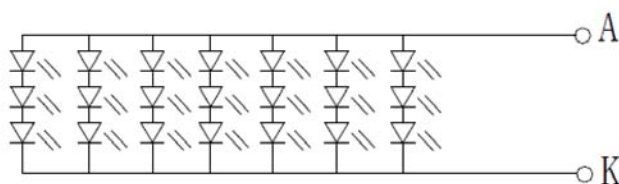
Note: TYP VCOM is only reference value. It must be optimized according to each LCM. Be sure to use VR and OP buffer on VCOM output. Please adjust VCOM to make the flicker level be minimum for getting excellent image.

4.2 Backlight Unit

The backlight system is an edge-lighting type with 21 LED. The characteristics of the LED are shown in the following tables.

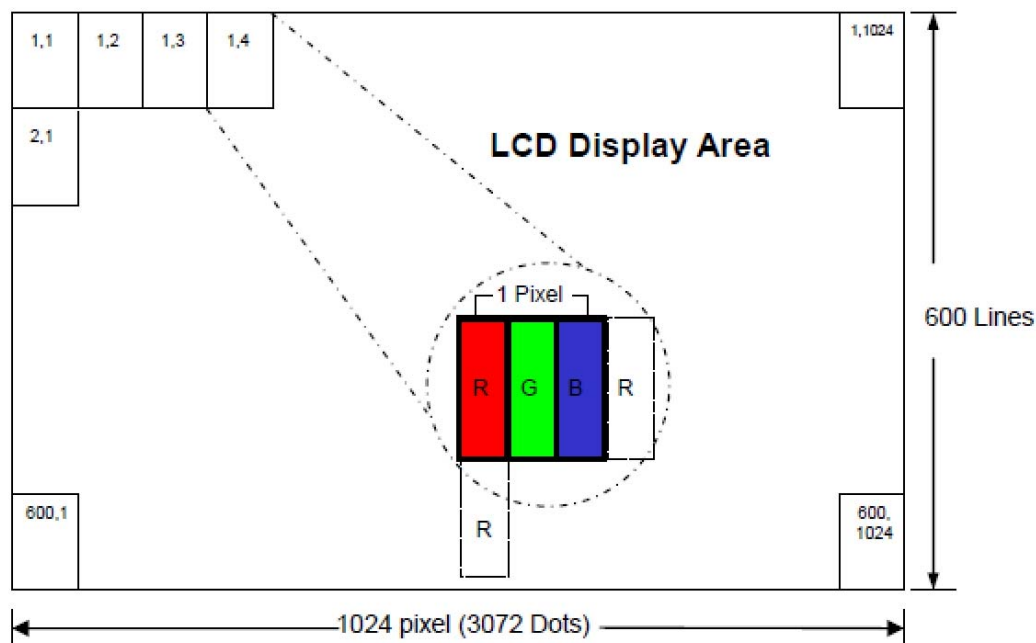
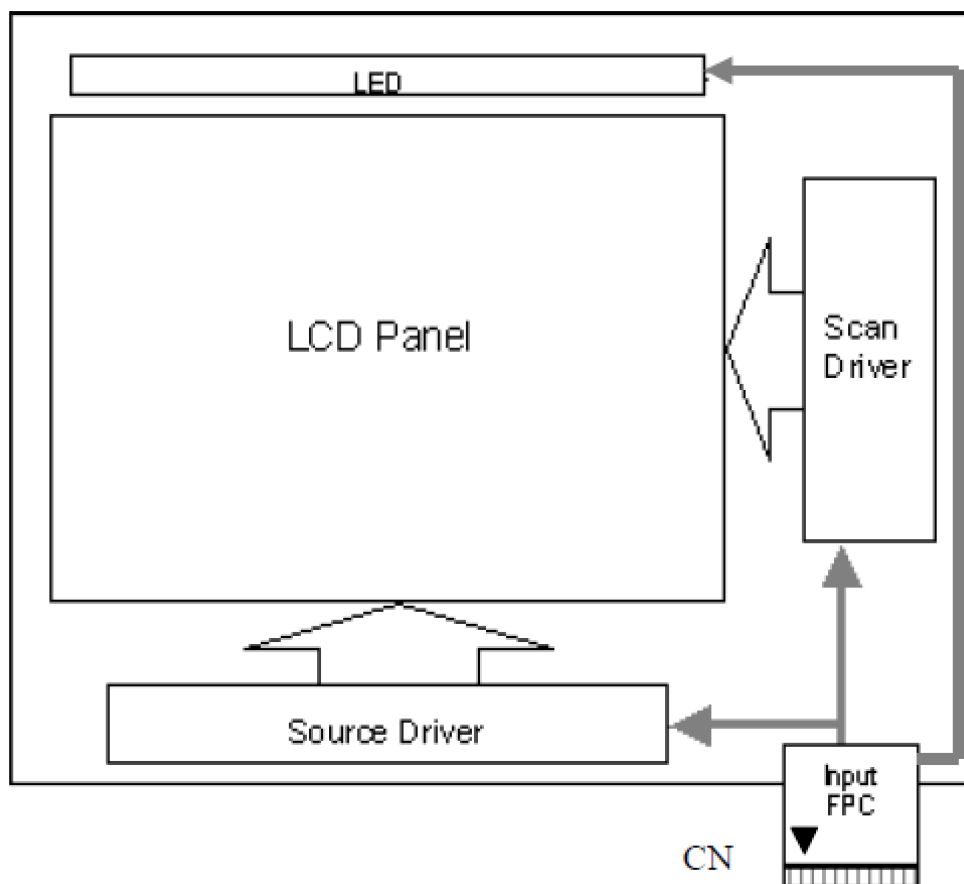
Item	Symbol	MIN	TYP	MAX	Unit	Remark
Backlight forward voltage	V_F	-	9.6	10.6	V	21 LEDs
Backlight forward current	I_F	--	140	--	mA	
LED life time	--	20000	--	--	Hrs	

Note 1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition: $T_a=25\pm3\text{ }^{\circ}\text{C}$, typical I_L value indicated in the above table and the $f_L=50\text{kHz}$ until the brightness becomes less than 50%.



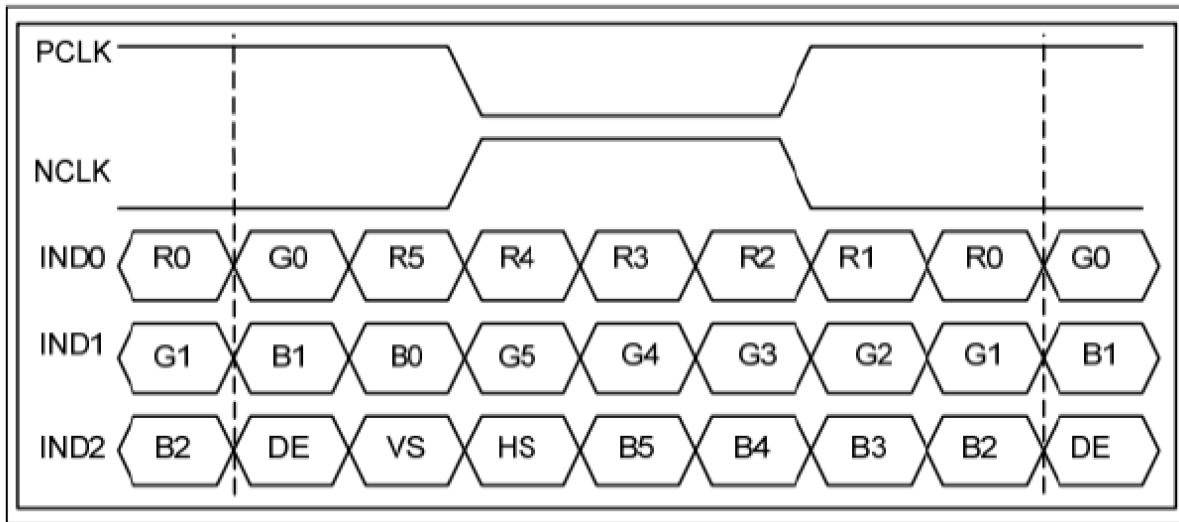
$$3 \times 7 = 21 \text{ LED} \quad 9.0 \text{ V} \quad 7 \times 20 \text{ mA} = 140 \text{ mA}$$

4.3 BLOCK DIAGRAM

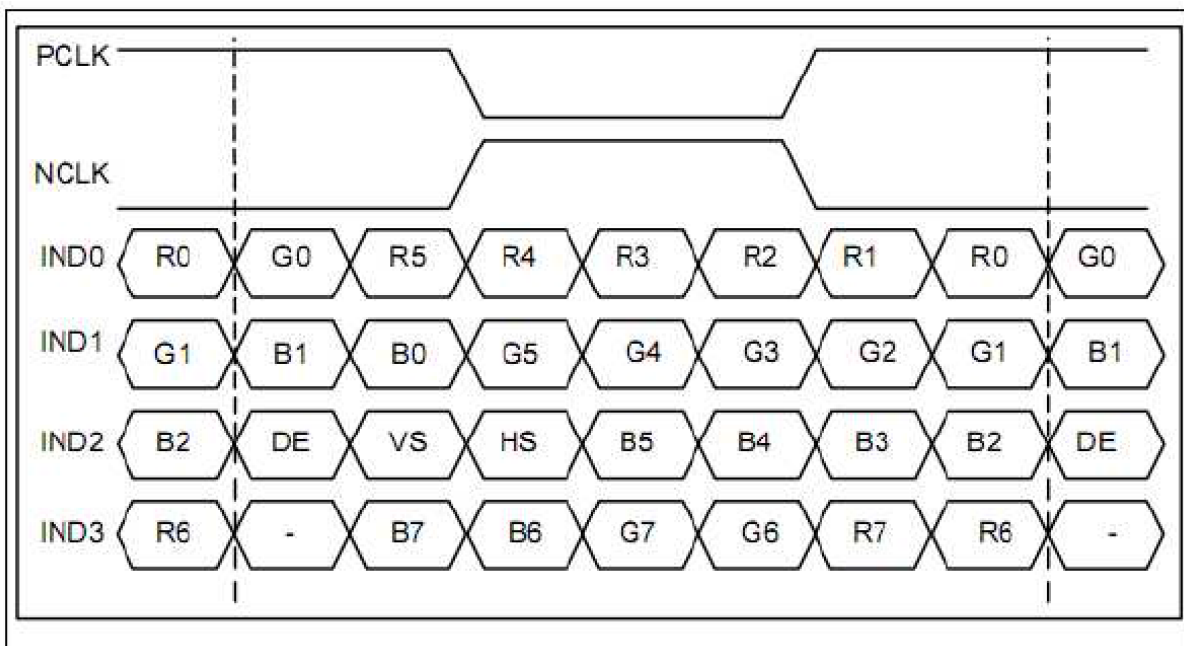


5 Timing Chart

5.1 6bit LVDS input



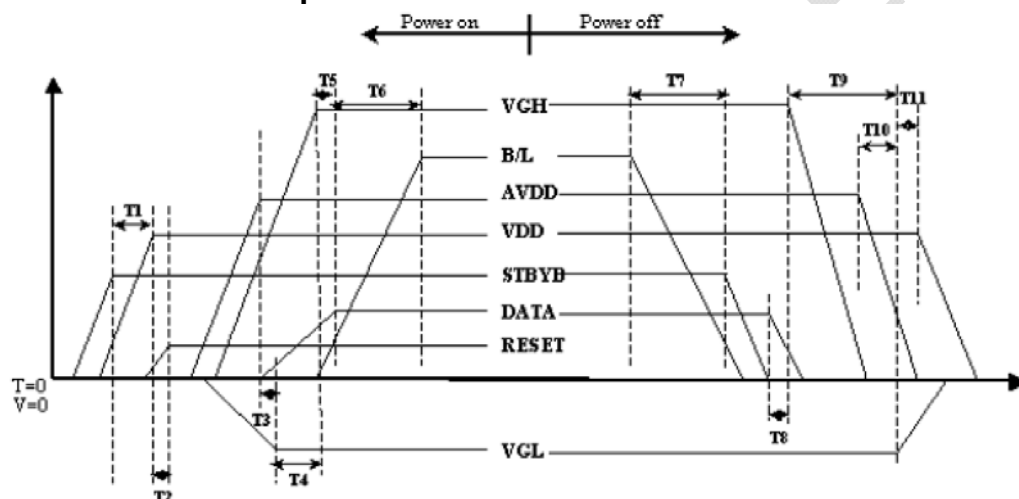
5.2 8bit LVDS input



5.3 Interface Timing (DE mode)

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

5.4 Power On / Off Sequence



Item	Min.	Typ.	Max.	Unit
T1	0	--	--	ms
T2	50	--	--	ms
T3	5	--	--	ms
T4	10	--	--	ms
T5	20	--	--	ms
T6	50	--	--	ms
T7	20	--	--	ms
T8	10	--	--	ms
T9	20	--	--	ms
T10	10	--	--	ms
T11	20	--	--	ms

6 Optical Characteristics

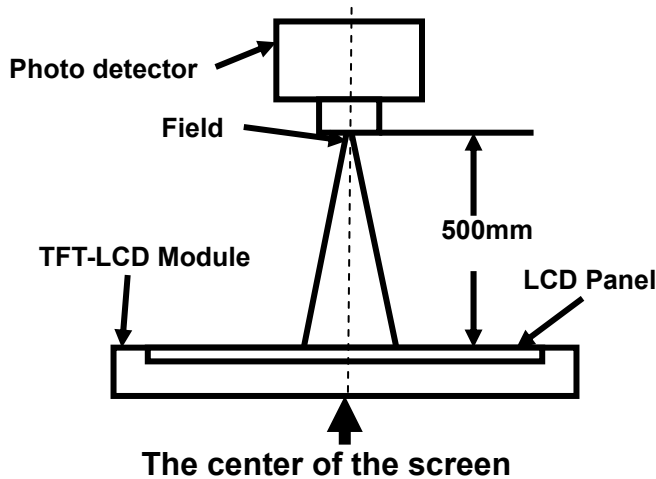
Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≧10	-	80	-	Degree	Note2,3
		θB		-	80	-		
		θL		-	80	-		
		θR		-	80	-		
Contrast Ratio		CR	θ=0°	-	600	-		Note 3
Response Time		T _{ON}	25℃	-	30	-	ms	Note 4
		T _{OFF}						
Chromaticity	White	x	Backlight is on	0.260	0.310	0.360		Note 1,5
		y		0.286	0.336	0.386		
	Red	x						Note 1,5
		y						
	Green	x						Note 1,5
		y						
	Blue	x						Note 1,5
		y						
Uniformity		U		75	80	--	%	Note 6
Luminance		L		250	300	-	cd/m ²	Note 7

Test Conditions:

1. $I_F = 20$ mA, and the ambient temperature is 25℃.
2. The test systems refer to Note 1 and Note 2.

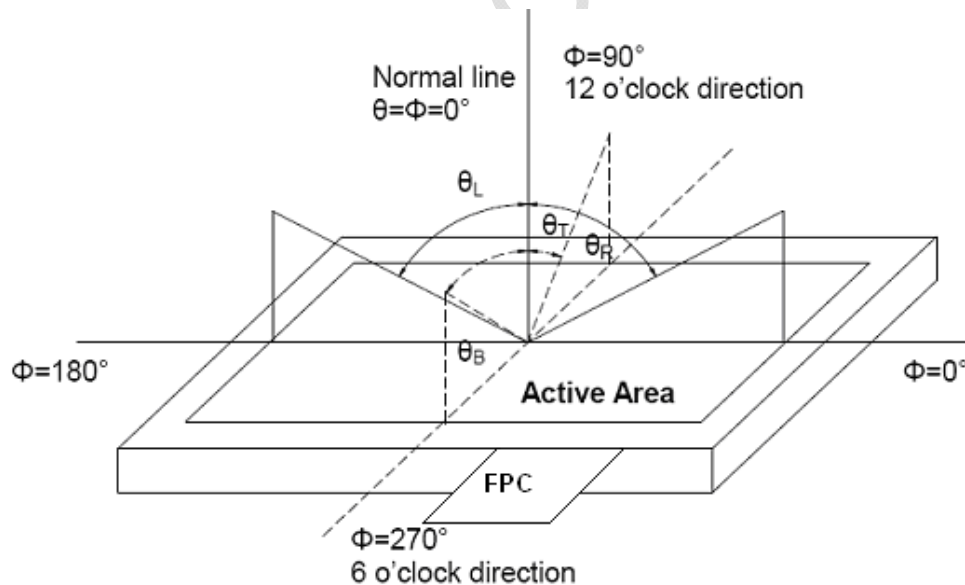
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD.



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

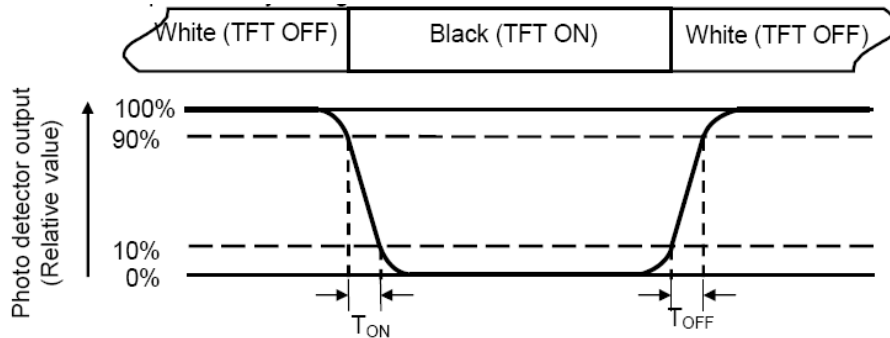
"White state ": The state is that the LCD should drive by V_{white}.

"Black state": The state is that the LCD should drive by V_{black}.

V_{white}: To be determined V_{black}: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

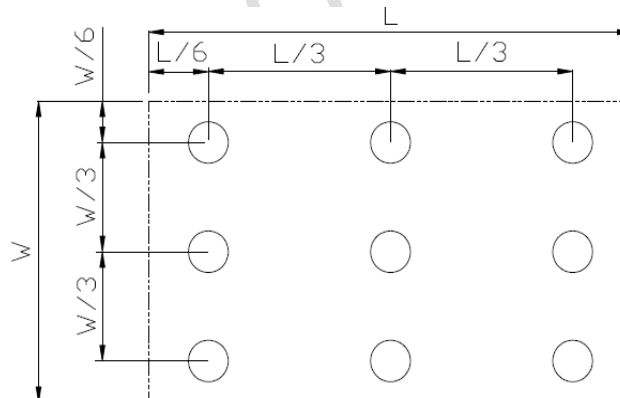
Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width



$L_{\max}$: The measured Maximum luminance of all measurement position.

$L_{\min}$: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	70℃, 120Hours	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	-20℃, 120Hours	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	80℃, 120Hours	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	-30℃, 120Hours	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	40℃, 90%RH, 120Hours	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-20℃(30min)→+70℃(30min), 100 cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984, G B2423.22-2002
7	ESD	Contact: ±4KV Air: ±8KV 150PF/330Ω, 5Points/panel, 5times	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, 2 hours for each direction of X, Y, Z	IEC60068-2-6:1982 GB/T2423.10—1995
10	Package Drop Test	Drop to the ground from 1m height, 1 corner, 3 edges, 6 surfaces.	IEC60068-2-32:1990 GB/T2423.8—1995

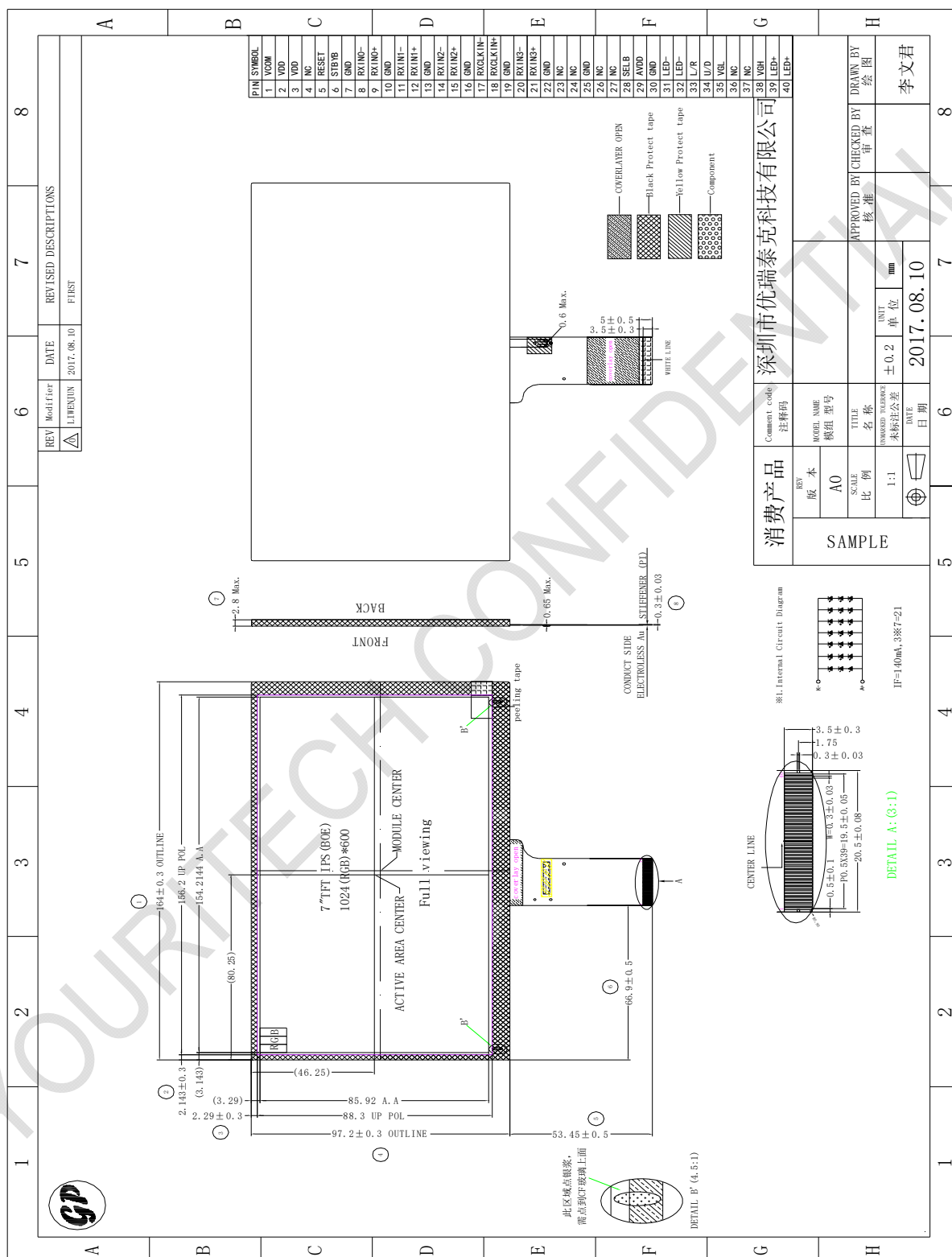
Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

8. Outline Dimension



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9 Packing Drawing

TBD

10 Precautions for Use of LCD Modules

a) Handling Precautions

- i. The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- ii. If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- iii. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- iv. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- v. If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
 - Ketone
 - Aromatic solvents
- vi. Do not attempt to disassemble the LCD Module.
 - vii. If the logic circuit power is off, do not apply the input signals.
 - viii. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

10.1.8.1 Be sure to ground the body when handling the LCD Modules.

10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

b) Storage precautions

- i. When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

- ii. The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%

- iii. The LCD modules should be stored in the room without acid, alkali and harmful gas.

c) Transportation Precautions

- i. The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.