

INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

Integration support

Obsolescence Management

PCAP

Embedded Systems

Human Machine Interface

Touch Solutions

Software

Quality Management

Open Frame Monitors

LCD Controller

Research & Development

Firmware

EMC tests

on-site service

Optical Bonding

In-house Manufacturing

Cover glass

System solutions

OEM Solutions

Custom Display

Production Custom designs Installation

Mechanical design



REVISION HISTORY

Version	Description of Changes	Date	Prepared
0	First Edition	2023-07-18	Aurice



Contents

1. General Description	4
2. Pin Assignment.....	5
3. Operation Specifications	7
3.1 Absolute Maximum Ratings	7
3.2 Electrical Specifications.....	8
3.2.1 TFT-LCD panel driving.....	8
3.2.2 Backlight Driving	9
3.3 Power Sequence.....	10
3.4 Timing Characteristics	12
4. Optical Specifications	15
5. Reliability Test Items	18
6. General Precautions.....	19
7. Mechanical Drawing.....	21
8. EDID Table	22



1. General Description

No.	Item	Specification	Remark
1	LCD size	13.3 inch (Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1920 x 3 (RGB) x1080	
4	Display mode	Normally Black	
5	Pixel pitch	0.153(H) mm x 0.153(V) mm	
6	Active area	293.76(H) mm x 165.24(V) mm	
7	Module size	305.34(W) x 178.82(H) x 3.0(D) mm	Note 1
8	Surface treatment	Anti-Glare,3H	
9	Color arrangement	RGB-stripe	
10	Interface	eDP	
11	Backlight power consumption	7.56W(Typ.)	
12	Weight	230g(Typ.)	

Note 1 : Refer to Mechanical Drawing



2. Pin Assignment

Connector used : 20455-03E-76 (IPEX)

Corresponding connector : 20453-030T (IPEX)

Pin No.	Symbol	I/O	Function	Remark
1	NC	-	No Connection	Note 1
2	H_GND	P	Ground	Note 2
3	Lane 1_N	I	Complement Signal Link Lane 1	
4	Lane 1_P	I	The Signal Link Lane 1	
5	H_GND	P	High Speed Ground	Note 2
6	Lane 0_N	I	Complement Signal Link Lane 0	
7	Lane 0_P	I	The Signal Link Lane 0	
8	H_GND	P	High Speed Ground	Note 2
9	AUX_CH_P	I	The Signal Auxiliary Channel	
10	AUX_CH_N	I	Complement Signal Auxiliary channel	
11	H_GND	P	High Speed Ground	Note 2
12	LCD_VCC	P	LCD Logic and driver power (3.3V)	
13	LCD_VCC	P	LCD Logic and driver power (3.3V)	
14	NC	I	No Connection	Note 1
15	LCD_GND	P	LCD Logic and driver ground	
16	LCD_GND	P	LCD Logic and driver ground	
17	HPD	O	HPD Signal	Note 3
18	BL_GND	P	Backlight ground	
19	BL_GND	P	Backlight ground	
20	BL_GND	P	Backlight ground	
21	BL_GND	P	Backlight ground	
22	BL_ENABLE	I	Backlight on/off	Note 3
23	BL_PWM_DIM	I	System PWM	Note 3
24	NC	-	No Connection	
25	NC	-	No Connection	
26	BL_PWR	P	Backlight power	
27	BL_PWR	P	Backlight power	



28	BL_PWR	P	Backlight power	
29	BL_PWR	P	Backlight power	
30	NC	-	No Connection	

I : input, O : output, P : Power

The shielding case is connected with signal GND

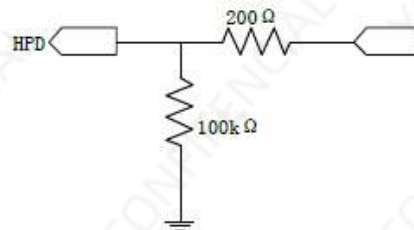
Connector used: 20455-030E-76 (I-PEX)

Corresponding connector: 20453-030T (I-PEX)

Note 1: Don't input any signals or any powers into a NC pin. Keep the NC pin Open.

Note 2: The shielding case is connected with signal GND.

Note 3: The equivalent circuit figure of the terminal.



(RCT is not responsible to its product quality, if the user applies a connector not corresponding to the above model.)

All terminals except NC terminal must be connected to input signal described as above or supply voltage or GND each.



3. Operation Specifications

3.1 Absolute Maximum Ratings

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power Supply Voltage	V _{cc}	3.0	3.6	V	Ta=25℃
Operation Temperature	Top	-20	70	℃	Note 1
Storage Temperature	Tst	-30	80	℃	

Notes :

1. Permanent damage to the device may occur if maximum values are exceeded functional operation should be restricted to the condition described under normal operating conditions.
2. Temperature and relative humidity range : 90% RH Max. (40 ℃ ≥ Ta) Maximum wet - bulb temperature at 39 ℃ or less. (Ta > 40 ℃) No condensation.



3.2 Electrical Specifications

3.2.1 TFT-LCD panel driving

DC Electrical Characteristics						
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Power Supply voltage	V_{CC}	3.0	3.3	3.6	V	
Permissible input ripple voltage	V_{RF}	-	-	100	mVp-p	$V_{DD}=+3.3V$
Power Supply Current	I_{DD}	-	130	145	mA	
Positive-going Input Threshold Voltage	V_{IT+}	-		+100	mV	
Negative-going Input Threshold Voltage	V_{IT-}	-100		-	mV	
Power Consumption	P_D	-	0.45	0.5	W	
	P_{BL}	-	-	7.56	W	
	P_{total}	-	-	8.06	W	

TFT peak current will smaller than Inrush current.

Notes :

1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for 3.3V at 25 °C.(Typ. value for reference)

a) Mosaic pattern 8*8



(a)



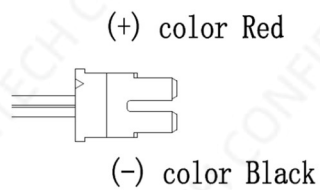
3.2.2 Backlight Driving

The backlight system is an edge-lighting type with white-LED.

(It is usually required to measure under the following condition: $T_a=25^{\circ}\text{C}\pm 2^{\circ}\text{C}$)

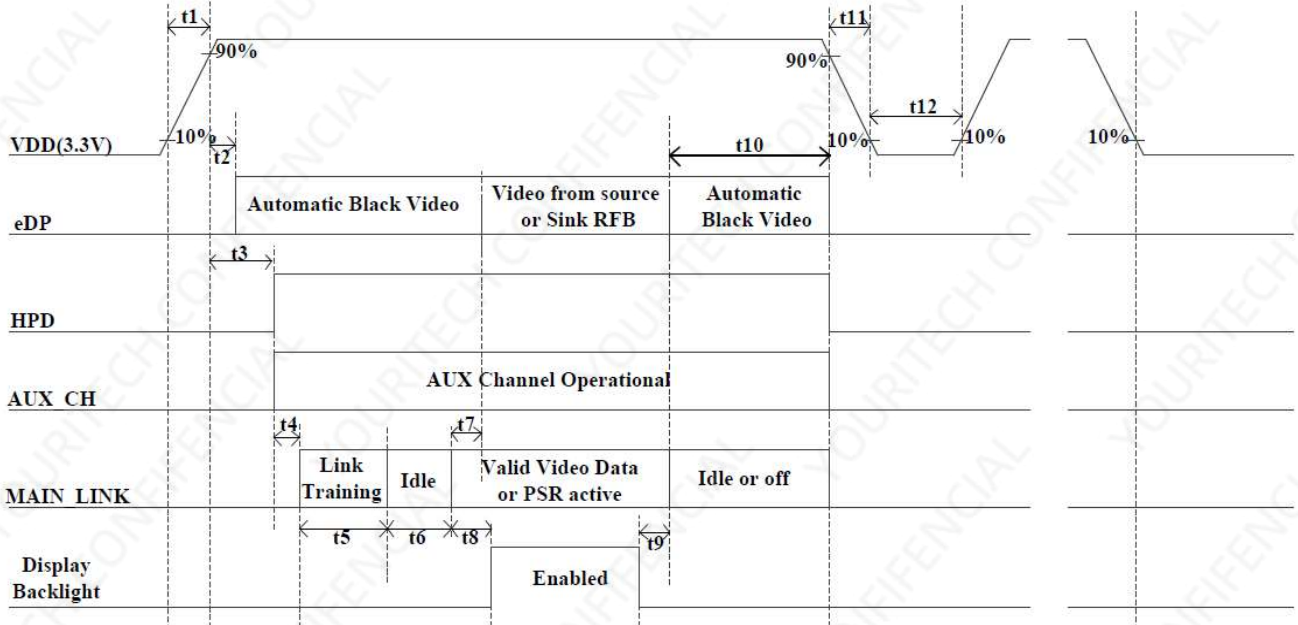
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Supply voltage	V_{BL}	-	30	31.5	V	BL_PWR
LED Forward Current	I_{BL}	-	240	-	mA	
LED Power consumption	P_{BL}	-	-	7.56	W	
LED Lifetime	N/A	30,000	-	-	Hour	

Connector JST BHSR-02VS-1



3.3 Power Sequence

On-Off conditions for supply voltage



Note: Do not keep the interface signal high-impedance or unusual signal when power is on.

Symbol	Min.	Max.	Unit	Remark
t1	0.5	10	ms	
t2	0	200	ms	
t3	0	100	ms	
t4	-	-	ms	
t5	-	-	ms	
t6	-	-	ms	
t7	0	50	ms	
t8	-	-	ms	
t9	-	-	ms	
t10	0	500	ms	
t11	1	50	ms	Note 1
t12	500	-	ms	

Note 1: As for the power off sequence for VDD (t11), be sure to keep above mentioned timing.

If the VDD power off sequence timing is other than shown above, LCD may cause permanent damage.

*1 : As for the power sequence for backlight, it is recommended to apply above mentioned input timing.

If the backlight is light on and off at a timing other than shown above, displaying image maybe get disturbed.



VDD-dip-conditions

1) $2.5\text{ V} \leq \text{VDD} < 3.0\text{ V}$

$t_d \leq 10\text{ ms}$

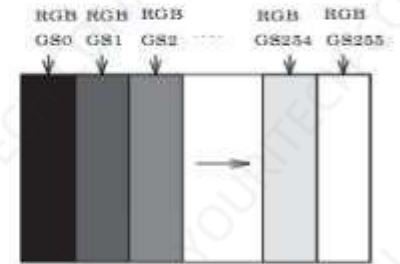
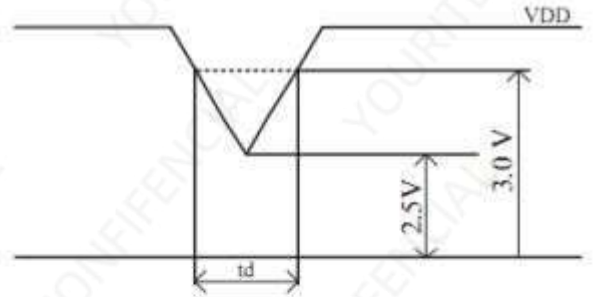
Under above condition, the display image should return to an appropriate figure after VDD voltage recovers.

2) $\text{VDD} < 2.5\text{ V}$

VDD-dip conditions should also follow the ON-OFF conditions for supply voltage.

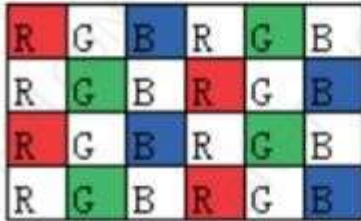
Typical current condition: 16-gray-bar pattern.

$\text{VDD} = +3.3\text{ V}$

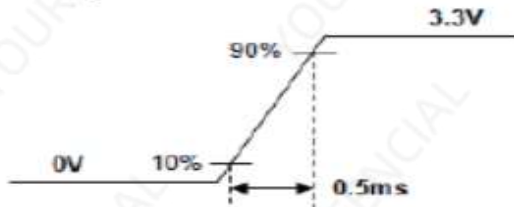


Maximum current condition: Vertical stripe pattern.

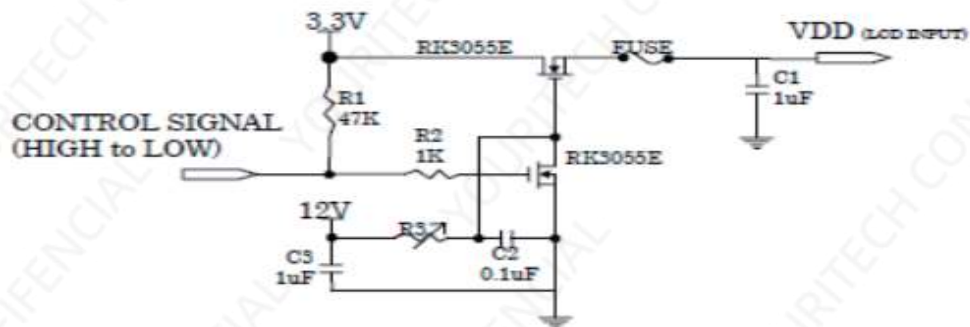
$\text{VDD} = +3.3\text{ V}$



• Rising time VDD :

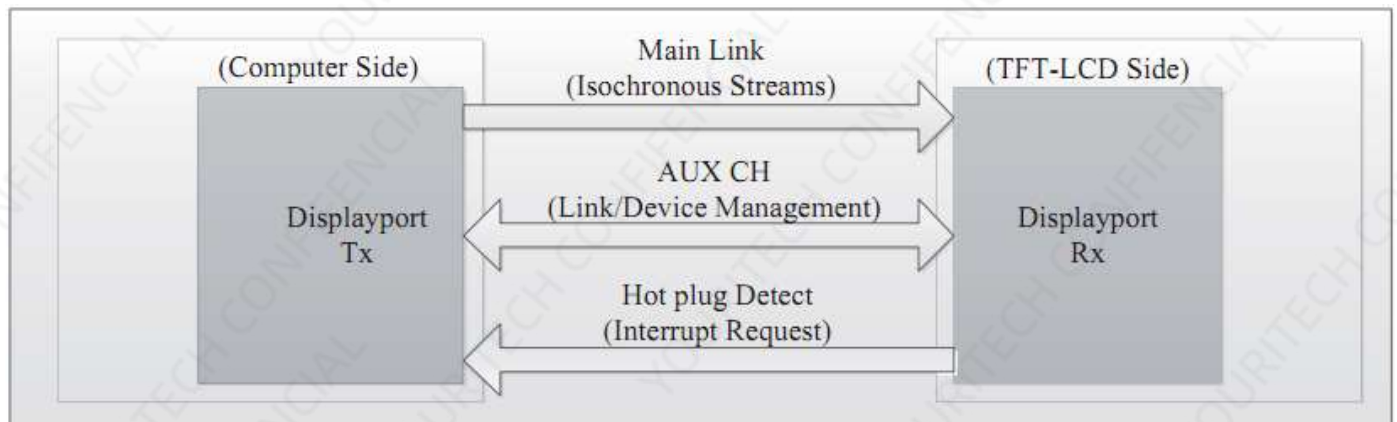


• measurement condition :



3.4 Timing Characteristics

3.4.1 eDP Interface



Note. Transmitter : HX8876-F04 or equivalent.
Transmitter is not contained in Module.

3.4.2 eDP Input Signal

Lane 0	Lane 1
R0-7:0	R1-7:0
G0-7:0	G1-7:0
B0-7:0	B1-7:0
R2-7:0	R3-7:0
G2-7:0	G3-7:0
B2-7:0	B3-7:0
R4-7:0	R5-7:0
G4-7:0	G5-7:0
B4-7:0	B5-7:0



3.4.3 Input signal, basic display colors&gray scale of colors

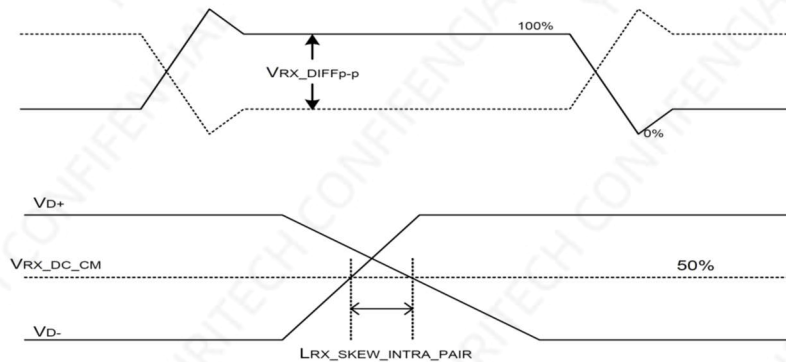
Color & Gray Scale		RED DATA								GREEN DATA								BLUE DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of RED	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of GREEN	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of BLUE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Gray Scale of WHITE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	▽	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1



3.4.4 eDP Rx Interface Timing parameter

The Specification of the eDP Rx interface timing parameter is shown as below table.

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock	SSC		0.5		%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	500	0	1000	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	-	V	
Differential termination resistance	R _{RX-DIFF}	80	-	100	Ω	
Single-ended termination resistance	R _{RX-SE}	40	-	60	Ω	
Rx short circuit current limit	I _{RX_SHORT}	-	-	20	mA	
Intra-pair skew at Rx package pins (HBR) Rx intra-pair skew tolerance at HBR	L _{RX_SKEW_INTRA_PAIR}	-	-	150	ps	



3.4.5 Timing Condition

This model is operated by the DE only.

Parameter		Symbol	Values			Unit
			Min.	Typ.	Max.	
Clock frequency		1 / T _c	-	138.66	-	MHz
Frame Period			-	60	-	Hz
Vertical Section	Period	T _v	1103	1111	1200	lines
	Active	T _{VD}	1080			lines
	Blanking	T _{VB}	-	31	-	lines
Horizontal Section	Period	T _H	2020	2080	2400	clocks
	Active	T _{HD}	1920			clocks
	Blanking	T _{HB}	-	160	-	clocks



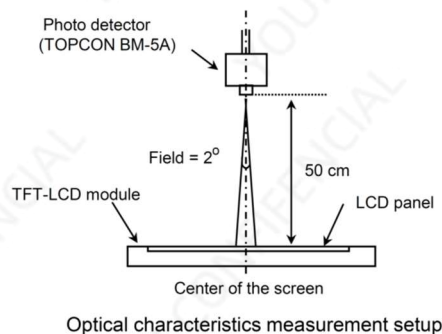
4. Optical Specifications

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle	Horizontal	Θ21	CR>10	80	89	-	Deg.	Note 1
		Θ22		80	89	-	Deg.	
	Vertical	Θ11		80	89	-	Deg.	
		Θ12		80	89	-	Deg.	
Response time		TOn+T Off	Θ=0° (Center)	-	25	30	ms	Note 2 Note 3
Contrast ratio		CR		960	1200	-	-	Note 2 Note 4
Color chromaticity		Wx		0.241	0.281	0.321	-	Note 2 Note 5 Note 6
		Wy		0.260	0.300	0.340		
		Rx		0.532	0.572	0.612		
		Ry		0.315	0.355	0.395		
		Gx		0.271	0.311	0.351		
		Gy		0.520	0.560	0.600		
		Bx		0.116	0.156	0.196		
		By		0.060	0.100	0.140	-	
Luminance (AA Center)		L		1400	1600	-	cd/m2	
NTSC ratio		%		42	45	-		
Luminance uniformity 13pt		Yu		70	75	-	%	

Test Conditions: The ambient temperature is 25°C.

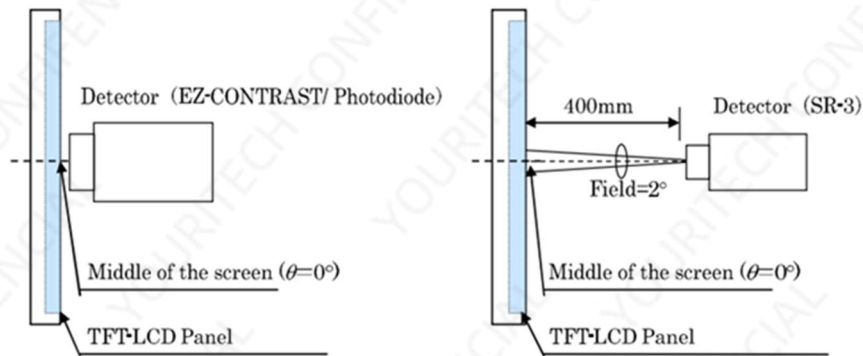
Note1: Definition of viewing angle range

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface



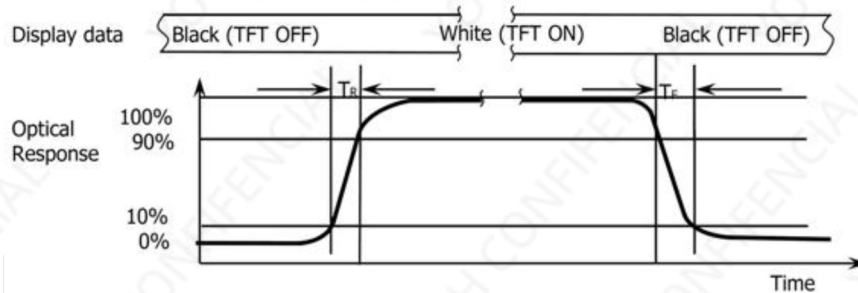
Note2: Definition of optical measurement system

The optical characteristic should be measured in darkroom. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by EZ-CONTRASE, Response time is measured by Photodiode. The other items are measured by SR-3Field of view: 2°/Height: 400mm.)



Note3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_R) is the time between photo detector output intensity changed from 10% to 90%. And fall time (T_F) is the time between photo detector output intensity changed from 90% to 10%.



Note4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

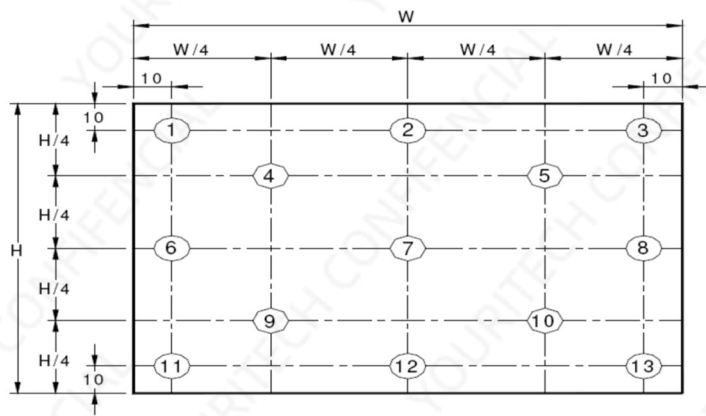
Note5: Definition of color chromaticity(CIE1931)

Color coordinates measured at center point of LCD.



Note6: Definition of Luminance Uniformity(“White” state)

Active area is divided in to 13 measuring areas.(1~13)



Every measuring point is placed at the center of each measuring area.

$$(13pt) Yu = \frac{\text{Maximum Luminance of 13 points (Brightness)}}{\text{Minimum Luminance of 13 points (Brightness)}}$$



5. Reliability Test Items

No.	Item	Test Conditions
1	High Temperature Storage	Ta=80°C, 240hrs
2	Low Temperature Storage	Ta=-30°C, 240hrs
3	High Temperature Operation	Ts=70°C, 240hrs
4	Low Temperature Operation	Ta=-20°C, 240hrs
5	Operate at High Temperature and Humidity	Ta=60°C, 90%RH, 240hrs
6	Thermal Shock	-30°C~+80°C 1hr/cycle 100 cycles

Result Evaluation Criteria: Under the display quality test condition with normal operation state.

Do not change the condition as such changes may affect practical display function.

Normal operation state temperature: 15~35°C, Humidity: 45~75%, Atmospheric pressure: 86~106kPa



6. General Precautions

1 Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.
8. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
9. Pins of I/F connector shall not be touched directly with bare hands.

2 Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

3 Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.
3. Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
4. In handling the LCD, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary.

4 Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.
4. It is not allowed to store or run directly in strong light or in high temperature and humidity for a long time; Strong light exposure causes degradation of polarizer and color filter.

5 Operation

1. Do not connect, disconnect the module in the "Power On" condition.
2. Power supply should always be turned on/off by following item "POWER ON OFF SEQUENCE".
3. Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
4. The FPC cable between the LED chips and its converter power supply shall be a minimized length and be connected directly. The longer cable between the back-light and the converter may cause lower luminance of light source (LED).



6 Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft cloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

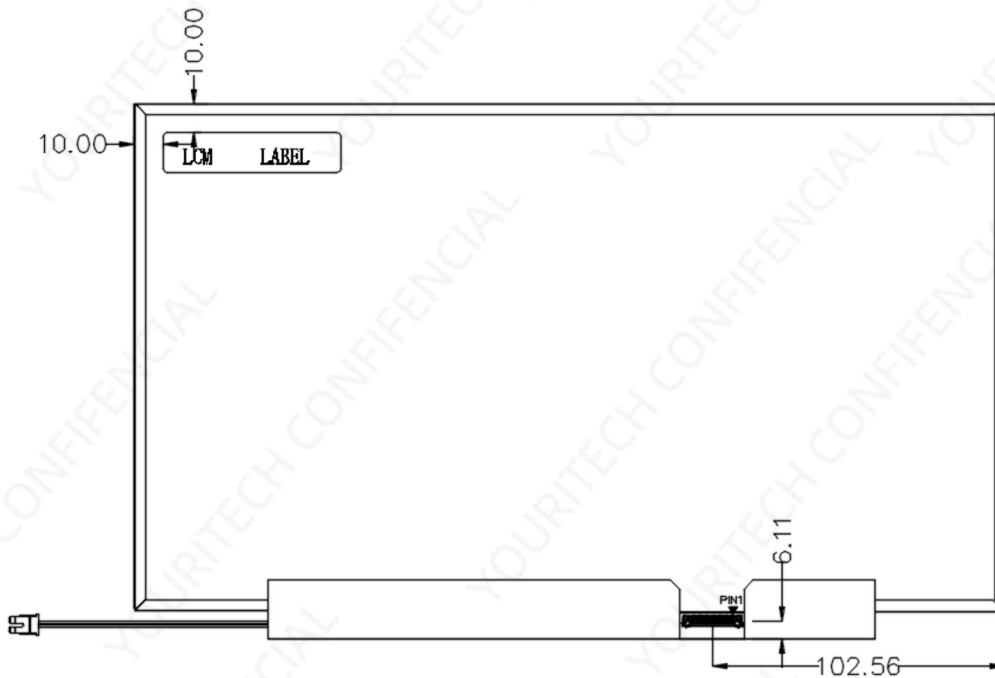
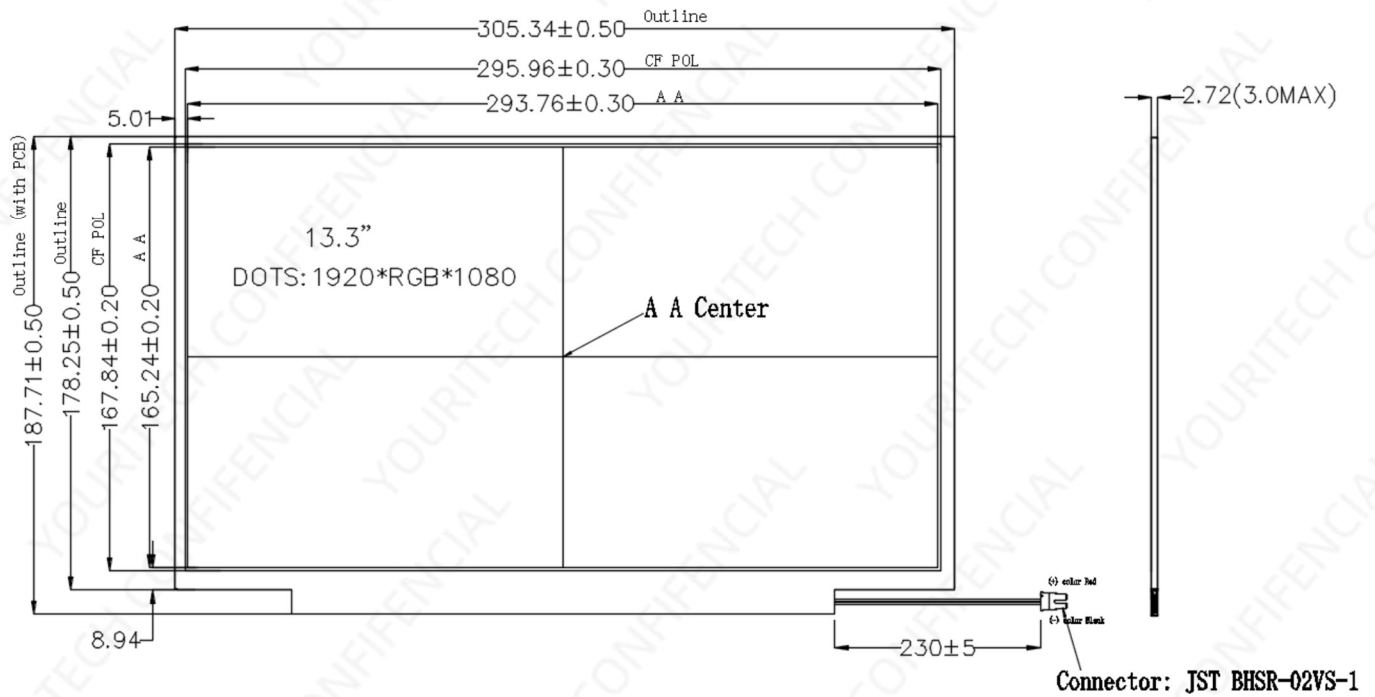
7 Other

1. Ultra-violet ray filter is necessary for outdoor operation.
2. Avoid condensation of water. It may result in improper operation or disconnection of electrode.
3. Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, Variation in part contents and environmental temperature, so on) Otherwise the module may be damaged.
4. If the module displays the same pattern continuously for a long period of time, it can be the situation when the image "sticks" to the screen.
5. This module has its circuitry PCB's on the front or rear side and should be handled carefully in order not to be stressed.
6. The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.



7. Mechanical Drawing

Unit : mm



8. EDID Table

Address (HEX)	Function	HEX	DEC	Input Values	Notes
00	Header	00	0	0	EDID Header
01		FF	255	255	
02		FF	255	255	
03		FF	255	255	
04		FF	255	255	
05		FF	255	255	
06		FF	255	255	
07		00	0	0	
08	ID Manufacturer Name	09	09	BOE	ID=BOE
09		E5	229		
0A	ID Product Code	A1	161		ID=2721
0B		0A	10		
0C	32-bits Serial No.	00	0		
0D		00	0		
0E		00	0		
0F		00	0		
10	Week of Manufacture	1E	30	30	
11	Year of Manufacture	1F	21	2021	Manufacture in 2021
12	EDID Structure Ver.	01	1	1	EDID Ver. 1.0
13	EDID Revision#	04	4	4	EDID Rev. 0.4
14	Video Input Definition	A5	165		Refer to right table
15	Max H Image Size	1D	29		29
16	Max V Image Size	11	17		17
17	Display Gamma	78	120		2.2
18	Feature Support	02	2		-
19	Red/Green Low Bits	39	57		-
1A	Blue/White Low Bits	B0	176		-
1B	Red x High Bits	A6	166	0.649	0.649
1C	Red y High Bits	54	84	0.328	0.328
1D	Green x High Bits	4E	78	0.307	0.307
1E	Green y High Bits	A0	160	0.626	0.626
1F	Blue x High Bits	26	38	0.151	0.151
20	Blue y High Bits	0F	15	0.062	0.062
21	White x High Bits	50	80	0.313	0.313
22	White y High Bits	54	84	0.329	0.329
23	Established Timing 1	00	0		Refer to right table
24	Established Timing 2	00	0		
25	Established Timing 3	00	0		
26	Standard Timing #1	01	1		Not Used
27		01	1		
28	Standard Timing #2	01	1		Not Used
29		01	1		
2A	Standard Timing #3	01	1		Not Used
2B		01	1		
2C	Standard Timing #4	01	1		Not Used
2D		01	1		
2E	Standard Timing #5	01	1		Not Used



2F		01	1		
30	Standard Timing #6	01	1		Not Used
31		01	1		
32	Standard Timing #7	01	1		Not Used
33		01	1		
34	Standard Timing #8	01	1		Not Used
35		01	1		
36	Detailed Timing/Monitor Descriptor #1	2A	42	138.7	138.66MHz Main clock
37		36	54		
38		80	128	1920	Hor Active = 1920
39		A0	160	160	Hor Blanking = 160
3A		70	112	-	4 bits of Hor. Active + 4 bits of Hor. Blanking
3B		38	56	1080	Ver Active = 1080
3C		1F	31	31	Ver Blanking = 31
3D		40	64	-	4 bits of Ver. Active + 4 bits of Ver. Blanking
3E		30	48	48	Hor Sync Offset = 48
3F		20	32	32	H Sync Pulse Width = 32
40		35	53	3	V sync Offset = 3 line
41		00	0	5	V Sync Pulse width : 5 line
42		26	38	294	Horizontal Image Size = 294 mm (Low 8 bits)
43		A5	165	165	Vertical Image Size = 165 mm (Low 8 bits)
44		10	16	-	4 bits of Hor Image Size + 4 bits of Ver Image Size
45		00	0	0	Hor Border (pixels)
46		00	0	0	Vertical Border (Lines)
47		1A	26	-	Refer to right table
48	Detailed Timing/Monitor Descriptor #2	00	0		
49		00	0		
4A		00	0		
4B		00	0		
4C		00	0		
4D		00	0		
4E		00	0		
4F		00	0		
50		00	0		
51		00	0		
52		00	0		
53		00	0		
54	Detailed Timing/Monitor Descriptor #2	00	0		
55		00	0		
56		00	0		
57		00	0	0	
58		00	0	0	
59		1A	26		Refer to right above table
5A		00	0		
5B		00	0		



5C	Detailed Timing/Monitor Descriptor #3	00	0		ASCII Data Sting Tag
5D		FE	254		
5E		00	0		
5F		42	66	B	Manufacture name : BOE18
60		4F	79	O	
61		45	69	E	
62		20	32		
63		31	49	1	
64		38	56	8	
65		0A	10		
66		20	32		
67		20	32		
68		20	32		
69		20	32		
6A		20	32		
6B		20	32		
6C	Detailed Timing/Monitor Descriptor #4	00	0		Product Name Tag (ASCII)
6D		00	0		
6E		00	0		
6F		FE	254		
70		00	0		
71		47	71	G	Model name : GE133FHB-N50
72		45	69	E	
73		31	49	1	
74		33	51	3	
75		33	51	3	
76		46	70	F	
77		48	72	H	
78		42	66	B	
79		2D	45	-	
7A		4E	78	N	
7B		35	53	5	
7C		30	48	0	
7D		0A	10		
7E	Extension Flag	00	0	1	0 : 1 個 EDID ; N-1 : N 个 EDID
7F	Checksum	07	7		

