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Mechanical design



SPECIFICATION FOR LCM Module

MODULE No:	ET185FH20-K
CUSTOMER:	

YOURITECH	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		



[illegible]

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1.Basic Specifications

* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 18.5 " TFT-LCD contains 1920x1080 pixels, and can display up to 16.7M colors.

1.1 TFT Features

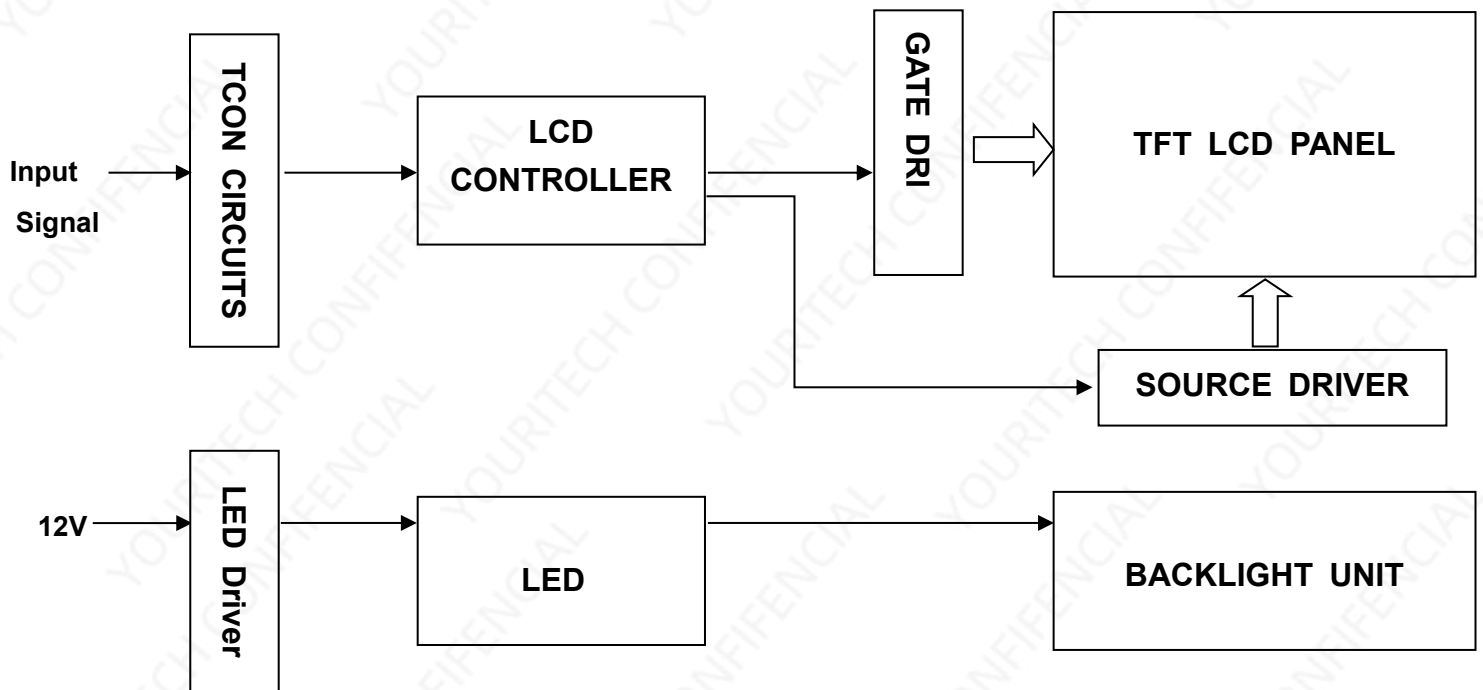
General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	408.96(H)*230.04(V) (18.5inch)	mm	
Driver element	TFT active matrix	-	
Display colors	16.7M	colors	
Number of pixels	1920(RGB)*1080	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.213(H)*0.213(V)	mm	
Viewing angle	Free	o'clock	
LCM Interface	Dual LVDS	-	
Display mode	Normally Black	-	
Operating temperature	-30~+80	°C	
Storage temperature	-30~+80	°C	

1.2 Mechanical Information

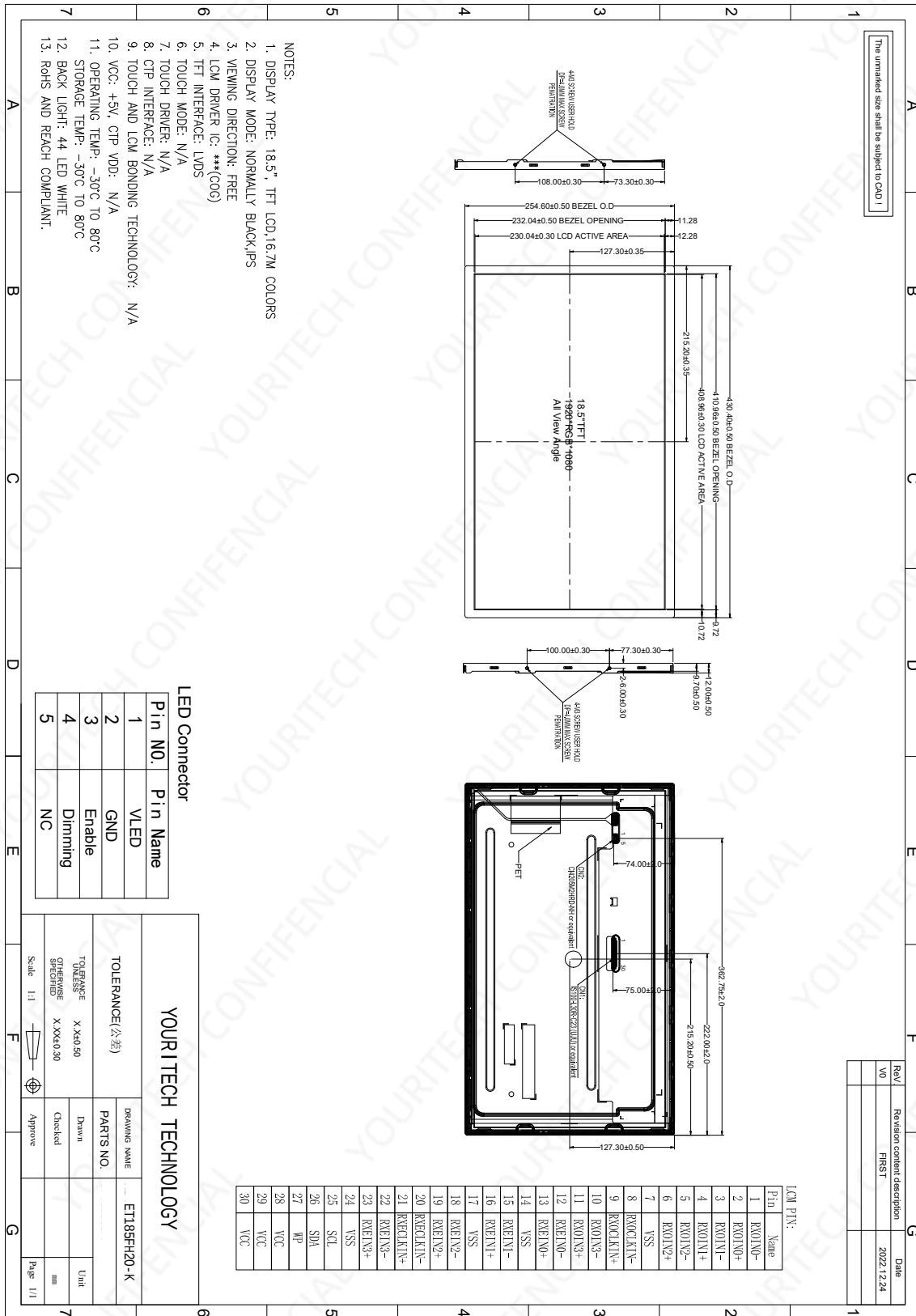
Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	430.40	-	mm	
	Vertical(V)	-	254.60	-	mm	
	Depth(D)	-	12	-	mm	
Weight		-	1800	-	g	



2. Block Diagram



3. Outline dimension



4. Input terminal Pin Assignment

4.1 TFT Pin Assignment

The electronics interface connector IS100-L30R-C23 (UJU) or equivalent.

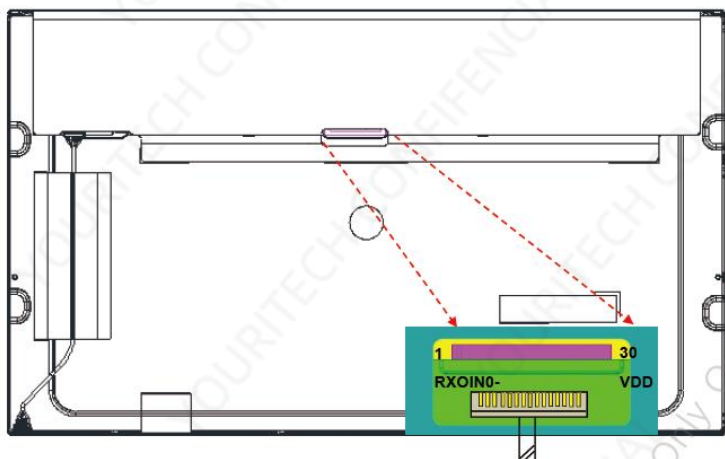
The LED connector is CI4205M2HRD-NH or equivalent.

NO.	SYMBOL	DISCRIPTIONs	I/O
1	RXOIN0-	Negative LVDS differential data input (Odd data)	I
2	RXOIN0+	Positive LVDS differential data input (Odd data)	I
3	RXOIN1-	Negative LVDS differential data input (Odd data)	I
4	RXOIN1+	Positive LVDS differential data input (Odd data)	I
5	RXOIN2-	Negative LVDS differential data input (Odd data)	I
6	RXOIN2+	Positive LVDS differential data input (Odd data)	I
7	VSS	Power Ground	P
8	RXOCLKIN-	Negative LVDS differential clock input (Odd clock)	I
9	RXOCLKIN+	Positive LVDS differential clock input (Odd clock)	I
10	RXOIN3-	Negative LVDS differential data input (Odd data)	I
11	RXOIN3+	Positive LVDS differential data input (Odd data)	I
12	RXEIN0-	Negative LVDS differential data input (Even data)	I
13	RXEIN0+	Positive LVDS differential data input (Even data)	I
14	VSS	Power Ground	P
15	RXEIN1-	Negative LVDS differential data input (Even data)	I
16	RXEIN1+	Positive LVDS differential data input (Even data)	I
17	VSS	Power Ground	P
18	RXEIN2-	Negative LVDS differential data input (Even data)	I
19	RXEIN2+	Positive LVDS differential data input (Even data)	I
20	RXECLKIN-	Negative LVDS differential clock input (Even clock)	I
21	RXECLKIN+	Positive LVDS differential clock input (Even clock)	I



22	RXEIN3-	Negative LVDS differential data input (Even data)	I
23	RXEIN3+	Positive LVDS differential data input (Even data)	I
24	VSS	Power Ground	P
25	SCL	I2C BUS(Only YOURITECH use)	
26	SDA	I2C BUS(Only YOURITECH use)	
27	WP	EEPROM write EN(Only YOURITECH use)	
28	VCC	+5.0V Power Supply	P
29	VCC	+5.0V Power Supply	P
30	VCC	+5.0V Power Supply	P

Note1: Start from left side



Note2: Input signals of clock shall be the same timing.

4.2 LED Driver Connector Pin Assignment

NO.	SYMBOL	DISCRIPTION	I/O
1	VLED	Supply voltage(12V).	P
2	GND	Ground.	P
3	Enable	5V-On / 0V-Off	I
4	Dimming	PWM Dimming	I
5	NC	NC	



5. LCD Optical Characteristics

5.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio		CR	$\Theta=0$ Normal viewing angle	900	1200	--		(1)(2)
Response time	Rising	T_R+T_F		--	30	35	msec	(1)(3)
	Falling							
Color Gamut		S(%)		65	70	--	%	
Luminance				280	350	--	cd/m ₂	(5)
Luminance Uniformity				80	85	--	%	(6)
Color Filter Chromaticity	White	W_X		0.2368	0.286	0.336		(1)(4) Active area
		W_Y		0.246	0.296	0.346		
	Red	R_X		0.583	0.633	0.683		
		R_Y		0.293	0.343	0.393		
	Green	G_X		0.248	0.298	0.348		
		G_Y		0.540	0.590	0.640		
	Blue	B_X		0.098	0.148	0.198		
		B_Y		0.019	0.069	0.119		
Viewing angle	Hor.	Θ_L	CR>10	80	85	--		(1)(4)
		Θ_R		80	85	--		
	Ver.	Θ_U		80	85	--		
		Θ_D		80	85	--		
Option View Direction		Free						

Measuring Condition

Measuring surrounding : dark room

Ambient temperature : 25±2°C

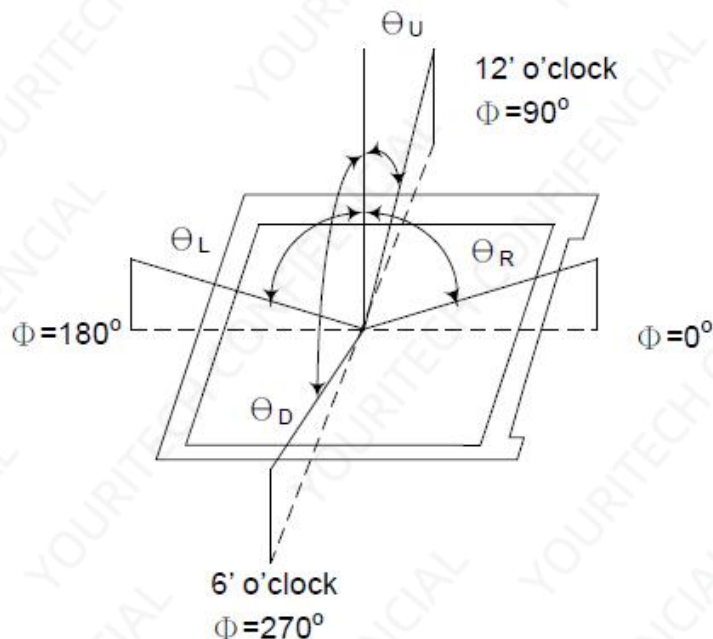
15min. warm-up time.

Measuring Equipment

FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.



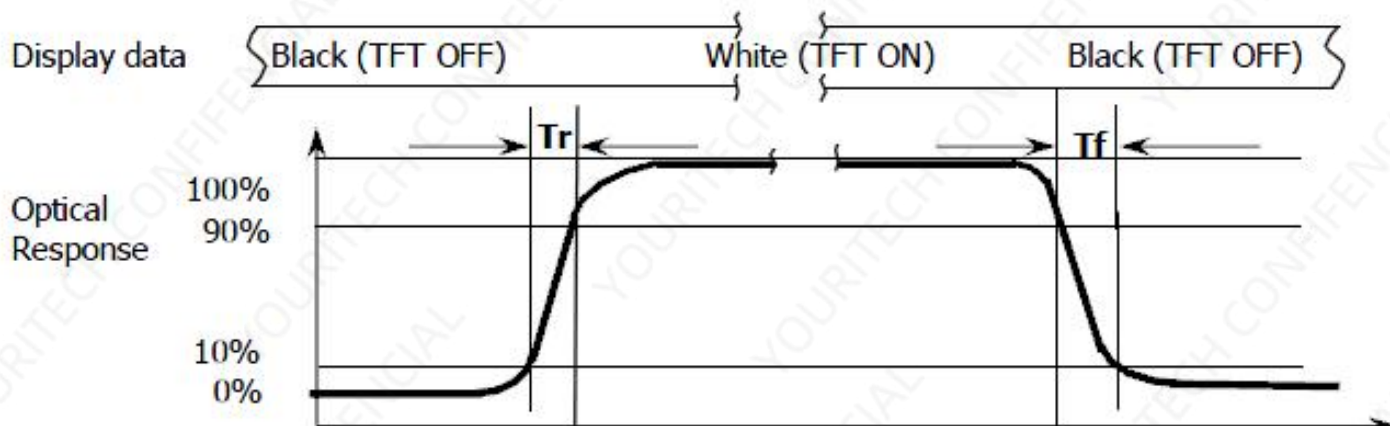
Note (1): Definition of Viewing Angle :



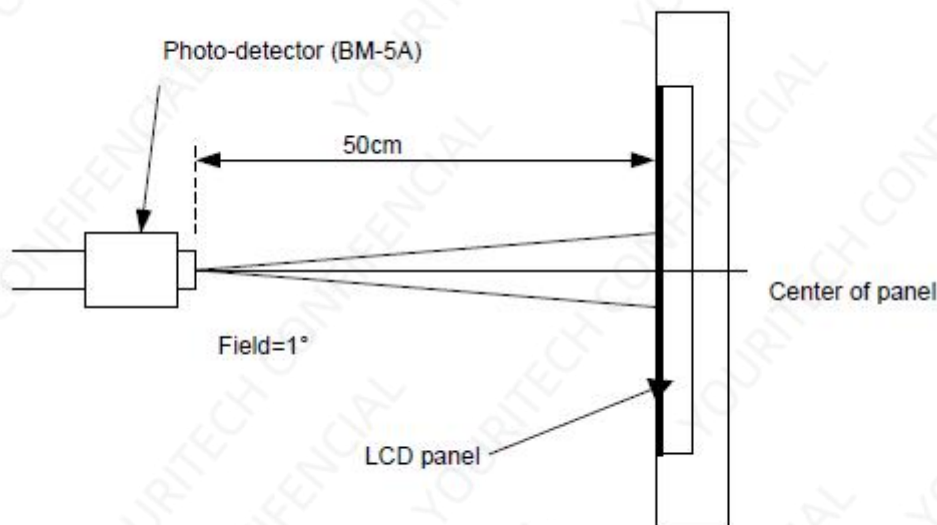
Note (2): Definition of Contrast Ratio(CR) :measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3): Response Time

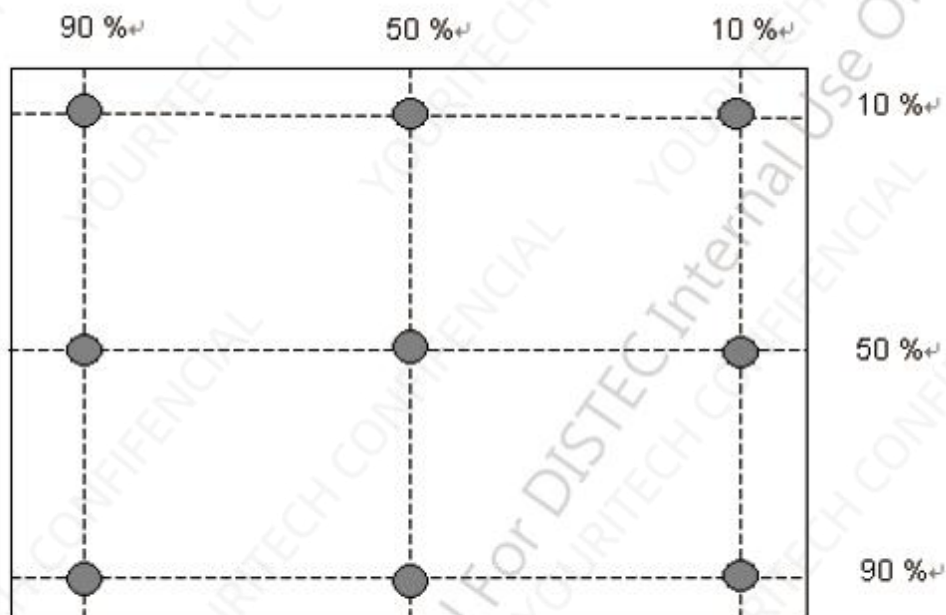


Note (4): Definition of optical measurement setup



Note 5: Central luminance is measured by CA310

Note 6: Luminance uniformity of these 9 points is defined as below and measured by CA310



$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points (1 - 9)}}{\text{Maximum Luminance in 9 Points (1 - 9)}}$$



6. Electrical Characteristics

6.1 Absolute Maximum Rating

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table

<Environment Absolute Maximum Ratings> [Ta =25±2 °C]

Parameter	Symbol	Min.	Type.	Max.	Unit	Remarks
Back-light Power Supply Voltage	HV _{DDOUT}	-	33V	-	V	Ta = 25 °C Note 1&2
Back-light LED Current	I _{HVDD}	-	300	360	mA	
Back-light LED Reverse Voltage	V _R	-	-	-	V	
Power Supply Input Voltage	VCC	3.5	5	5.8	V	

Note:

1. These range above is maximum value not the actual operating temperature . Actual Operating temperature is no more than 40°C and temperature refers to the LCM surface temperature ;
- 2.YOURITECH is not responsible for product problems beyond the use conditions.
- 3.When the ambient temperature is T °C, the surface temperature of Panel can not exceed (T+15)°C.



7. ELECTRICAL SPECIFICATIONS

7.1 TFT LCD Module

< LCD Module Electrical Specifications >

[Ta =25±2 °C]

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
Power Supply Input Voltage	V _{DD}	4	5	5.8	V	Note 1
Power Supply Current	I _{DD}	-	500	-	mA	
LED Driver Power Supply Voltage	H _{VDD}	10.8	12	12.6	V	Note 2
LED Driver Power Supply Current	I _{HVDD}	-	1000	-	mA	
LED Power Consumption	P _{LED}	-	12	-	W	
Positive-going Input Threshold Voltage	V _{IT+}	-		+100	mV	V _{com} = 1.2V typ.
Negative-going Input Threshold Voltage	V _{IT-}	-100		-	mV	
Differential input common mode voltage	V _{com}		1.2		V	V _{IH} =100mV, V _{IL} =-100mV

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for 5V at 25 °C
Max value at Black Pattern

2. Calculated value for reference $I_{LED} \times V_{LED} \div 0.85 = P_{LED}$



7.2 Back-light Unit

<LED Driving guideline specifications >

Ta=25+/-2°C

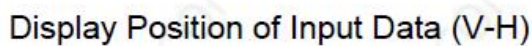
Parameter		Min.	Typ.	Max.	Unit	Remarks
Power supply voltage for Back light			33		V	
Power supply Current for Back light		-	300	-	mA	
Power supply for Back light		-	10	-	W	Note 1
EN Control Level	Backlight on	2.5	-	-	V	EN logic high voltage
	Backlight off	-	-	0.8	V	EN logic low voltage
PWM Control Level	PWM High Level	2.5	-	-	V	
	PWM Low Level	-	-	0.8	V	
PWM Control Frequency		0.15	-	1	KHz	
Duty Ratio		-	5	-	100	%
Life-time		-	30000	-	Hrs	Note 2

Notes : 1. Calculator Value for reference $I_{LED} \times V_{LED} = P_{LED}$

2. The LED Life-time define as the estimated time to 50% degradation of initial luminous under the condition of the ambient temperature of 25°C.



Pixel Format



Scan direction



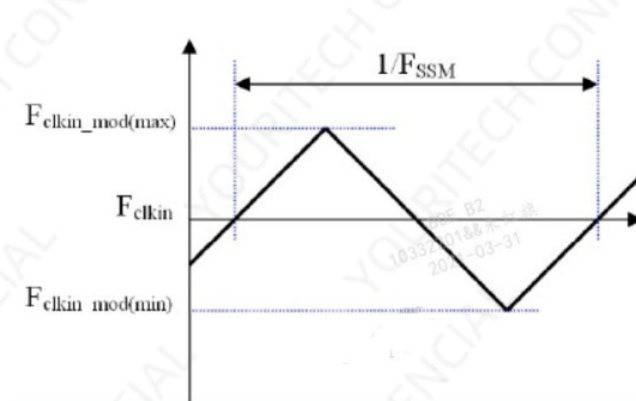
8. SIGNAL TIMING SPECIFICATION

8.1 The ET185FH20-K is operated by the DE only.

Item	Symbols		Min	Typ	Max	Unit	Note
DCLK	Period	tCLK	9.5	-	25	ns	Note 1
	Frequency	-	40	-	105	MHz	
Hsync	Period	tHP	990	1040	-	tCLK	
	Horizontal Valid	tHV	-	960	-	tCLK	
	Horizontal Blank	tHB	30	80	-	tCLK	
Vsync	Period	tVP	1100	1125	1138	tHP	
	Vertical Valid	tVV	-	1080	-	tHP	
	Vertical Blank	tVB	30	45	58	tHP	
	Frequency	fV	45	60	75	Hz	
LVDS Receiver clock	Input spread spectrum ratio	SSr	-	1.5	2	%	Note 2
	spread spectrum modulation frequency	Fssm	-	-	100	KHZ	

Note 1: The DCLK range at last line of V-blanking should be set in 0-H-active/2.

Note 2: The SSGG(spread Spectrum Clock Generator) is defined as following figure

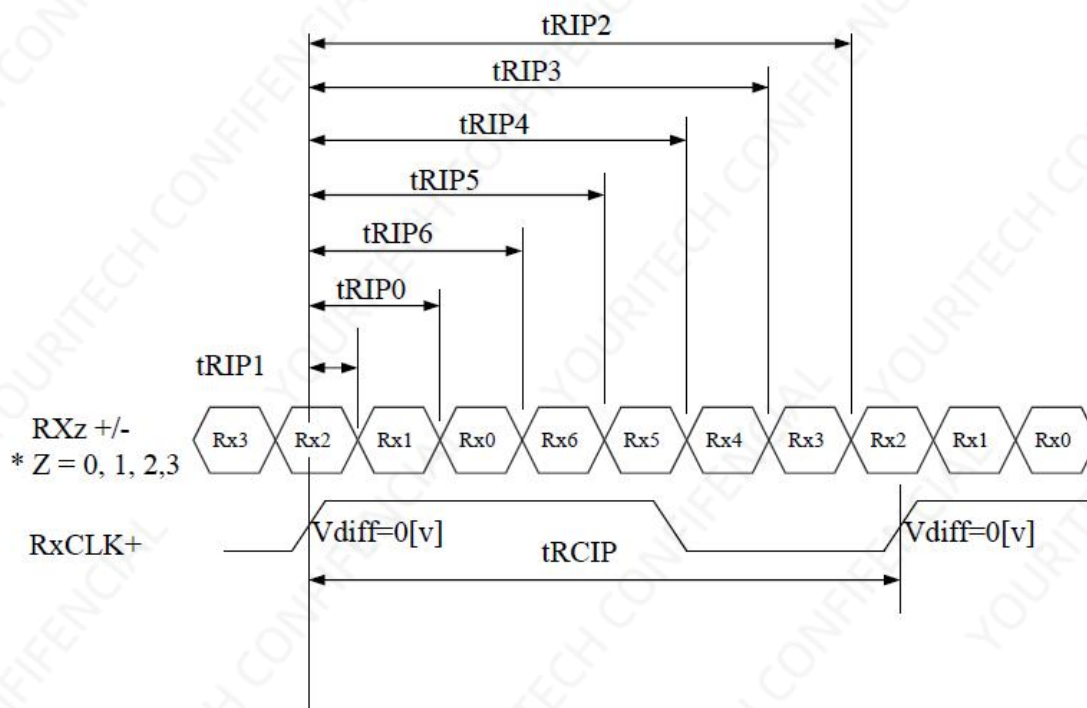


8.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table

<LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Typ	Max	Unit	Remark
CLKIN Period	tRCIP	9.5		25	nsec	
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec	
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec	
Input Data 2	tRIP6	2 × tRCIP/7-0.4	2 × tRCIP/7	2 × tRCIP/7+0.4	nsec	
Input Data 3	tRIP5	3 × tRCIP/7-0.4	3 × tRCIP/7	3 × tRCIP/7+0.4	nsec	
Input Data 4	tRIP4	4 × tRCIP/7-0.4	4 × tRCIP/7	4 × tRCIP/7+0.4	nsec	
Input Data 5	tRIP3	5 × tRCIP/7-0.4	5 × tRCIP/7	5 × tRCIP/7+0.4	nsec	
Input Data 6	tRIP2	6 × tRCIP/7-0.4	6 × tRCIP/7	6 × tRCIP/7+0.4	nsec	

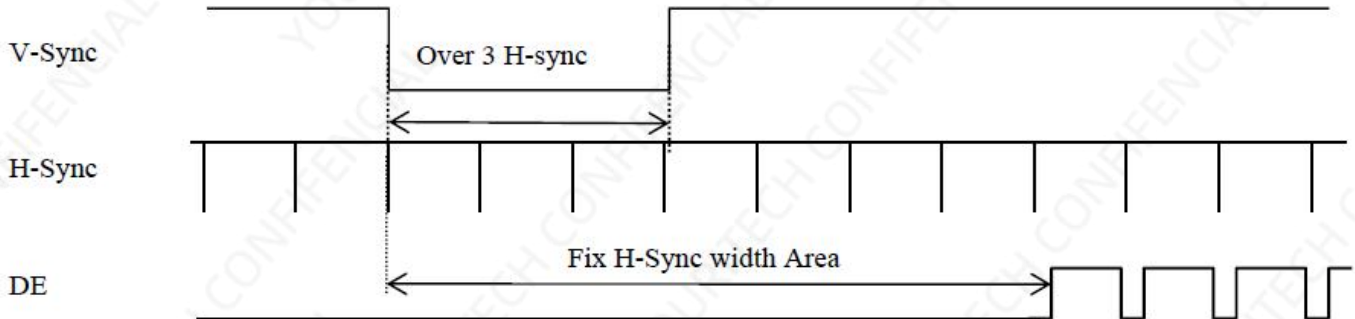


* Vdiff = (RXz+)-(RXz-),....,(RXCLK+)-(RXCLK-)



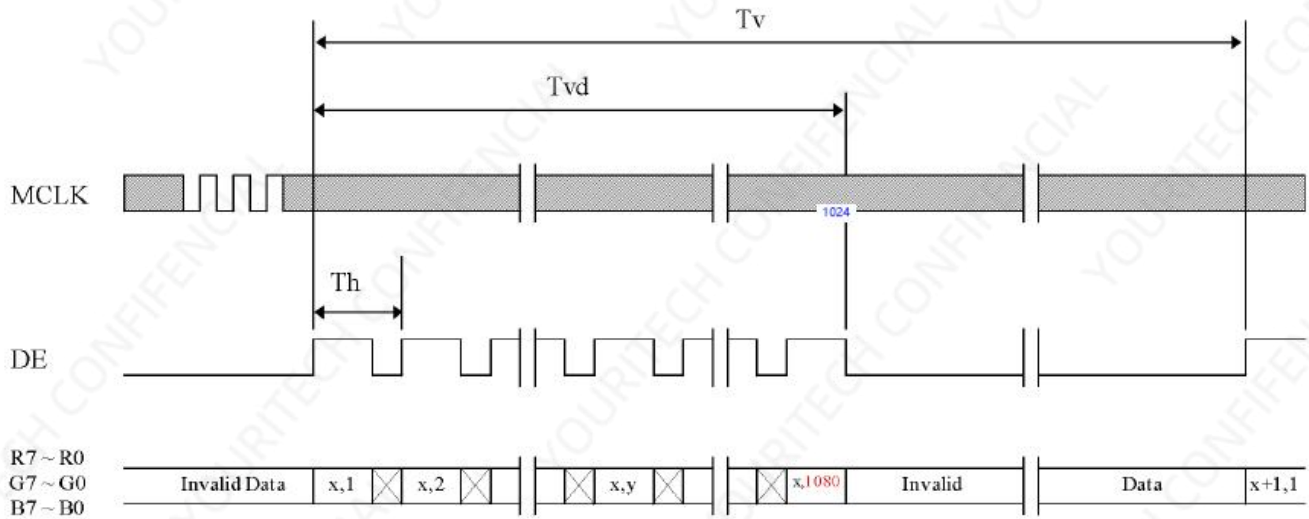
9. SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

Sync Timing Waveforms

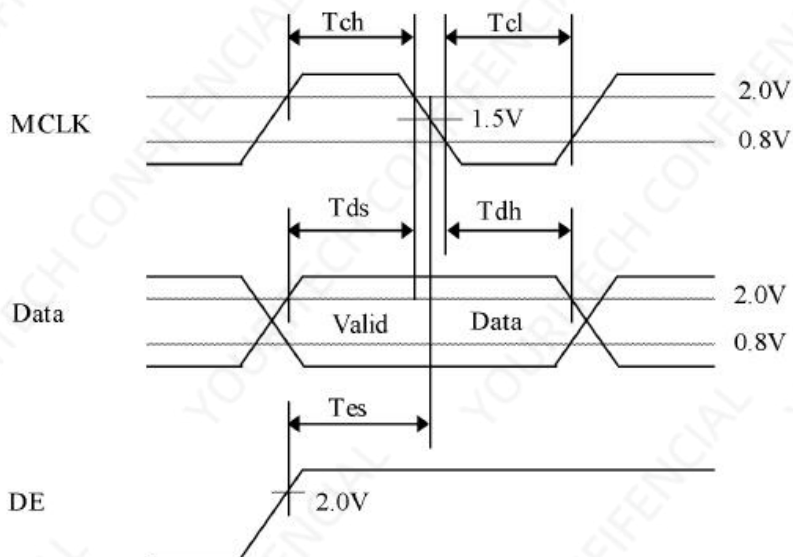
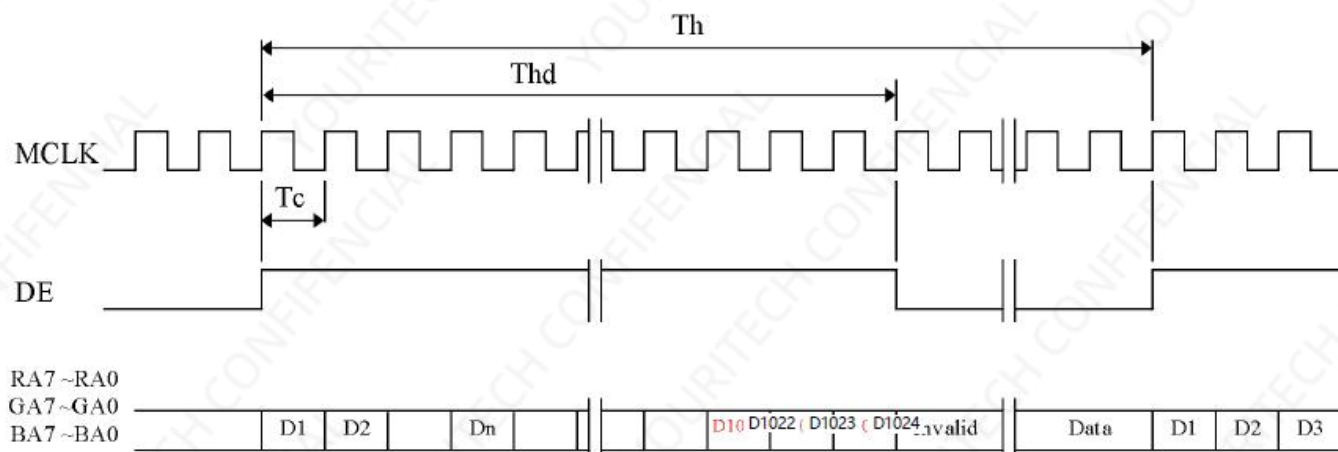


- 1) Need over 3 H-Sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

Vertical Timing Waveforms

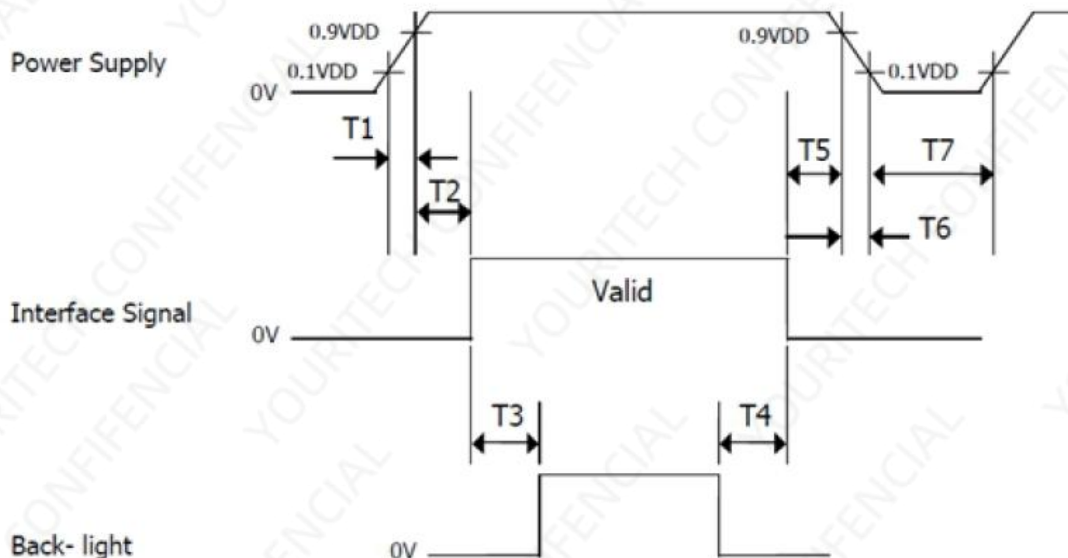


Horizontal Timing Waveforms



10. POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	300	-	-	ms
T4	500	-	-	ms
T5	0	-	50	ms
T6	0	-	10	ms
T7	500	-	-	ms



11. LCD Module Out-Going Quality Level

11.1 VISUAL & FUNCTION INSPECTION STANDARD

11.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

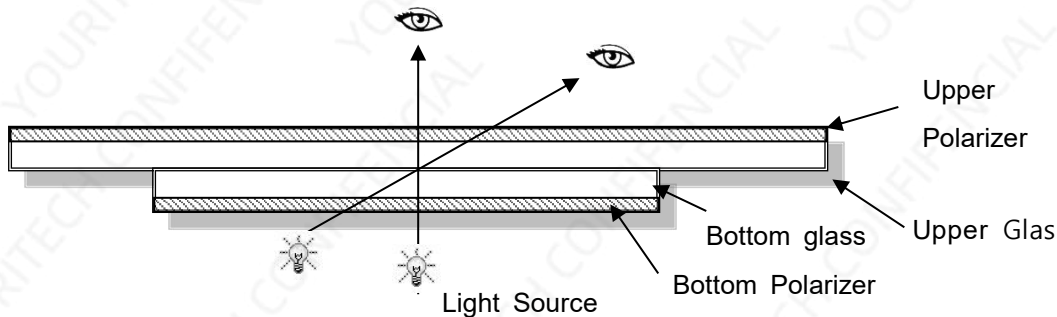
Temperature : $25\pm 5^{\circ}\text{C}$

Humidity : $65\%\pm 10\%\text{RH}$

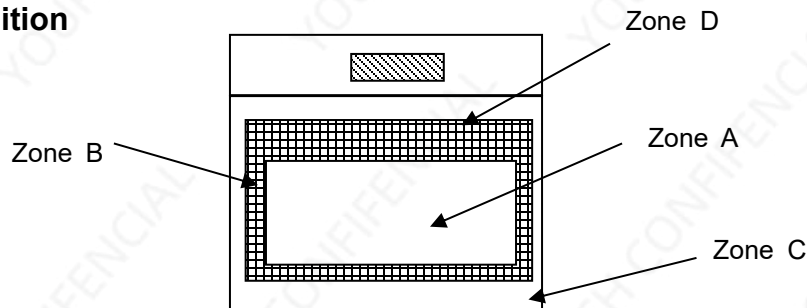
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



11.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C Cover (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D : IC Bonding Area

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer



11.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class II

AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , LCM: Liquid Crystal Module,

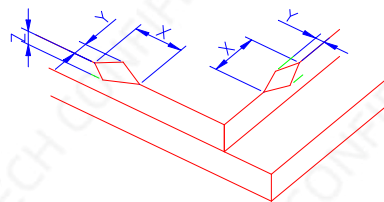
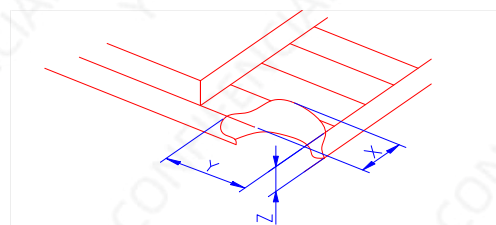
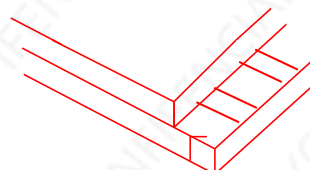
No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. etc	Major
2	Missing	Missing components and etc	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed, deformation and etc	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot/Line defect	Light dot, Dim spot, (Note1) Polarizer Air Bubble, Polarizer accidented spot and etc	
6	Soldering appearance	Good soldering , Peeling off is not allowed and etc	
7	LCD/Polarizer	Black/White spot/line, scratch, crack, etc.	

Note1: a) Light dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern.

b) Dim dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture.



11.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of IT O, T: Height of LCD	(1) The edge of LCD broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td><Inner border line of the seal</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
	X	Y	Z					
	≤3.0mm	<Inner border line of the seal	≤T					
(2)LCD corner broken	 <table><tr><td>X</td><td>Y</td><td>Z</td></tr><tr><td>≤3.0mm</td><td>≤L</td><td>≤T</td></tr></table>	X	Y	Z	≤3.0mm	≤L	≤T	
X	Y	Z						
≤3.0mm	≤L	≤T						
(3) LCD crack	 Crack Not allowed							



Spot defect

$\Phi=(X+Y)/2$

① light dot (black/white spot , pinhole, stain, etc.)

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.5$	Ignore	Ignore	
$0.5 < \Phi \leq 0.7$	3(distance $\geq 10\text{mm}$)		
$0.7 < \Phi \leq 0.9$	2(distance $\geq 10\text{mm}$)		
$\Phi > 0.9$	1		

② Dim spot (light leakage、dent、dark spot, etc)

Zone Size (mm)	Acceptable Qty		
	A	B	C
$\Phi \leq 0.5$	Ignore	Ignore	
$0.5 < \Phi \leq 0.7$	3(distance $\geq 10\text{mm}$)		
$0.7 < \Phi \leq 0.9$	2(distance $\geq 10\text{mm}$)		
$\Phi > 0.9$	1		

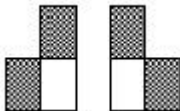
③ Polarizer accidented spot

Zone Size (mm)	Acceptable Qty			
	A	B	C	
$\Phi \leq 0.5$	Ignore		Ignore	
$0.5 < \Phi \leq 0.8$	3(distance $\geq 10\text{mm}$)			
$\Phi > 0.8$	1			

④Polarizer Bubble

Zone Size (mm)	Acceptable Qty			
	A	B	C	
$\Phi \leq 0.5$	Ignore		Ignore	
$0.5 < \Phi \leq 0.8$	2(distance $\geq 10\text{mm}$)			
$\Phi > 0.8$	1			



3.0	LCD Pixel defect	Pixel bad points <table border="1" data-bbox="539 309 1497 1055"> <thead> <tr> <th data-bbox="539 309 730 360">Item</th><th data-bbox="730 309 1246 360">Zone A</th><th data-bbox="1246 309 1497 360">Acceptable Qt</th></tr> </thead> <tbody> <tr> <td data-bbox="539 360 730 521" rowspan="3">Bright dot</td><td data-bbox="730 360 1246 416">Random</td><td data-bbox="1246 360 1497 416">$N \leq 2$</td></tr> <tr> <td data-bbox="730 416 1246 468">2 dots adjacent</td><td data-bbox="1246 416 1497 468">$N \leq 0$</td></tr> <tr> <td data-bbox="730 468 1246 521">3 dots adjacent</td><td data-bbox="1246 468 1497 521">$N \leq 0$</td></tr> <tr> <td data-bbox="539 521 730 689" rowspan="3">Dark dot</td><td data-bbox="730 521 1246 577">Random</td><td data-bbox="1246 521 1497 577">$N \leq 3$</td></tr> <tr> <td data-bbox="730 577 1246 631">2 dots adjacent</td><td data-bbox="1246 577 1497 631">$N \leq 0$</td></tr> <tr> <td data-bbox="730 631 1246 689">3 dots adjacent</td><td data-bbox="1246 631 1497 689">$N \leq 0$</td></tr> <tr> <td data-bbox="539 689 730 999">Distance</td><td data-bbox="730 689 1246 999"> 1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot. </td><td data-bbox="1246 689 1497 999">5mm</td></tr> <tr> <td colspan="2" data-bbox="539 999 1246 1055">Total bright and dark dot</td><td data-bbox="1246 999 1497 1055">$N \leq 4$</td></tr> </tbody> </table> Note: A) Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. B) Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture. C) 2 dot adjacent = 1 pair = 2 dots Picture:  2 dot adjacent  2 dot adjacent (vertical)  2 dot adjacent  2 dot adjacent (slant)	Item	Zone A	Acceptable Qt	Bright dot	Random	$N \leq 2$	2 dots adjacent	$N \leq 0$	3 dots adjacent	$N \leq 0$	Dark dot	Random	$N \leq 3$	2 dots adjacent	$N \leq 0$	3 dots adjacent	$N \leq 0$	Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm	Total bright and dark dot		$N \leq 4$
Item	Zone A	Acceptable Qt																							
Bright dot	Random	$N \leq 2$																							
	2 dots adjacent	$N \leq 0$																							
	3 dots adjacent	$N \leq 0$																							
Dark dot	Random	$N \leq 3$																							
	2 dots adjacent	$N \leq 0$																							
	3 dots adjacent	$N \leq 0$																							
Distance	1. Minimum Distance Between Bright dots. 2. Minimum Distance Between dark dots 3. Minimum Distance Between dark and bright dot.	5mm																							
Total bright and dark dot		$N \leq 4$																							



4.0	Line defect (LCD /Polarizer backlight black/white line, scratch, stain)					
						
	W: width, L : length					
	N : Count					
		Width(mm)	Length(m)	Acceptable Qty		
				A	B	C
		$\Phi \leq 0.2$	Ignore	Ignore		Ignore
		$0.2 < W \leq 0.3$	$L \leq 10.0$	$N \leq 3$		
		$0.3 < W \leq 0.4$	$L \leq 10.0$	$N \leq 2$		
		$W > 0.4$	Define as spot defect			
5.0	Electronic Components SMT.	Not allow missing parts, solderless connection, cold solder joint, mismatch, The positive and negative polarity opposite				
6.0	Display color& Brightness.	1. Color: Measuring the color coordinates, The measurement standard according to the datasheet or samples. 2. Brightness: Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.				
7.0	LCD Mura/Waving/ Hot spot	Not visible through 5% ND filter in 50% gray or judge by limit sample if necessary.				

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed



12. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	80°C,96H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature Operating	-30°C, 96H	
High Temperature Storage	80°C, 96H	
Low Temperature Storage	-30°C, 96H	
High Temperature & High Humidity Operating	+60°C, 90% RH ,96 hours.	
Thermal Shock (Non-operation)	-30°C,30 min ↔ 80°C,30 min, Change time:5min 20CYC.	
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15°C~35°C, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



13. Cautions and Handling Precautions

13.1 Handling and Operating the Module

(1) When the module is assembled, it should be attached to the system firmly.

Do not warp or twist the module during assembly work.

(2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

(4) Do not allow drops of water or chemicals to remain on the display surface.

If you have the droplets for a long time, staining and discoloration may occur.

(5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.

Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static; it may cause damage to the CMOS ICs.

(9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(10) Do not disassemble the module.

(11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(12) Pins of I/F connector shall not be touched directly with bare hands.

(13) Do not connect, disconnect the module in the "Power ON" condition.

13.2 Storage and Transportation.

(1) Do not leave the panel in high temperature, and high humidity for a long time.

It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%

(2) Do not store the TFT-LCD module in direct sunlight.

(3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

(4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.

In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

(5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.