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MODEL NO : ET238UH01-T

MODEL VERSION: 01

SPEC VERSION : 1.0

ISSUED DATE: 2022-03-25

☐ Preliminary Specification

☒ Final Product Specification

Customer : _____

Approved by	Notes

Youri Confirmation

Prepared by	Reviewed by	Approved by
John	Johnny	Wang

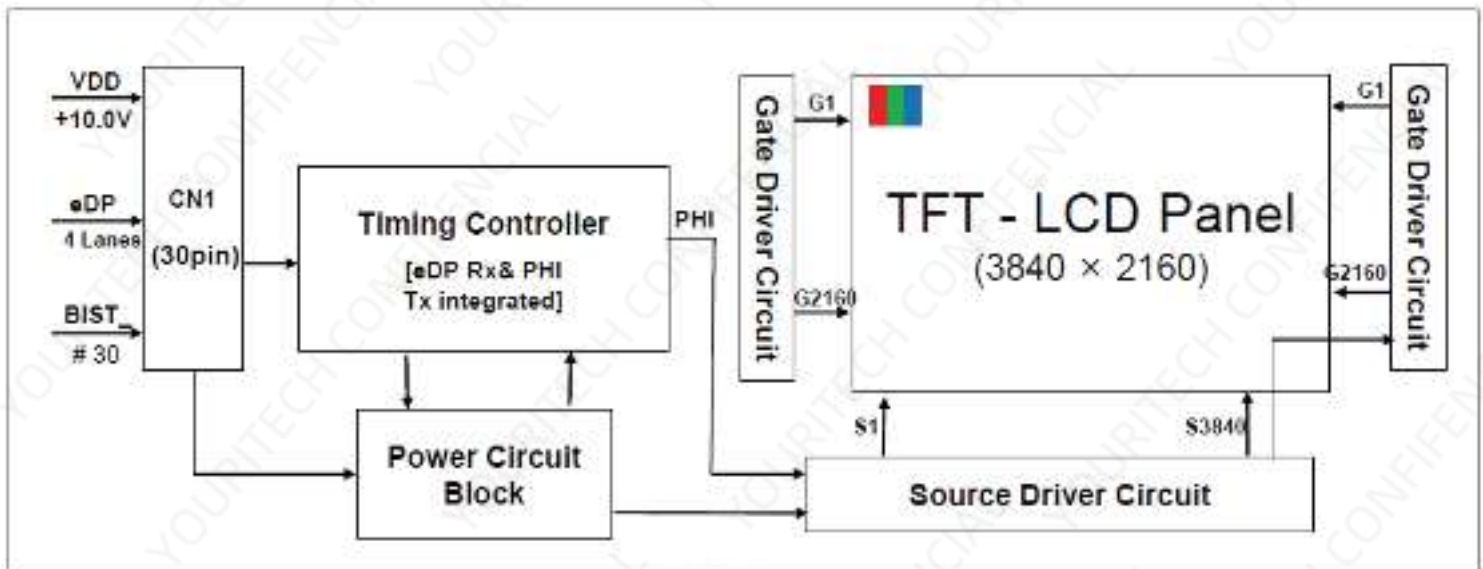
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1.0 GENERAL DESCRIPTION

1.1 Introduction

(Thin Film Transistors) as an active switching devices. This module has a 23.8 inch diagonally measured active area with UHD resolutions (3840 horizontal by 2160 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 1.07B colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- 4 lane eDP Interface with 5.4Gbps Link Rates
- High-speed response
- 10bit (8bit+H-FRC) color depth, display 1.07B colors
- Compatible with sRGB 99% & NTSC75%
- low reflection and wide viewing angle
- RoHS/Halogen Free
- Gamma Correction

1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	527.04 (H) × 296.46(V)	mm	
Number of pixels	3840(H) × 2160(V)	pixels	
Pixel pitch	0.13725(H) × 0.13725(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	1.07B	colors	
Display mode	Normally Black		
Dimensional outline	545.0 (H) × 323.4(V) × 12.5(D) typ.	mm	Detail refer to drawing
Weight	TBD	g	
Surface Treatment	Haze 25%, 3H		
Back-light	Edge side, 2-LED Lighting Bar type		

2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

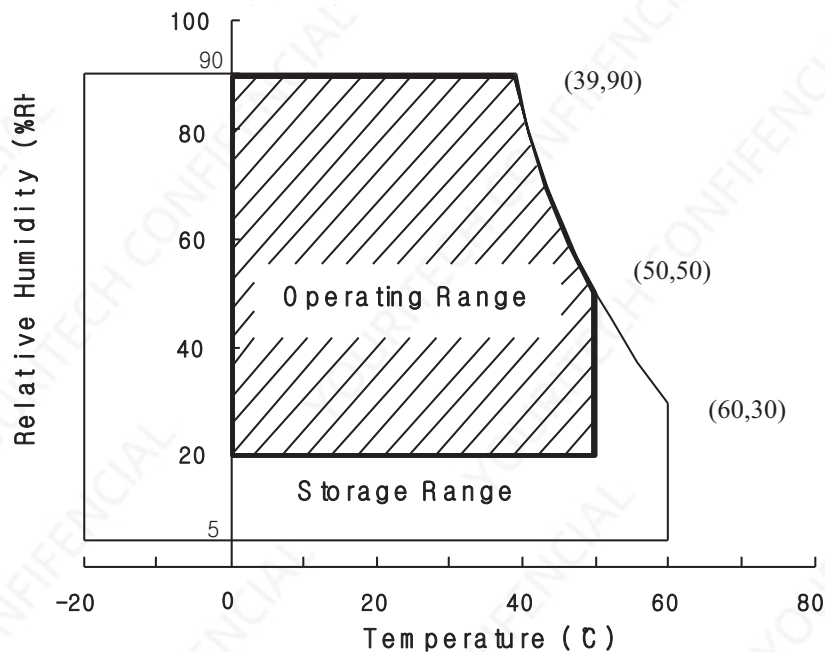
[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.3	11.5	V	$T_a = 25\text{ }^{\circ}\text{C}$
Logic Supply Voltage	V_{IN}	VSS-0.3	$V_{DD}+0.3$	V	
Operating Temperature	T_{OP}	0	+50	$^{\circ}\text{C}$	1)
Storage Temperature	T_{ST}	-20	+60	$^{\circ}\text{C}$	1)
OC Surface Temperature (Operation)	$T_{surface}$	0	+65	$^{\circ}\text{C}$	2)

Note : 1) Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 $^{\circ}\text{C}$ max. and no condensation of water.

2) Panel Surface Temperature should be Min. 0 $^{\circ}\text{C}$ and Max. +65 $^{\circ}\text{C}$ under the VDD = 5.0V, Frame rate = 60Hz, 25 $^{\circ}\text{C}$ ambient Temp. no humidity control and LED string current is typical value.



3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta =25±2 °C]

Parameter		Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V _{DD}	9	10.0	11	V	Note1
Power Supply Current	I _{DD}	-	410	560	mA	
In-Rush Current	I _{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V _{RF}	-	-	400	mV	Note1,3
High Level Differential Input Threshold Voltage	V _{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	-	-	mV	
Differential input voltage	V _{ID}	100	-	600	mV	
Differential input common mode voltage	V _{cm}	0	-	2		V _{IH} =100mV, V _{IL} =-100mV
Power Consumption	P _D	-	3	6.8	W	
	P _{BL}	28.22	31.25	34.27	W	
	P _{total}	-	34.25	41.07	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=10V, Frame rate=60Hz

Test Pattern of power supply current



a) Typ : Mosaic Pattern



b) Max :1 line Inversion

2. Duration of rush current is about 2 ms and rising time of VDD is 1ms

3. Ripple Voltage should be covered by Input voltage Spec.

4. Calculated value for reference (Input pins*VPIN × IPIN) excluding inverter loss.

3.2 Backlight Unit

< Table 4. LED Backlight Unit >

Parameter		Min.	Typ.	Max.	Unit	Remarks
LED Light Bar Input Voltage Per Input Pin	VPIN	39.2	43.4	47.6	V	Duty 100%
LED Light Bar Input Current Per Input Pin	IPIN		360*2		mA	
LED Power Consumption	P _{BL}	28.22	31.25	34.27	W	
LED Life-Time-30,000-Hrs						

LED bar consists of 84 LED packages 14 strings parallel) * 6 packages (serial) .

4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25 \pm 2^\circ\text{C}$) with the equipment of Luminance meter system (Goniometer system and TOPCONE PR730) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta_{0=0}$ ($=\theta_3$) as the 3 o'clock direction (the "right"), $\theta_{0=90}$ ($=\theta_{12}$) as the 12 o'clock direction ("upward"), $\theta_{0=180}$ ($=\theta_9$) as the 9 o'clock direction ("left") and $\theta_{0=270}$ ($=\theta_6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or Φ , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V $\pm 10\%$ at 25°C . Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 74.25MHz, $I_{BL} = 260\text{mA}$, $T_a = 25 \pm 2^\circ\text{C}$]

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle range	Horizontal	Θ_3	CR > 10	85	89	-	Deg.	Note 1
		Θ_9		85	89	-	Deg.	
	Vertical	Θ_{12}		85	89	-	Deg.	
		Θ_6		85	89	-	Deg.	
Luminance Contrast ratio		CR	$\Theta = 0^\circ$ (Center) Normal Viewing Angle	700	1000			Note 2
Luminance of White		Y_w		500	600		cd/m ²	Note 3
White luminance uniformity		ΔY		75	80		%	Note 4
Reproduction of color	White	W_x		0.283	0.313	0.343	-	Note 5
		W_y		0.299	0.329	0.359	-	
	Red	R_x		0.623	0.653	0.683	-	
		R_y		0.304	0.334	0.364	-	
	Green	G_x		0.255	0.285	0.315	-	
		G_y		0.592	0.622	0.652	-	
	Blue	B_x		0.119	0.149	0.179	-	
		B_y		0.032	0.062	0.092	-	
Response Time	GTG	T_g			14	20	ms	Note 6

Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
2. Contrast measurements shall be made at viewing angle of $\theta = 0^\circ$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
4. The White luminance uniformity on LCD surface is then expressed as :

$$\Delta Y = (\text{Minimum Luminance of 9points} / \text{Maximum Luminance of 9points}) * 100$$
 (See FIGURE 2 shown in Appendix).
5. The color chromaticity coordinates specified in Table 4. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
6. Response time T_g is the average time required for display transition by switching the input signal as below table and is based on Frame rate $f_V = 60\text{Hz}$ to optimize.
 Each time in below table is defined as Figure 3 and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)".
7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

- CN1 Module Side Connector : STM MSAK24025P30 or Equivalent

Pin No	Symbol	Function	Remark
1	VDD	Power Supply (10.0V)	
2	VDD	Power Supply (10.0V)	
3	VDD	Power Supply (10.0V)	
4	VDD	Power Supply (10.0V)	
5	VDD	Power Supply (10.0V)	
6	GND	Ground	
7	GND	Ground	
8	NC	No Connection	
9	NC	No Connection	
10	GND	Ground	
11	HPD	Hot Plug Detection Signal	
12	GND	Ground	
13	DAUXN	AUX_CHN Component Signal for Auxiliary Channel	
14	DAUXP	AUX_CHP True Signal for Auxiliary Channel	
15	GND	Ground	
16	DRX0P	Lane3P True Signal for Main Link 0	
17	DRX0N	Lane3N Component Signal for Main Link 0	
18	GND	Ground	
19	DRX1P	Lane3P True Signal for Main Link 1	
20	DRX1N	Lane3N Component Signal for Main Link 1	
21	GND	Ground	
22	DRX2P	Lane3P True Signal for Main Link 2	
23	DRX2N	Lane3N Component Signal for Main Link 2	
24	GND	Ground	
25	DRX3P	Lane3P True Signal for Main Link 3	
26	DRX3N	Lane3N Component Signal for Main Link 3	
27	GND	Ground	
28	GND	Ground	
29	NC	No Connection	
30	BIST	BIST Function	

6.0 SIGNAL TIMING SPECIFICATION

the DE only.

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	519.48	533.3	560.88	MHz
	High Time	Tch	-	4/7	-	Tc
	Low Time	Tcl	-	3/7	-	Tc
Frame Period		Tv	2200	2222	2320	lines
			-	60	-	Hz
			-	16.7	-	ms
Vertical Display Period		Tvd	-	2160	-	lines
One line Scanning Period		Th	3900	4000	4150	clocks
Horizontal Display Period		Thd	-	3840	-	clocks

6.0 SIGNAL TIMING SPECIFICATION

6.1 6.1 The is operated by the DE only.

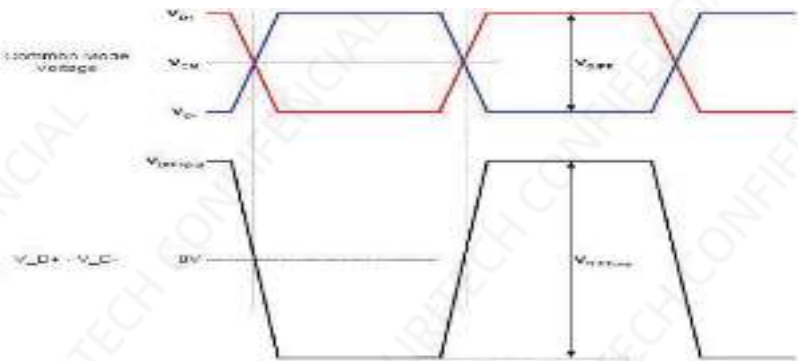
Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	-	140	-	MHz
	High Time	Tch	-	4/7Tc	-	
	Low Time	Tcl	-	4/7Tc	-	
Frame Period		Tv	-	2222	-	lines
			50	60	62	Hz
			20	16.7	16.13	ms
Vertical Display Period		Tvd	-	2160	-	lines
One line Scanning Period		Th	-	4200	-	clocks
Horizontal Display Period		Thd	-	3840	-	clocks

6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 4.

<Table 4. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Typ	Max	Unit	Remark
Spread spectrum clock	ssc	-2	-	+2	%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	120	-	-	mV	
Rx input DC common mode voltage	VRX_DC_CM	0	-	2	V	
Differential termination resistance	R _{RX-DIFF}	-	100	-	Ω	
Single-ended termination resistance	R _{RX-SE}	-	50	-	Ω	
Rx short circuit current limit	I _{RX_SHORT}	-	-	50	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	L _{RX_SKEW_INTRA_PAIR}	-	-	150	ps	

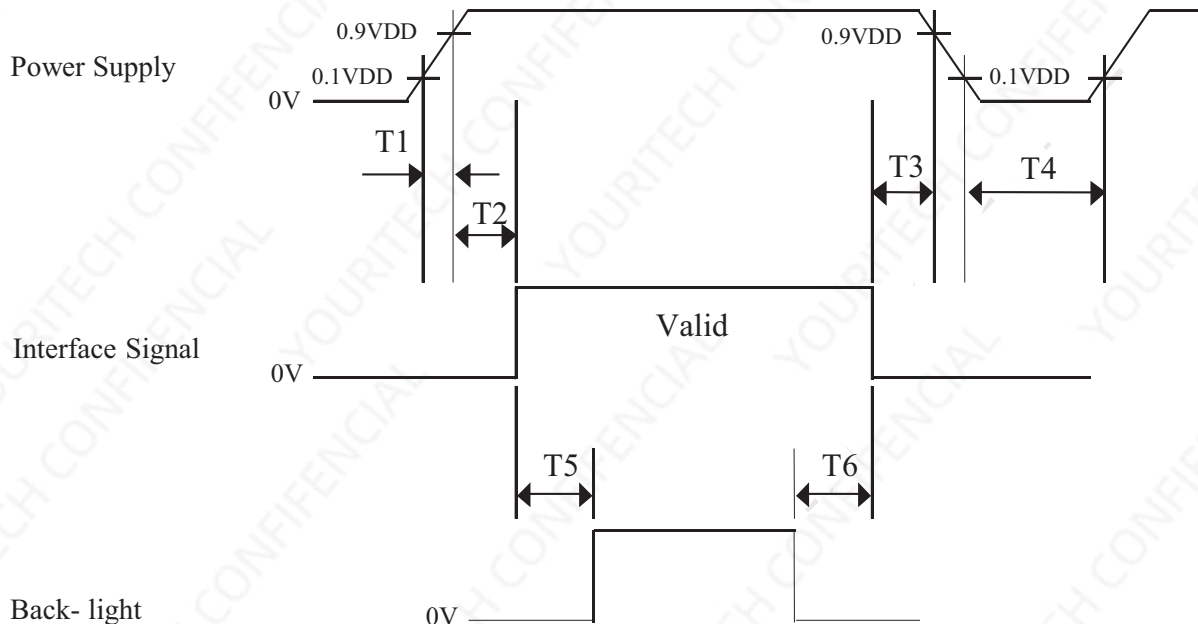


7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

Color & Gray Scale		RED DATA								GREEN DATA								BLUE DATA								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray Scale of RED	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of GREEN	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
Gray Scale of BLUE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	
Gray Scale of WHITE	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	
	△	↑								↑								↑								
	▽	↓								↓								↓								
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	
	▽	1	1	1	1	1	1	0	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	

8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below.



- $0.5 \text{ ms} \leq T1 \leq 10 \text{ ms}$
- $0 \leq T2 \leq 50 \text{ ms}$
- $0 < T3 \leq 50 \text{ ms}$
- $1 \text{ sec} \leq T4$
- $200 \text{ ms} \leq T5$
- $200 \text{ ms} \leq T6$

Notes:

1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
2. Do not keep the interface signal high impedance when power is on.
3. Back Light must be turn on after power for logic and interface signal are valid.
4. T7 decreases smoothly, there is none re-bouncing voltage.
5. If T3=0ms, there is a risk of flicker when power On/Off.
6. If T6=0ms, there is a risk of abnormal display when power off.

9.0 MECHANICAL CHARACTERISTICS

9.1 Dimensional Requirements

Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	545.0 (H) × 323.4(V) × 12.5(D) typ.	mm
Weight	TBD	gram
Active area	527.04(H) × 296.46(V)	mm
Pixel pitch	0.13725(H)mm x 0.13725(V)mm	mm
Number of pixels	3840 (H) × 2160 (V) (1 pixel = R + G + B dots)	pixels

9.2 Mounting

See FIGURE 5. shown in Appendix

9.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

9.4 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.

10.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

No	Test Items	Conditions	
1	High temperature storage test	Ta = 60 °C , 240 hrs	
2	Low temperature storage test	Ta = -20 °C , 240 hrs	
3	High temperature & high humidity operation test	Ta = 50 °C , 80%RH, 240hrs	
4	High temperature operation test	Ta = 50 °C , 240hrs	
5	Low temperature operation test	Ta = 0 °C , 240hrs	
6	Thermal shock	Ta = -20 °C ↔ 60 °C (0.5 hr), 100 cycle	
7	Vibration test (non-operating)	Frequency	Random, 10 ~ 300 Hz, 30 min/Axis
		Gravity\	1.5 Grms
		Period	X, Y, Z 30 min
8	Shock test (non-operating)	Gravity	50G
		Pulse width	11msec, sine wave
		Direction	± X, ± Y, ± Z Once for each
9	Electro-static discharge test	Air : 150 pF, 330Ω, 15 KV Contact : 150 pF, 330Ω, 8 KV	

11.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module

- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module

- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation

- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- Ultra-violet ray filter is necessary for outdoor operation
- If the product will be used in extreme conditions such as high temperature, humidity, display patterns, operation time, etc., it is strongly recommended to contact BOE for application engineering device. Otherwise, the reliability and function of the module may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stocks, markets, and controlling systems.

(4) Cautions for the atmosphere

- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics

- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

11.0 HANDLING & CAUTIONS

(6) Other cautions

- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.
- When this reverse model is used as a forward-type model (PCB on top side), BOE can not guarantee any defects of LCM.
- If LCD module containing system is out of BOE 's operating or storing condition, BOE can not guarantee LCD module operating properly.

12.0 APPENDIX

Figure 1. Measurement Set Up

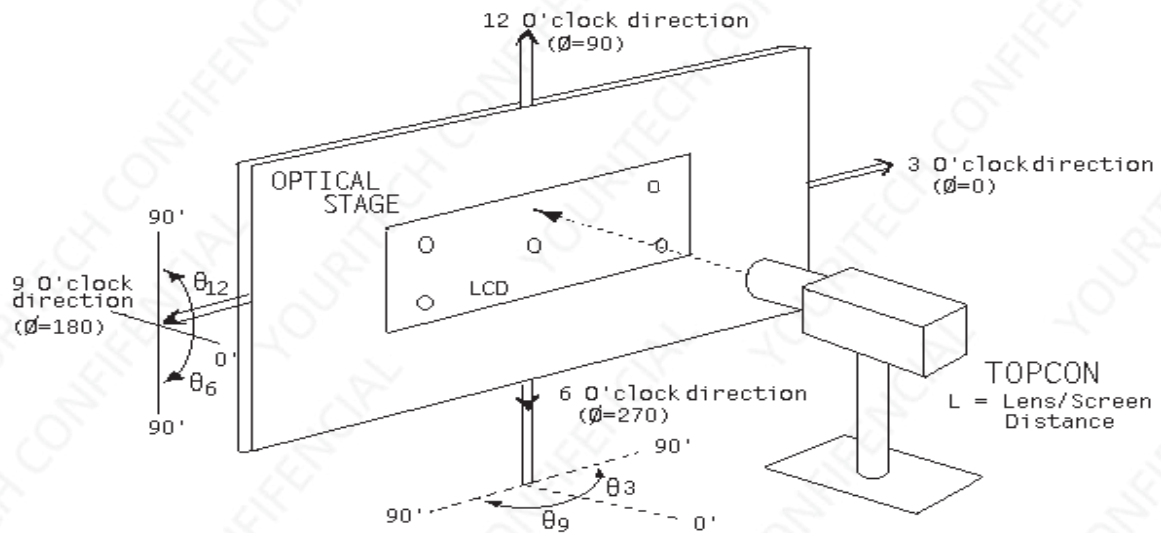


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)

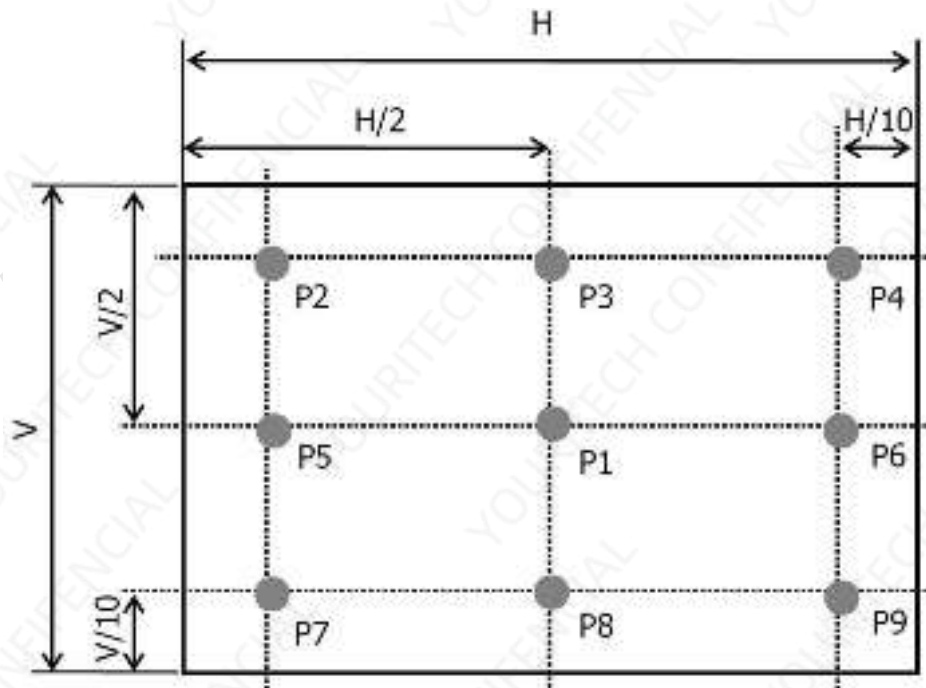


Figure 3. Response Time Testing

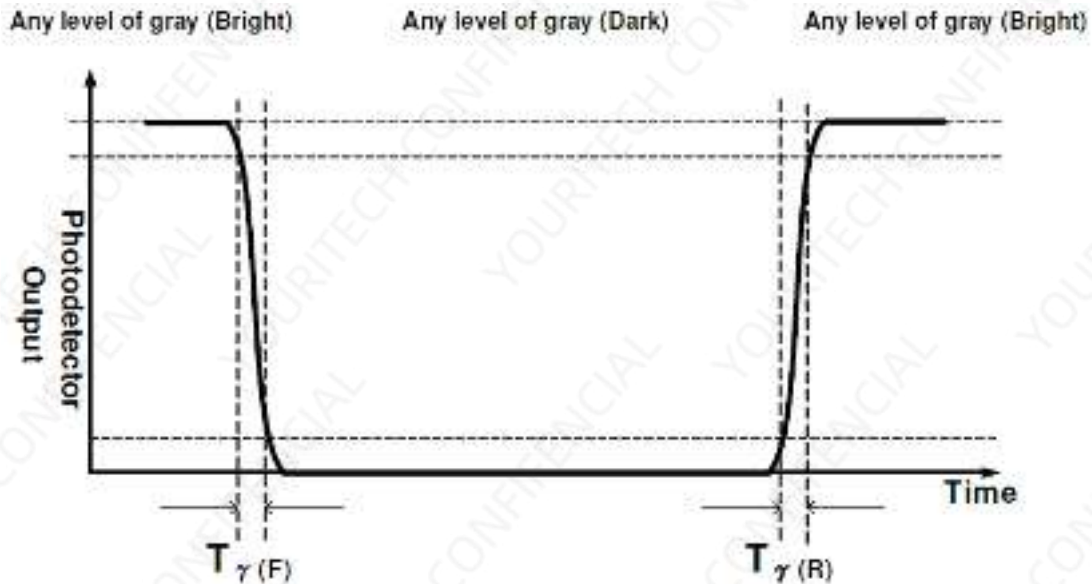
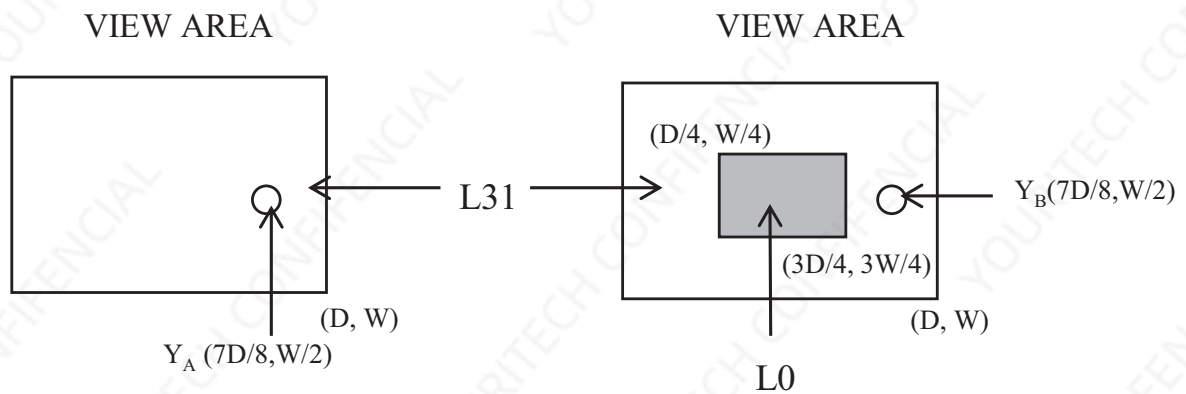


Figure 4. Cross Modulation Test Description



$$\text{Cross-Talk (\%)} = \left| \frac{Y_B - Y_A}{Y_A} \right| \times 100$$

Where: Y_A = Initial luminance of measured area (cd/m^2)
 Y_B = Subsequent luminance of measured area (cd/m^2)
 The location measured will be exactly the same in both patterns

深圳市优瑞泰克科技有限公司

Customer Code.
Customer's P/N

SPECIFICATION OF BACKLIGHT
Edition (版本) 01

Date: Page: 1/1 THE THIRD ANGLE

Product Model

ET238UH01-T

Approval (核准)

Check (检查)

Design (设计)

Pin No.	Symbol	Description
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545.00±0.5 (H, 0.0)

530.20±0.5 (BEZEN OPEN)

527.00 (A.A)

7.40

9.00

8.30±0.50

4-3.90±0.3

148.20±0.3

88.60

12.5±0.5

409.80

67.60

182.70

500.00±10

500.00±10

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Customer:

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