

INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

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on-site service

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Cover glass

System solutions

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Custom Display

Production Custom designs Installation

Mechanical design



ET370BA01-IL



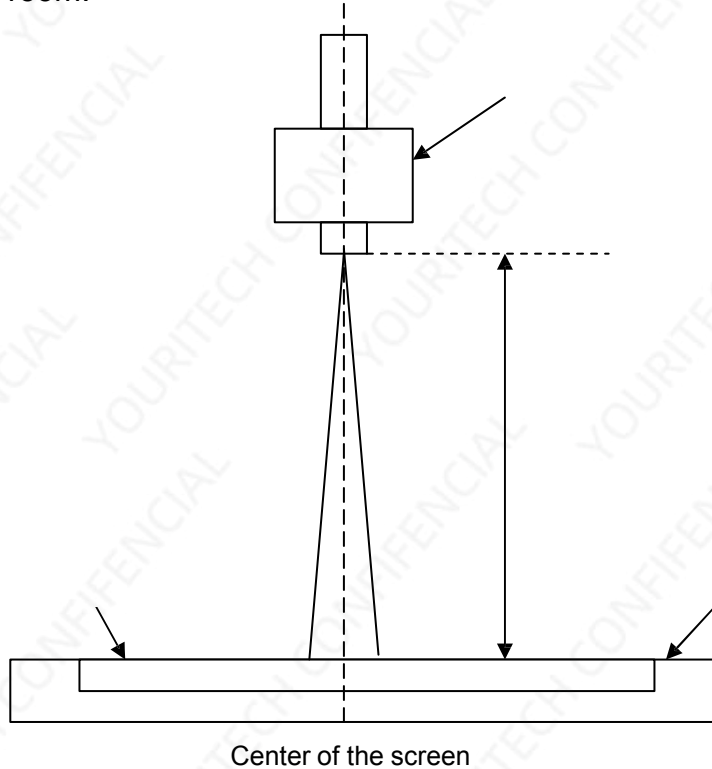
1. General Features

Items	Specification	Unit	Note
Active Screen Size	37.1	inches	
Display area	941.2x50.0	mm	
Outline Dimension	969.2(H) x 78.0(V) x 20(D)	mm	
Pixel Format	1920 x 102	Pixels	
Pixel Pitch	0.4902 (H) x 0.4902 (W)	mm	
Color Depth	16777216	Color	8-bit
Viewing Angle	89/89/89/89 (R/L/ U/D)		CR>10
Luminance	500	cd/m ²	
Contrast Ratio	1000:1		
Backlight	WLED		
Power Consumption	40	Watt	
Life Time	50000	Hrs	
Operating Temperature	0 - 50	°C	
Storage Temperature	-20 - 60	°C	
Operating Humidity	10 - 90	%RH	
Storage Humidity	10 - 90	%RH	



Note 1: Measurement method

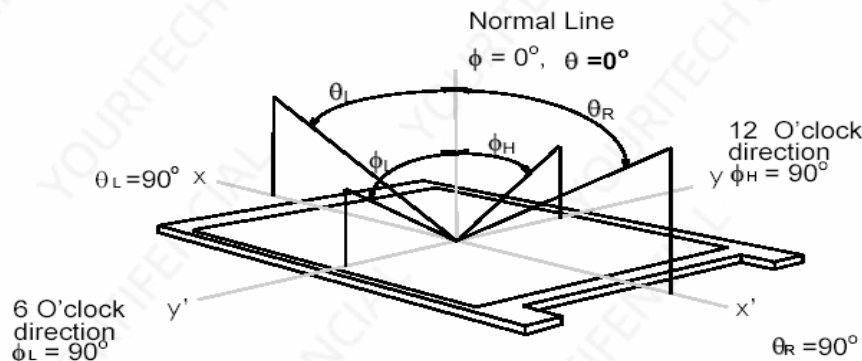
The LCD module should be stabilized at given temperature for 30 minutes to avoid abrupt temperature change during measuring (at surface 35rJ). In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 30 minutes in a stable, windless and dark room.



Note 2: Definition of viewing angle measured by ELDIM (EZContrast 88)

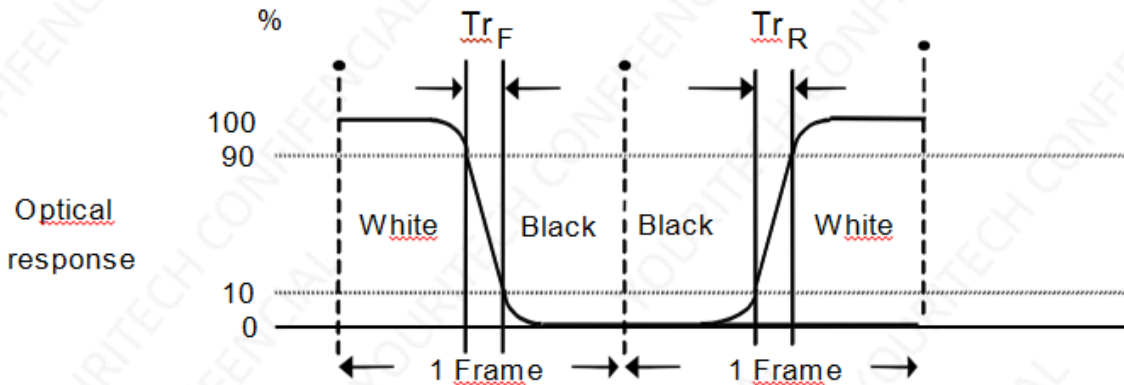
Viewing angle is the measurement of contrast ratio ≥ 10 , at the screen center, over a 180°

horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as follows; 90° (θ) horizontal left and right and 90° (ϕ) vertical, high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated about its center to develop the desired measurement viewing angle.



Note 4: Definition of Response time measured by Westar TRD-100A

The output signals of photo detector are measured when the input signals are changed from “Full Black” to “Full White” (rising time, Tr_R), and from “Full White” to “Full Black” (falling time, Tf_F), respectively. The response time is interval between the 10% and 90% (1 frame at 60 Hz) amplitudes.



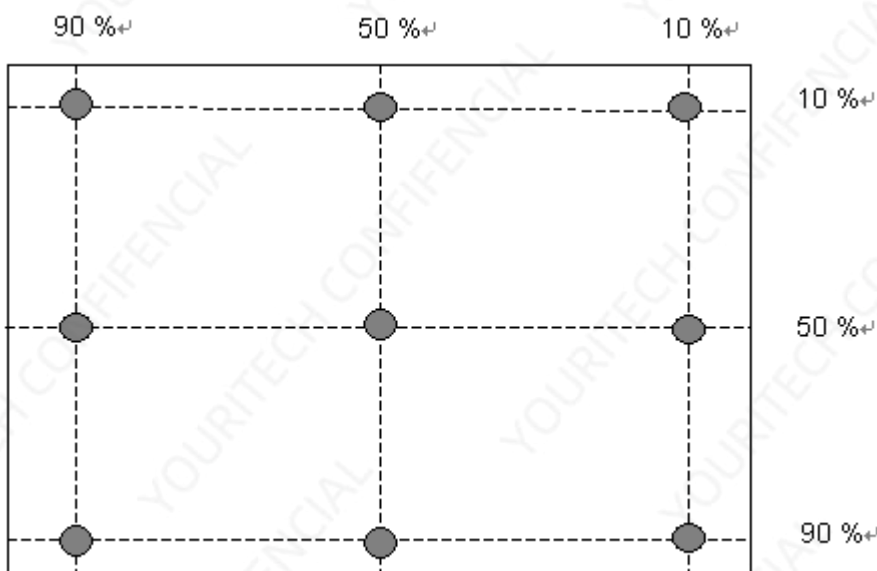
$$Tr_R + Tf_F = 5 \text{ msec (typ.)}$$

Algorithm: | Gray Level A – Gray Level B | rJ16, then the average gray to gray response time is 2 ms, (F= 60 Hz).

Note 5: Color chromaticity and coordinates (CIE) is measured by TOPCON SR-3

Note 6: Central luminance is measured by TOPCON SR-3

Note 7: Luminance uniformity of these 9 points is defined as below and measured by TOPCON SR-3



$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$



2. Connector

2.1 Input Signal & Power Connector : PF050-O51B-C20-F(UJU).

PIN	Symbol	Description	PIN	Symbol	Description
1	NC	No Connection	26	NC	No Connection
2	NC	No Connection	27	NC	No Connection
3	NC	No Connection	28	RXE0+	LVDS Receiver Signal(+)
4	NC	No Connection	29	RXE1-	LVDS Receiver Signal(-)
5	NC	No Connection	30	RXE1+	LVDS Receiver Signal(+)
6	NC	No Connection	31	RXE2-	LVDS Receiver Signal(-)
7	LVDS_SEL	'H'=JEIDA , 'L'or NC= VESA	32	RXE2+	LVDS Receiver Signal(+)
8	NC	No Connection	33	RXE0+	LVDS Receiver Signal(+)
9	NC	No Connection	34	GND	Ground
10	NC	No Connection	35	RCLKE-	LVDS Receiver Clock Signal(-)
11	GND	Ground	36	RCLKE+	LVDS Receiver Clock Signal(+)
12	RXO0-	LVDS Receiver Signal(-)	37	GND	Ground
13	RXO0+	LVDS Receiver Signal(+)	38	RXE3-	LVDS Receiver Signal(-)
14	RXO1-	LVDS Receiver Signal(-)	39	RXE3+	LVDS Receiver Signal(+)
15	RXO1+	LVDS Receiver Signal(+)	40	NC	No Connection
16	RXO2-	LVDS Receiver Signal(-)	41	NC	No Connection
17	RXO2+	LVDS Receiver Signal(+)	42	NC	No Connection
18	GND	Ground	43	NC	No Connection
19	RCLKO-	LVDS Receiver Clock Signal(-)	44	GND	Ground
20	RCLKO+	LVDS Receiver Clock Signal(+)	45	GND	Ground
21	GND	Ground	46	GND	Ground
22	RXO3-	LVDS Receiver Signal(-)	47	NC	No Connection
23	RXO3+	LVDS Receiver Signal(+)	48	VDD	Power Supply +12.0V
24	NC	No Connection	49	VDD	Power Supply +12.0V
25	NC	No Connection	50	VDD	Power Supply +12.0V
			51	VDD	Power Supply +12.0V

Notes : 1. NC(Not Connected) : This pins are only used for internal **perations**.

2. Input Level of LVDS signal is based on the IEA 664 Standard.

3. LVDS_SEL : This pin is used for selecting LVDS signal data format.

If this Pin : Low (GND)or Open (NC) Æ VESA LVDS format

Otherwise : High (3.3V) Æ JEIDA LVDS format



2.2 Backlight Connector:

PH2.0-6pin

Pin No.	Symbol	Description	note
1	VCC	Power supply voltage 12v	
2	VCC	Power supply voltage 12v	
3	ON/OFF	Output enable signal input 0~5V	
4	DIM	Dim signal input DC 0~5V	
5	GND	Power ground	
6	GND	Power ground	



3. Mechanical Characteristics

Unit:mm

