

INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

Integration support

Obsolescence Management

PCAP

Embedded Systems

Human Machine Interface

Touch Solutions

Software

Quality Management

Open Frame Monitors

LCD Controller

Research & Development

Firmware

EMC tests

on-site service

Optical Bonding

In-house Manufacturing

Cover glass

System solutions

OEM Solutions

Custom Display

Production
Custom designs
Installation

Mechanical design



初始化代码发布文件

Preliminary Specification

项 目: **G1601FP184GG**
芯片型号: **CH13721C**
版 本: **V01**
发 布 人: **杨 华**
发布日期: **2024-04-25**
适用对象: **M 检& FAE**

目录

目录	2
版本更新履历.....	3
1 点屏设定说明.....	4
1.1 点屏及电压设定	4
1.2 时序设定.....	5
2 Initial Code.....	6

版本更新履历

版本号	日期	发布说明	发布人
V01	2024/04/25	初版发布	杨 华

1 点屏设定说明

1.1 点屏及电压设定

点屏设定

Interface	MIPI 4lane
H-active	1080
HBP	12
HFP	12
HSW	4
V-active	2160
VBP	12
VFP	12
VSW	4
Display mode	MIPI CMD mode
Frame rate	60Hz

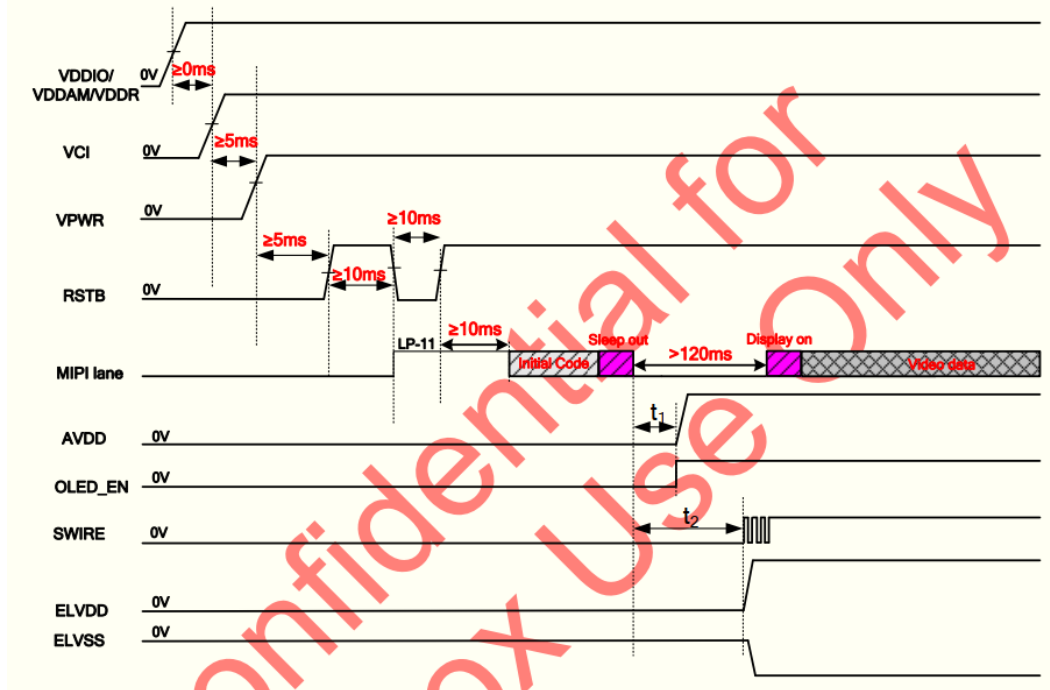
电压设定

VCI	3.0V
VDDIO	1.8V
AVDD	6.7V
ELVSS	-3.0V
ELVDD	4.6V

1.2 时序设定

Power on sequence

The Power on sequence has been applied following Fig1, otherwise correct functionality is not guaranteed.



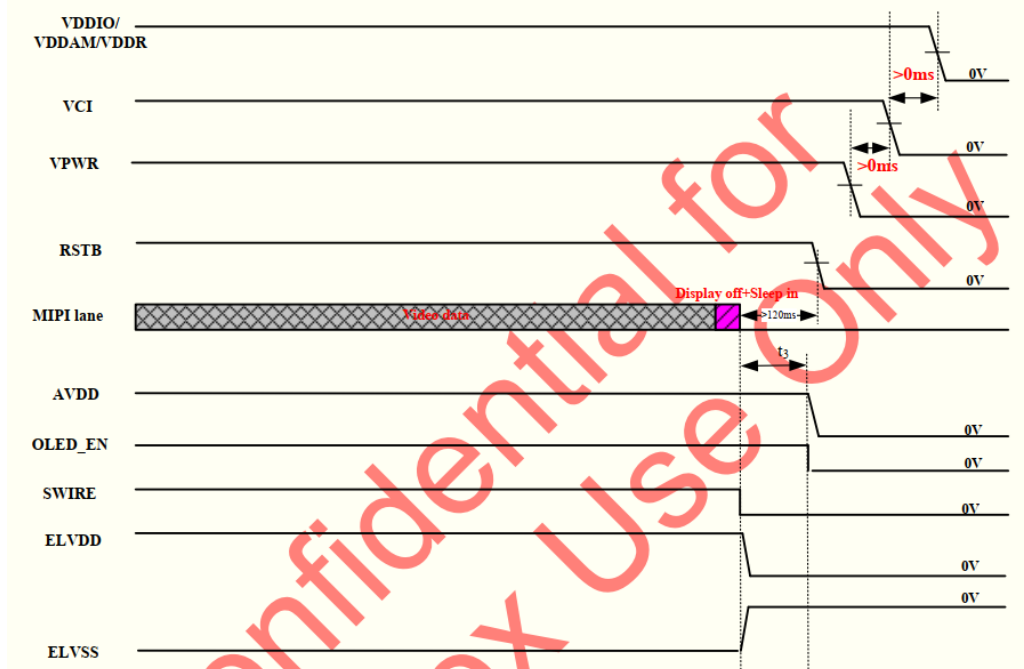
Note1: t_1 is AVDD set up time, is controlled by AVDD_ONT[7:0];

Note2: t_2 is ELVDD/ELVSS set up time, is controlled by SWIRE_ONF[5:0];

Note3: VPWR is the power of Power IC for AVDD/ELVDD/ELVSS;

Power off sequence

The Power off sequence have been applied following Fig2, otherwise correct functionality is not guaranteed.



Note1: t_3 is Power off Delay time, is controlled by AVDD_OFT[7:0];

Note2: VPWR is the power of Power IC for AVDD/ELVDD/ELVSS;

2 Initial Code

//*****Initial,Code*****//

0x35,0x00 //TE on

0x11 //sleep out

Delay 120ms

0x29 //display on

Delay 40ms

● 0x53,0x00 // Normal

● 0x53,0x02 // AOD

● 0x53,0xC0 // HBM