

INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

Integration support

Obsolescence Management

Embedded Systems

Human Machine Interface

Touch Solutions

Software

Quality Management

Open Frame Monitors

LCD Controller

Research & Development

Firmware

EMC tests

on-site service

In-house Manufacturing

Cover glass

System solutions

OEM Solutions

Custom Display

PCAP

Mechanical design

Production Custom designs Installation



SPECIFICATION

[] Preliminary Specification
[●] Final Specification

Description 12.1” 800xRGBx600 TFT-LCD Module
Part Number P1210SVF1MB00

Customer		Industrial Product Dept, PDBU Tianma Microelectronics Co., Ltd.	
Signatures	Date	Approved By	Date
		Kevin Kim	2025-3-12
		Reviewed By	
		Longping Deng	2025-3-12
		Prepared By	
		Anna Huang	2025-3-12
Comments:			

* This cover page is for your Comments and Signatures back to TIANMA.

REVISION HISTORY

Rev	Issued Date	Description	Editor
1.0	2021-5-6	Preliminary Specification release	Anna Huang
1.1	2022-1-4	Update Optical Characteristics at P16 Update electrical characteristics at P10~11 Update mechanical drawing at P20 Update marking at P21 Update size and weight for packing at P22	Anna Huang
1.2	2022-1-21	Modify Pin19 NC definition at P3	Anna Huang
1.3	2022-3-21	Chang Pin 19 NC=GND P3&P6	Anna Huang
1.4	2022-6-30	Change to new format ESD Spec change at P19	Anna Huang
	2022-7-28	Update Mechanical Drawing at P20 Update Optical Characteristics at P16	Anna Huang
1.5	2022-9-23	Final version release	Anna Huang
	2022-11-7	Interface add DE mode only at P2	Anna Huang
	2023-5-11	Correct 11.1 Packing at P22	Anna Huang
1.6	2024-11-29	Add UL NO at page 1	Anna Huang
2.0	2025-3-12	Module manufacture site change ShanghaiG5→WuHanG4.5 Label information update in page 21	Anna Huang

CONTENTS

1. SUMMARY.....	1
1.1 General Description.....	1
1.2 Features.....	1
2. GENERAL SPECIFICATIONS.....	2
3. INPUT / OUTPUT TERMINALS.....	3
3.1 CN1 Pin assignment (LCD Interface).....	3
3.2 CN2 Pin assignment (Back Light).....	4
4. ABSOLUTE MAXIMUM RATINGS	5
5. ELECTRICAL CHARACTERISTICS	6
5.1 DC Characteristics for Panel Driving.....	7
5.2 DC Characteristics for Backlight Driving.....	8
5.3 Recommended Power ON/OFF Sequence.....	9
5.4 LCD Module Block Diagram	10
6. DISPLAY COLORS AND INPUT DATA SIGNALS.....	11
7. TIMING CHARACTERISTICS	16
7.1 DC characteristics	12
7.2 LVDS AC Electrical Characteristics	13
7.3 LVDS DC Electrical Characteristics.....	13
7.4 Input timing.....	13
7.5 Data Input Format VESA Data Mapping.....	13
8. OPTICAL CHARACTERISTICS	16
9. RELIABILITY TEST.....	19
10. MECHANICAL DRAWING	20
11. PACKING INSTRUCTION.....	23
12. PRECAUTIONS FOR USE OF LCD MODULES.....	24
12.1 Handling Precautions.....	24
12.2 Storage precautions.....	24
12.3 Transportation Precautions	24
12.4 Screen saver Precautions.....	24
12.5 Safety Precautions	24

1. Summary

1.1 General Description

This is a 12.1 inch a-Si TFT-LCD module with Normal- Black technology. It is composed of a TFT-LCD panel, a driver circuit, PCB, and a LED backlight unit.

1.2 Features

- Ultra-wide viewing angle
- Interface: 1port LVDS, 6/8bit selectable
- LED driver integrated
- Surface treatment: Anti-Glare

- Acquisition product for UL62368-1/CSA C22.2 No.62368-1-03 (UL File No: E250878)
- Compliant with the European RoHS Directive (2011/65/EU) and Delegated Directive (2015/863/EU, Amending Annex II of 2011/65/EU)

2. General Specifications

Feature		Spec	Unit
Display Spec	Size	12.1 inches	
	Resolution	800(RGB)x600	
	Pixel Pitch	0.3075(H) × 0.3075(V)	mm
	TFT Active Area	246.0 (H) × 184.5 (V)	mm
	Technology Type	a-Si	
	Pixel Configuration	R.G.B Vertical Stripe	
	Display Mode	SFT, Normally Black	
	Surface Treatment	Anti-Glare	
	Viewing Direction	ALL	
Mechanical Characteristics	LCM (W x H x D)	279.0 (H) ×209.0 (V) ×9.0 (D)	mm
	Weight	492	g
Optical Characteristics	Luminance(typ)	500	cd/m ²
	Contrast Ratio(typ)	1000:1	
	NTSC(typ)	72	%
	Viewing Angle(typ)	88/88/88/88	degree
Electrical Characteristics	Interface	1port LVDS, 6/8bit selectable (DE mode only)	
	Color Depth	16.7M/262K	colors
	Power Consumption(typ)	LCD:1.14 Backlight:8.64	W

Table 2.1 General TFT Specifications

3. Input / Output Terminals

3.1 CN1 Pin assignment (LCD Interface)

Connector Information	
LCD Module connector	CN1: MSB240420HE (Produced by STM) or equivalent.
Matching connector	P240420H Terminal: 2404PS-00

Table 3.1.1 Connector information

Pin	Name	I/O	Description
1	VCC	P	3.3V Power Supply
2	VCC	P	3.3V Power Supply
3	GND	P	Ground
4	6-8Bit SET	I	Select 6 or 8 Bits LVDS Input (VCC/NC:8Bits ; GND: 6Bits)
5	RIN0-	I	Negative(-) LVDS differential data input
6	RIN0+	I	Positive(+) LVDS differential data input
7	GND	P	Ground
8	RIN1-	I	Negative(-) LVDS differential data input
9	RIN1+	I	Positive(+) LVDS differential data input
10	GND	P	Ground
11	RIN2-	I	Negative(-) LVDS differential data input
12	RIN2+	I	Positive(+) LVDS differential data input
13	GND	P	Ground
14	CLKIN-	I	Clock Signal(-)
15	CLKIN+	I	Clock Signal(+)
16	GND	P	Ground
17	RIN3-	I	Negative(-) LVDS differential data input (Used for 8Bits LVDS Input; GND for 6Bits)
18	RIN3+	I	Positive(+) LVDS differential data input (Used for 8Bits LVDS Input; GND for 6Bits)
19	REVERSE	I	Display Reversed Function (VCC: Display Reverse; GND/NC: Normal Display)
20	NC/GND	/	Test Function Pin(Do not set this pin to High)

Table 3.1.2 Pin Assignment for LCD Interface

Note1: I/O definition: I---Input, O---Output, P---Power/Ground, N---No connection

3.2 CN2 Pin assignment (Back Light)

Connector Information	
LCD Module connector	MSB24038P5 (Produced by STM) or equivalent.
Matching connector	P24038P5 Terminal: 24038PS-00

Table 3.2.1 Connector information

Pin	Symbol	I/O	Signal Name
1	VDD	P	12V
2	GND	P	GND
3	Enable	I	5V-On / 0V-Off
4	Dimming	I	PWM Dimming
5	NC	/	NC

Table 3.2.2 Pin Assignment for Back Light Interface

Note1: I/O definition: I---Input, O---Output, P---Power/Ground, N---No connection

4. Absolute Maximum Ratings

Item	Symbol	MIN	MAX	Unit	Remark
LCD Power Voltage	VCC	-0.3	5.0	V	
LVDS supply voltage	VCC_LVDS	0.5	5	V	Note5
Digital input voltage	-	-0.3	VCC+0.3	V	Note4
Backlight Power Voltage	VDD	-0.3	33	V	
Backlight Input Voltage	V _{IN_BL}	-0.3	5.5	V	Note1
Operating Temperature	TOPR	-20	+70	°C	Note2
Storage Temperature	TSTG	-30	+80	°C	
Relative Humidity Note2	RH	--	≤95	%	Ta≤40°C
		--	≤85	%	40°C<Ta≤50°C
		--	≤55	%	50°C<Ta≤60°C
		--	≤36	%	60°C<Ta≤70°C
		--	≤24	%	70°C<Ta≤80°C
Absolute Humidity Note3	AH	--	≤70	g/m ³	Ta>70°C

Table 4.1 Absolute Maximum Ratings

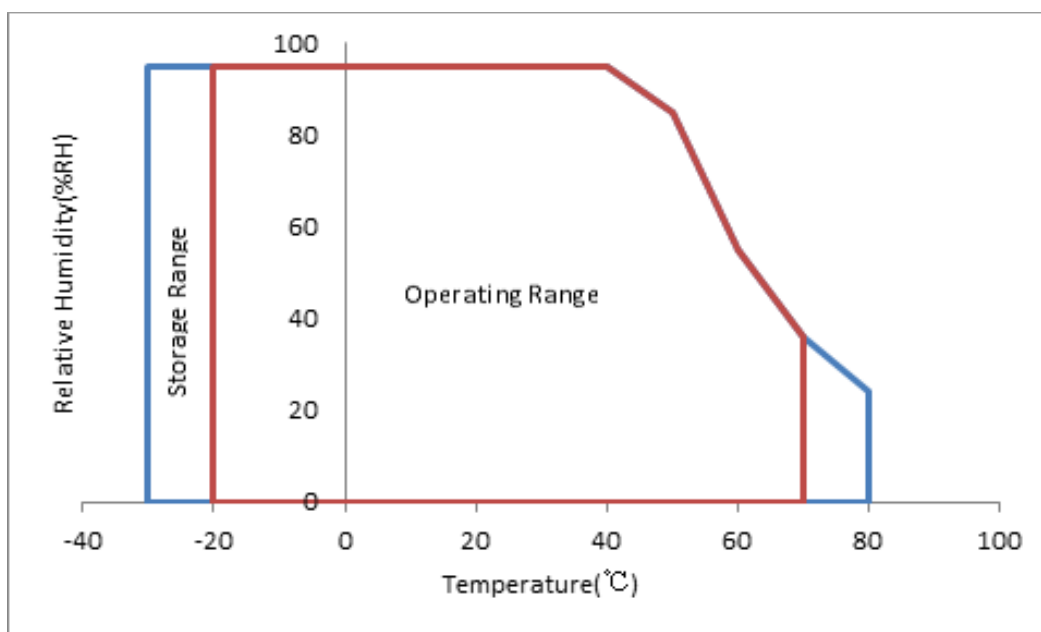


Table 4.2 Absolute Maximum Ratings chart

Note1: Backlight Input voltage include Dimming, Enable.

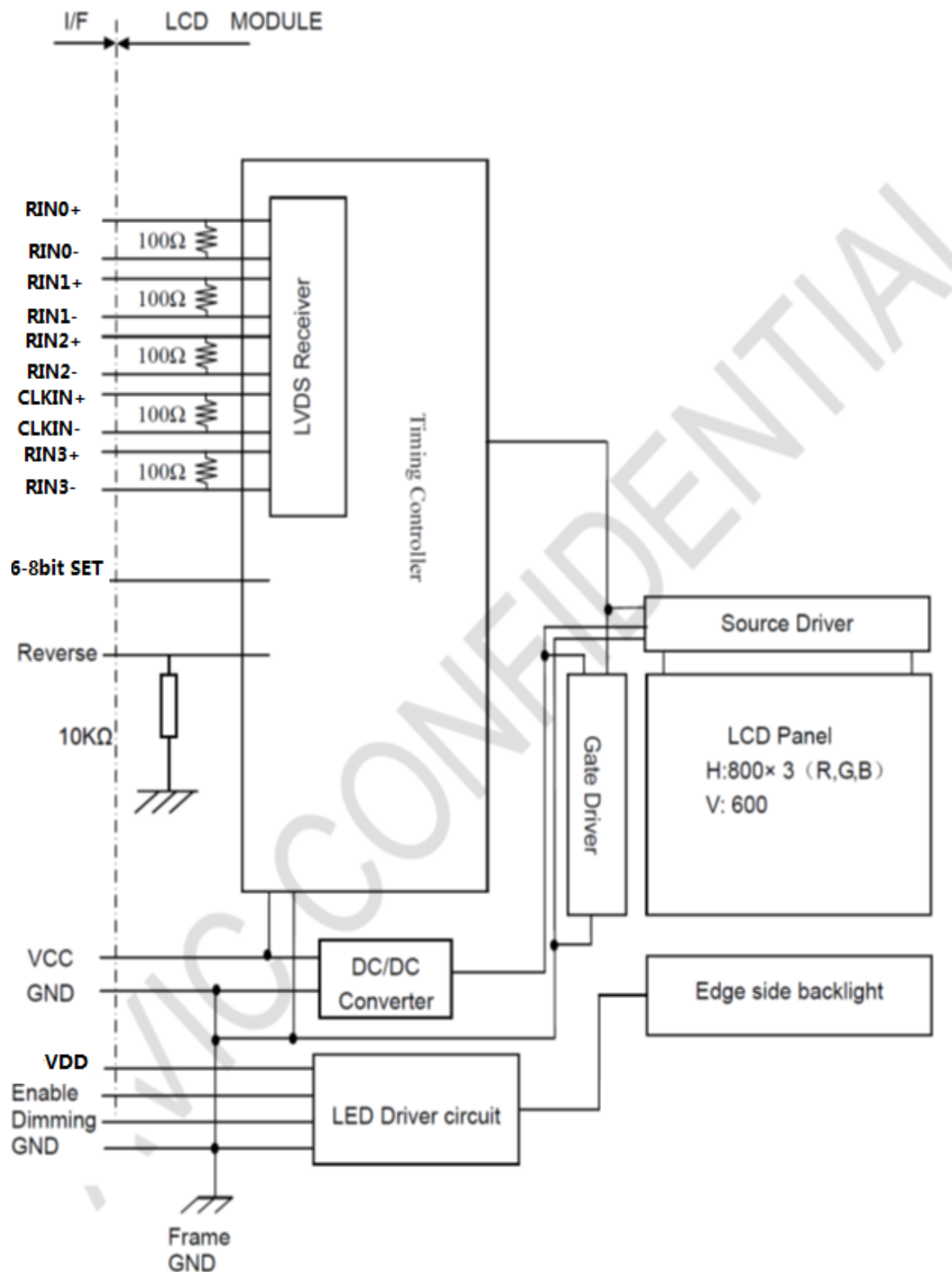
Note2: Ta means the ambient temperature. It is necessary to limit the relative humidity to the specified temperature range. Condensation on the module is not allowed.

Note3: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed

Note 4: Including 6-8Bit SET & REVERSE

Note5: Including R_{IN}±&CLK_{IN}±

5. Electrical Characteristics



Note1: System ground (GND), Frame ground in the product should be connected together in customer equipment.

5.1 DC Characteristics for Panel Driving

GND=0V, Ta=25°C

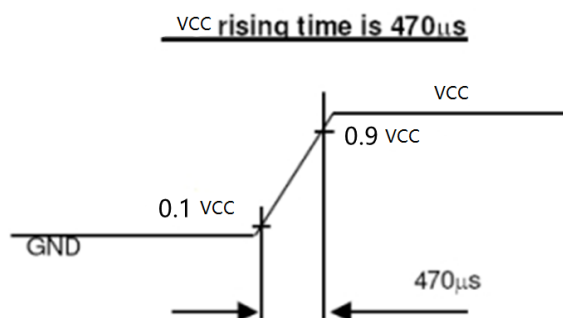
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage	VCC	3.0	3.3	3.6	V	-
Power supply current	ICC	-	345	450	mA	at VCC = 3.3V Note 1
LCD power consumption	P	-	1.14	1.49	W	
Logic Input Voltage	Low level	VIL	0	-	0.3VCC	Note 2
	High level	ViH	0.7VCC	-	VCC	
Permissible ripple voltage	VRP			200	mV	VCC
Rush current	I _{rush}			3	A	Note 3

Table 5.1.1 Operating Voltages

Note 1: All white pattern

Note 2: Including 6-8Bit SET & REVERSE

Note 3: Measurement Conditions:



5.2 DC Characteristics for Backlight Driving

Ta=25°C

Item		Symbol	Min	Typ	Max	Unit	Remark
Backlight power supply voltage		VDD	11.8	12	13.2	V	
Backlight power supply current		I_Total	-	720	1000	mA	Note1
Backlight power consumption		P_Total	-	8.64	12	W	
Input voltage for Dimming	High level	-	1.5	-	5	V	Input voltage for Dimming
	Low level	-	-	-	0.8	V	
Input voltage for Enable	High level	-	1.2	-	5	V	Input voltage for Enable
	Low level	-	-	-	0.35	V	
Dimming frequency		Fpwm	200	-	10K	HZ	
Dimming duty		D	5	-	100	%	Note2
PWM pulse width		-	5	-	-	us	
Inrush current		Inrush	-	-	4	A	Note5
LED Life Time		-	-	50000	-	H	Note6

Note 1: I_Total is the power supply current of LED driver, P_Total is the power consumption of LED driver and backlight.

Note 2: According to LED driver IC characteristics, the minimum value of VBR duty may vary with VBR frequency, higher the frequency, bigger the duty.

Note 3: Optical performance should be evaluated at Ta=70°C only with 100%PWM.

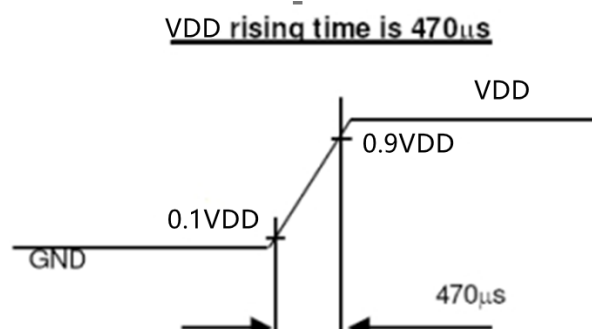
If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced.

Operating life means brightness goes down to 50% of initial brightness.

Operating life time is estimated data.

Note 4: Ta=25°C only with 100%PWM, the other condition should follow Note3.

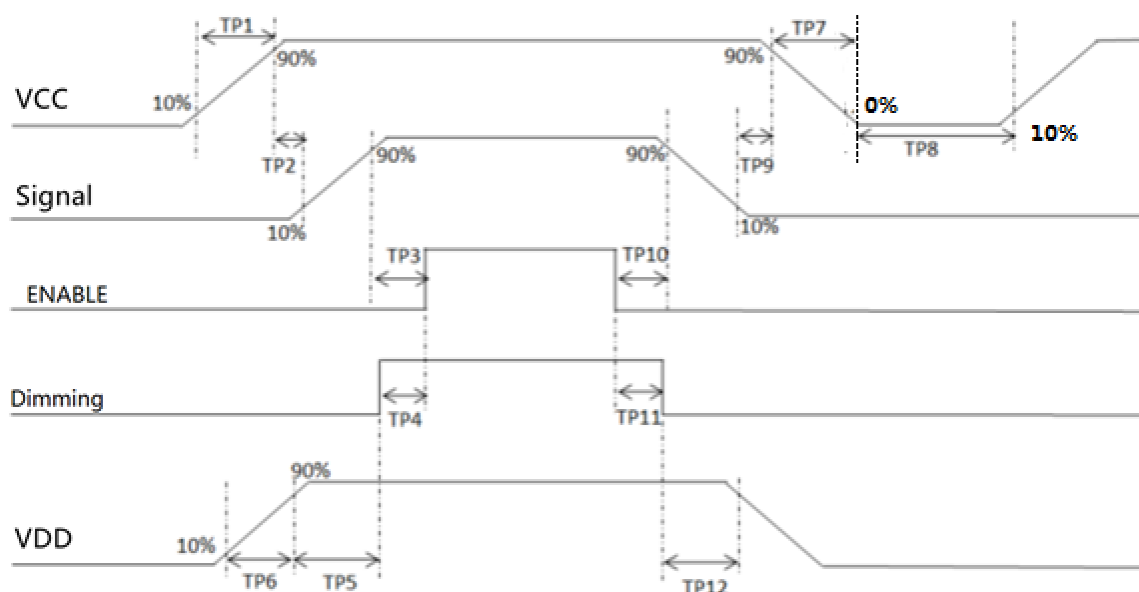
Note 5: In-rush current test conditions:



Note6: Optical performance should be evaluated at Ta=25°C. Only If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% of initial brightness. Typical operating life time is an estimated data.

5.3 Recommended Power ON/OFF Sequence

Item	Symbol	MIN	Typ	MAX	Unit	Remark
VCC on to VCC stable	Tp1	0.5	-	3	ms	
VCC stable to signal on	Tp2	20	-	100	ms	
Signal stable to ENABLE(BL_EN) on	Tp3	200	-	-	ms	
Dimming(BL_PWM) on to ENABLE(BL_EN) on	Tp4	0	-	200	ms	
VDD to Dimming(BL_PWM) on	Tp5	10	-	-	ms	
VDD on to VDD stable	Tp6	0.5	-	10	ms	
VCC off time	Tp7	0.5	-	10	ms	
VCC off to next VCC on	Tp8	500	-	-	ms	
Signal off before VCC off	Tp9	123	-	500	ms	
ENABLE(BL_EN) off before signal off	Tp10	200	-	-	ms	
ENABLE(BL_EN) off before Dimming(BL_PWM) off	Tp11	0	-	200	ms	
Dimming(BL_PWM) off before VDD off	Tp12	10	-	-	ms	



5.3.1 interface power on/off sequence

5.4 LCD Module Block Diagram

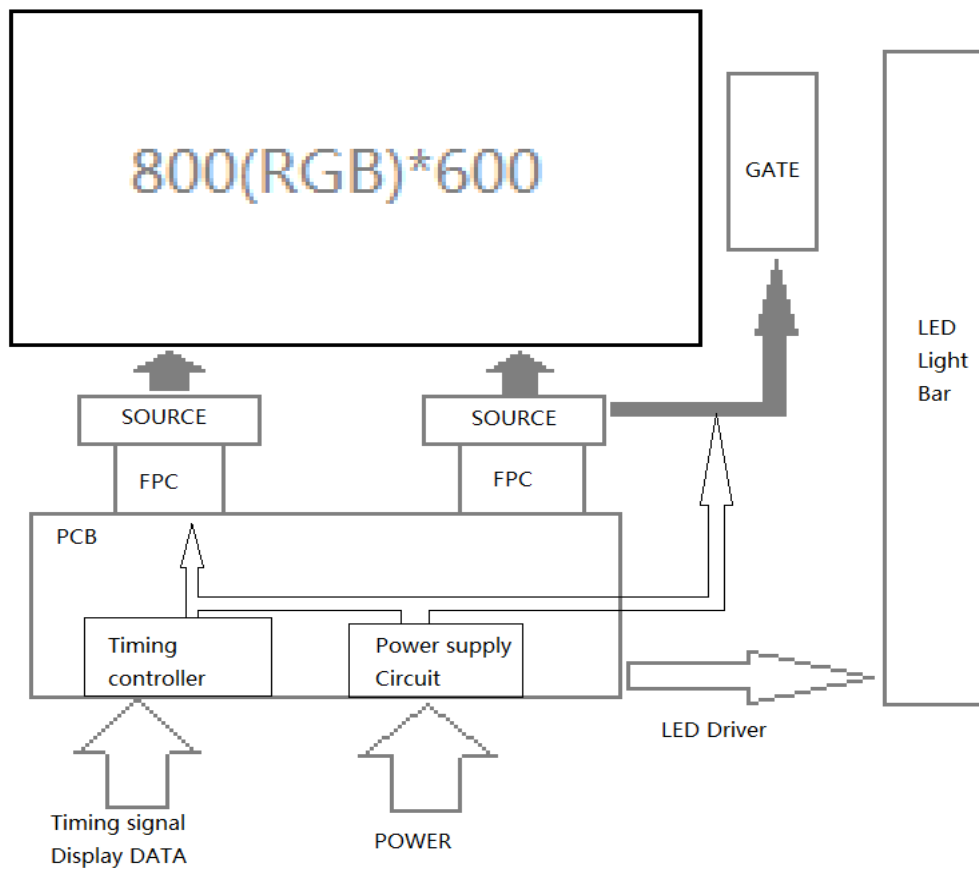


Figure 5.4.1 LCD Module Block Diagram

6. Display colors and input data signals

This product can display in equivalent to 16.7M colors in 256 scales. Also the relation between display colors and input data signals is as the following table.

Display colors		Data signal (0:Low level , 1:High Level)																							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue grayscale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

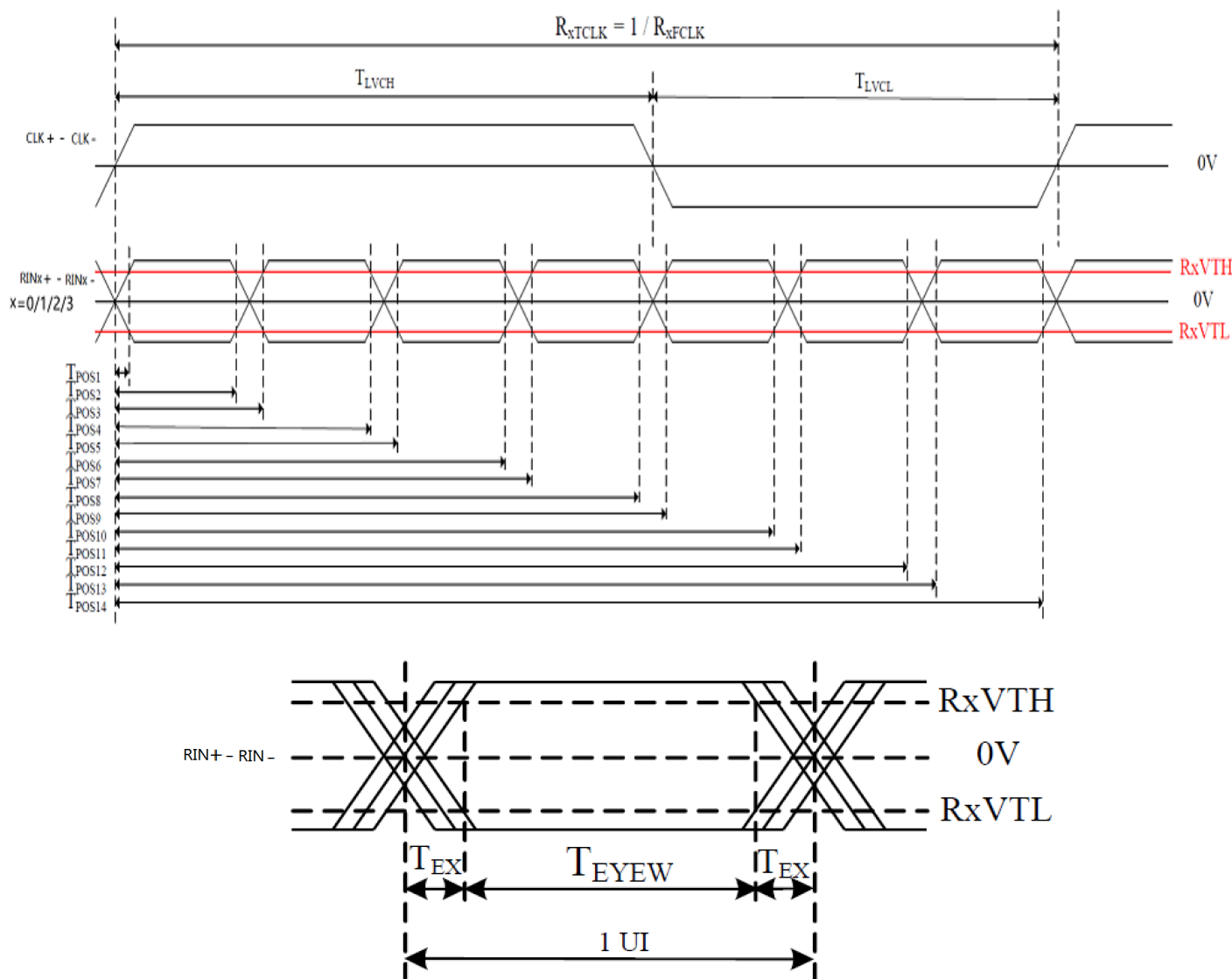
7. Timing Characteristics

7.1 DC Characteristics

Parameter	Symbol	Min	Typ.	Max.	Unit	Conditions
Power supply voltage	VCC	3.0	3.3	3.6	V	
Low level input voltage	VIL	0	-	0.3x VCC	V	CMOS I/F digital circuit
High level input voltage	VIH	0.7x VCC	-	VCC	V	CMOS I/F digital circuit
Input leakage current	ILEAK	-	-	+/-1	uA	CMOS I/F digital circuit
Pull low/high resistor	RPULL	180K	250K	320K	ohm	For the digital input pin @VCC=3.3V

7.2 LVDS AC Electrical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Clock Frequency	R _{xFCLK}	20		80	MHz	
Clock Period	R _{XTCLK}	12.5		50	ns	
1 data bit time	UI	-	1/7	-	R _{XTCLK}	
Clock high time	T _{LVCH}		4		UI	
Clock low time	T _{LVCL}		3		UI	
Position 1	T _{POS1}	-0.25	0	0.25	UI	
Position 2	T _{POS2}	0.75	-	1.25	UI	
Position 3	T _{POS3}	0.75	1	1.25	UI	
Position 4	T _{POS4}	1.75	-	2.25	UI	
Position 5	T _{POS5}	1.75	2	2.25	UI	
Position 6	T _{POS6}	2.75	-	3.25	UI	
Position 7	T _{POS7}	2.75	3	3.25	UI	
Position 8	T _{POS8}	3.75	-	4.25	UI	
Position 9	T _{POS9}	3.75	4	4.25	UI	
Position 10	T _{POS10}	4.75	-	5.25	UI	
Position 11	T _{POS11}	4.75	5	5.25	UI	
Position 12	T _{POS12}	5.75	-	6.25	UI	
Position 13	T _{POS13}	5.75	6	6.25	UI	
Position 14	T _{POS14}	6.75	-	7.25	UI	
Input eye width	T _{EYEW}	0.5	-	-	UI	
Input eye border	T _{EX}	-	-	0.25	UI	

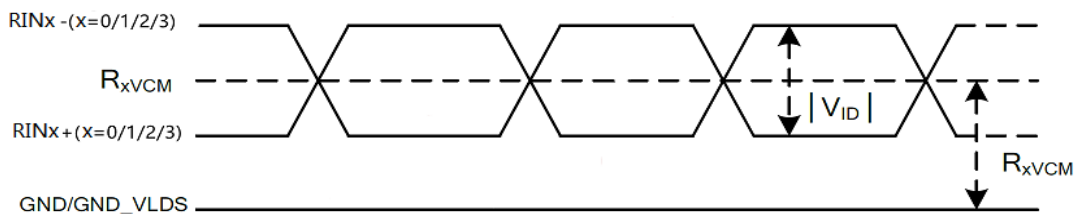


7.3 LVDS DC Electrical Characteristics

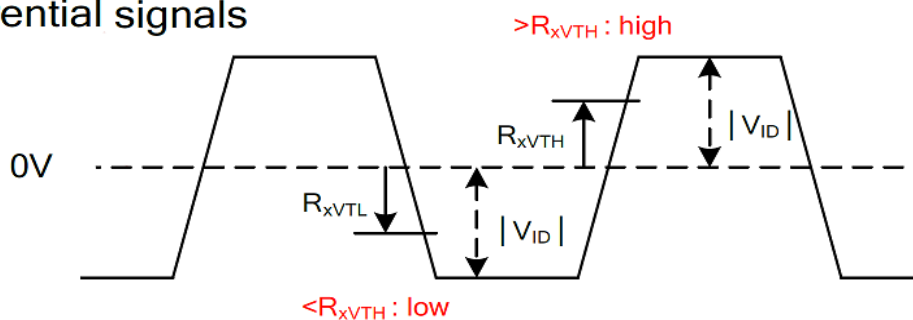
VCC=3.3V, GND=0V, Ta=25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Differential input high Threshold voltage	R_{xVTH}			0.1	V	
Differential input Low Threshold voltage	R_{xVTL}	-0.1			V	
Input voltage range(single-end)	R_{xVIN}	0		VCC-1.0	V	
Differential input common Mode voltage	R_{xVCM}	0.6	1.2	$2.4 - V_{ID} /2$	V	
Differential input voltage	$ V_{ID} $	0.2	0.4	0.6	V	
Differential input leakage current	R_{VxIz}	-10		10	uA	

Single end signals



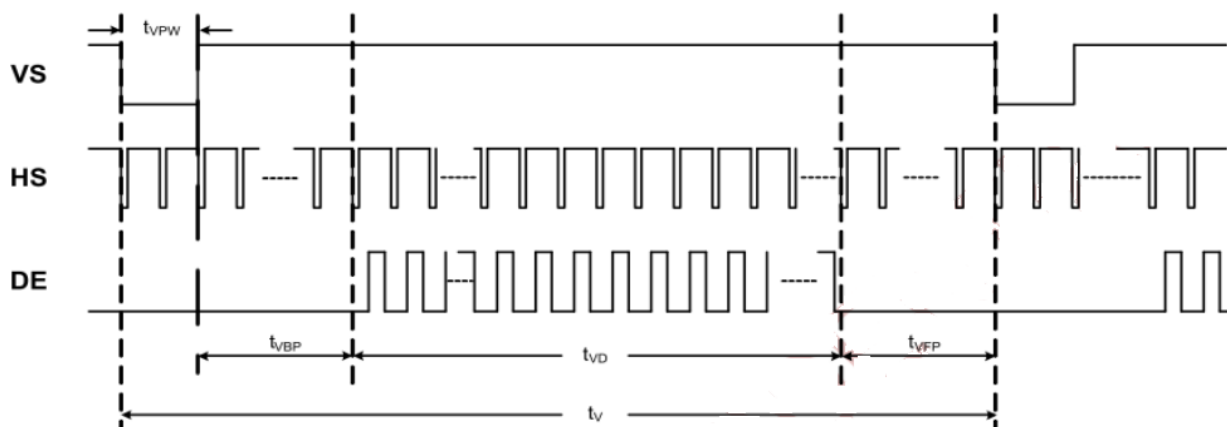
Differential signals



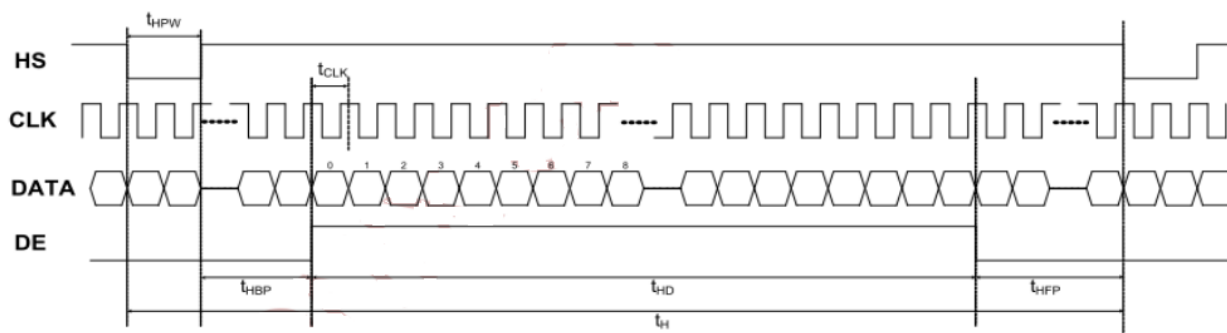
7.4 Input timing

Parameter	Symbol	MIN	Typ	MAX	Unit	Remark
CLK frequency	tclk	30.38	31.63	40.27	MHz	
Horizontal display area	thd	800			tclk	
Horizontal pulse width	thpw	2	2	8	tclk	
Horizontal back porch	thbp	8	16	40	tclk	
Horizontal front porch	thfp	16	16	100	tclk	
HSYNC period	th	826	834	948	tclk	
Vertical display area	tvd	600			th	
Vertical plus width	tpw	2	2	8	th	
Vertical back porch	tvbp	3	10	40	th	
Vertical front porch	tvfp	8	20	60	th	
VSD period	tv	613	632	708	th	
Frame rate	FR	60			Hz	

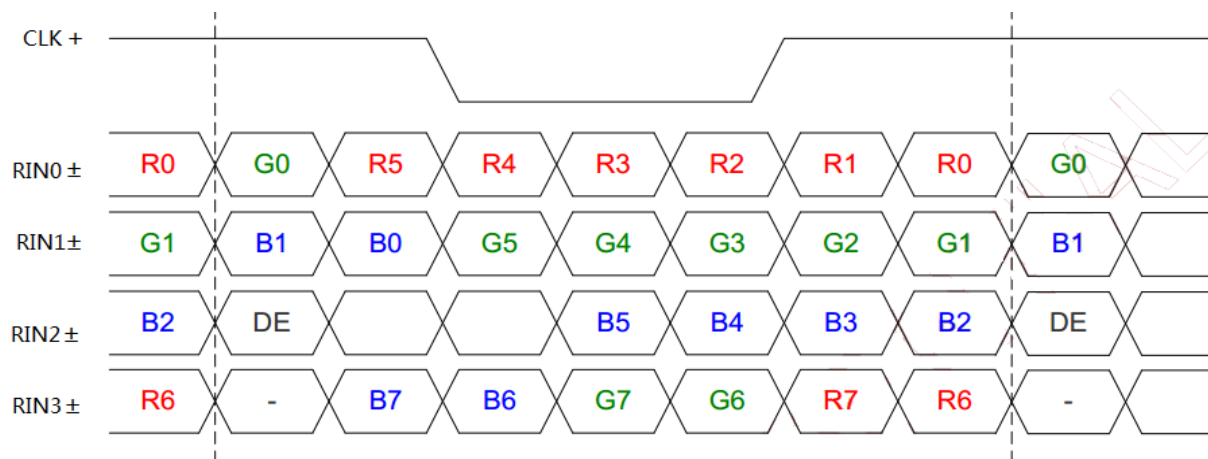
Vertical input timing



Horizontal input timing



7.5 Data Input Format VESA Data Mapping



8. Optical Characteristics

Item		Symbol	Condition	Min	Typ.	Max	Unit	Remark
View Angles		θT	CR≥10	80	88	-	Degree	Note 2
		θB		80	88	-		
		θL		80	88	-		
		θR		80	88	-		
Contrast Ratio		CR	θ=0°	700	1000	-	-	Note1 Note3
Luminance uniformity		U		70	80	%	-	Note6
Response Time		T _{ON}	25°C	-	25	35	ms	Note1 Note4
		T _{OFF}						
Chromaticity	White	x	Backlight is on	0.252	0.302	0.352	-	Note5 Note1
		y		0.281	0.331	0.381		
	Red	x		0.586	0.636	0.686		
		y		0.285	0.335	0.385		
	Green	x		0.239	0.289	0.339		
		y		0.565	0.615	0.665		
	Blue	x		0.101	0.151	0.201		
		y		0.011	0.061	0.111		
NTSC				65	72	-	%	Note5
Luminance		L		400	500	-	cd/m²	Note7

Table 7.1 Optical Parameters

Test Conditions:

1. The ambient temperature is 25°C. VDD= 3.3V, VCC=12V, 100% brightness,
2. The test systems refer to Note 1 and Note2.

Note1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical characteristics are measured at the center point of the LCD screen.

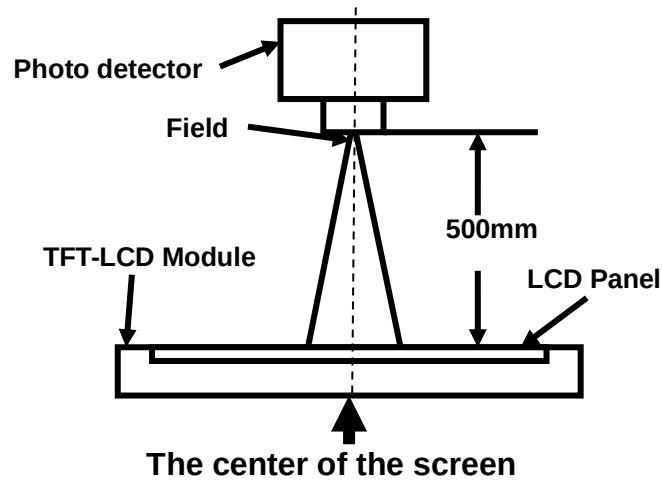


Fig1. Measurement Set Up

Note2: Definition of viewing angle range and measurement system. Viewing angle is measured at the center point of the LCD .

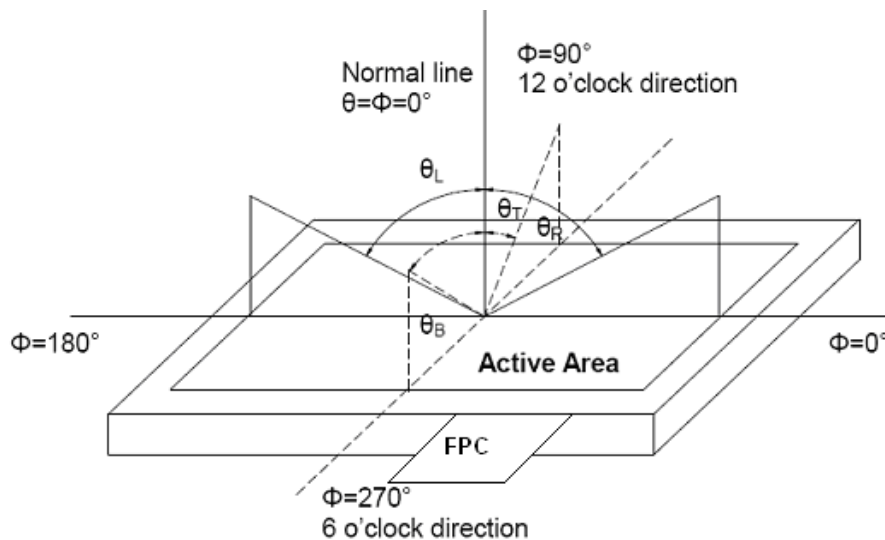


Fig2. Measurement viewing angle

Note3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

Note4: Definition of Response time

For SFT LCM, the response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_r) is the time between photo detector output intensity changed from 10% to 90%. And fall time (T_f) is the time between photo detector output intensity changed from 90% to 10%.

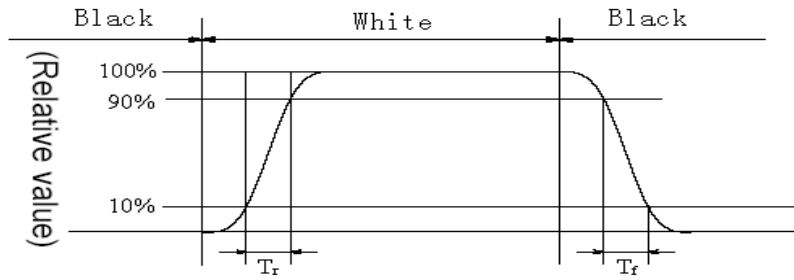


Fig4. Response Time Testing(SFT)

Note5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig.5). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = L_{min} / L_{max}

L_{max} : The measured Maximum luminance of all measurement position.

L_{min} : The measured Minimum luminance of all measurement position.

L-----Active area length; W----- Active area width

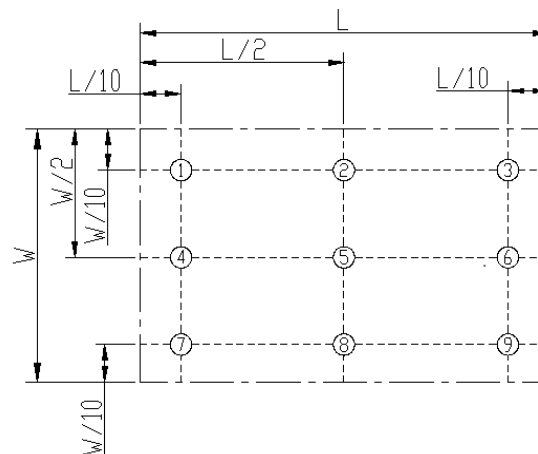


Fig5. Luminance Uniformity Measurement Locations(9 points)

Note7: Definition of Luminance:

Measure the luminance of white state at center point.

9. Reliability Test

No	Test Item	Condition	Remark
1	High Temperature & High Humidity Operation	60°C , 90%RH , 240hrs	IEC60068-2-78 GB/T2423.3
2	High Temperature Storage	+80°C, 240hrs	IEC60068-2-2 GB2423.2
3	Low Temperature Storage	-30°C, 240hrs	EC60068-2-1 GB2423.1
4	High Temperature Operation	+70°C , 240hrs	IEC60068-2-2,GB2423.2
5	Low Temperature Operation	-20°C , 240hrs	IEC60068-2-1 GB2423.1
6	Thermal Shock (Non-operation)	-30°C,30min;80°C,30min;1H/cycle, Change time:5min, 100 Cycles	Start with cold temperature nd with high temperature, EC60068-2-14,GB2423.22
7	Vibration	10~55HZ Stroke:1.5mm Sweep:10HZ~55HZ~10HZ 2H For X\Y\Z	IEC60068-2-6 GB/T2423.10
8	Package condition	5-20 -200HZ , PSD : 0.01-0.01 - 0.001 Total:0.781g2/HZ, x/y/z every direction every direction 30min	GB/T 4857.23
9	ESD	C=150pF,R=330Ω ; Air:±15kV,9points,25times/point ; Contact:±8kV,9points,25times/point	IEC61000-4-2 GB/T17626.2

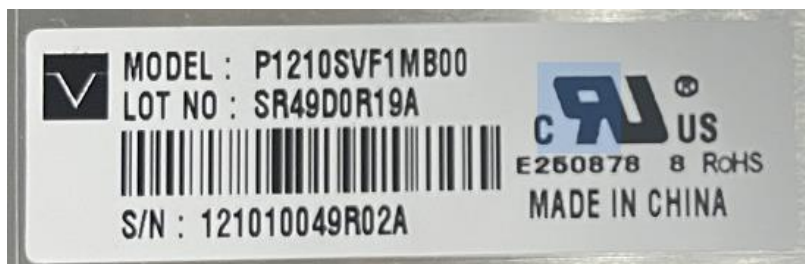
Table 8.1 RA test condition

Note1: Before cosmetic and function test, the product must have enough recovery time, at least 24 hours at room temperature.

Note2: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.



10.1 Marking---Product label



UL MARK

ROHS

Country of manufacture

Note: Add 8 between UL NO&ROHS

Note1: The meaning of OEM number, Example:

LOT NO : SR49D0R19A

SR	4	9	D	0	R	19A
Rev: SR or A Sample: SR Mp:A	Year: 0~9	Month: 1~9,A,B,C	Fixed	S rank	Day: 1~9,A~V	Line &Shift

SN: 121010049R02A

1210100	4	9	R	02A
TM internal model NO mark	Year: 0~9	Month: 1~9,A,B,C	Day: 1~9,A~V	Serial NO: 000~ZZZ

11. Packing Instruction

AVIC will pack products to deliver to customer in accordance with AVIC packing specifications, and will deliver products to customer in such a state that products will not suffer from a damage during transportation .The delivery conditions are as follows.

11.1 PACKING

(1) Packing box

20 products are packed up with the maximum in a packing box(See “**11.5 OUTLINE FIGURE FOR PACKING**”).

Products are put into a plastic bag for prevention of moisture with cushion, and then the bag is sealed up with heat sealing.

The type name and quality are shown on outside of the packing box, either labeling or printing.

(2) Pallet Packing (See“**11.5 OUTLINE FIGURE FOR PACKING**”)

① Packing boxes are tired on a cardboard pallet.(4 boxes×4 tiers maximum)

② Cardboard sleeve and top cap are attached to the packing boxes, then they are fixed by a band.

11.2 INSPECTION RECORD SHEET

Inspection record sheets are included in the packing box with delivery products to customer. It is summarized to a number of products for pass/fail assessment.

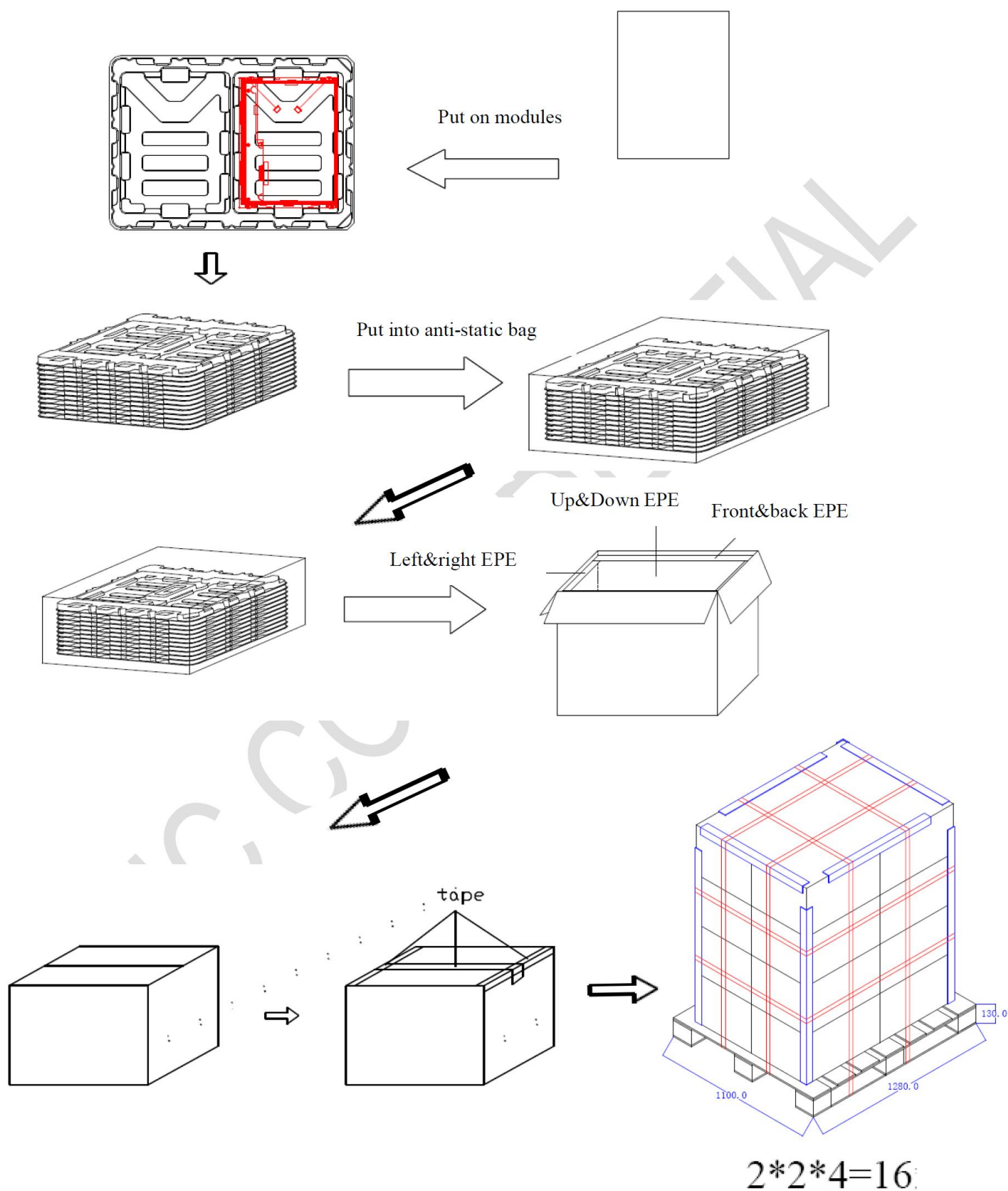
11.3 TRANSPORTATION

The product is transported by vehicle, aircraft or shipment in the state of pallet packing.

11.4 SIZE AND WEIGHT FOR PACKING BOX

Parameter	Packing box	Unit
Size	577 (L) ×421 (W) ×326 (H) (typ.)	mm
LCD Weight	0.492±5% (typ.)	kg
Total weight	14.48 (typ.)	kg

11.5 OUTLINE FIGURE FOR PACKING



12. Precautions for Use of LCD Modules

12.1 Handling Precautions

- (1) The display panel is made of glass. Do not subject it to mechanical shock by dropping it, etc.
- (2) If the display panel is damaged and the liquid crystal fluid inside it leaks out be sure not to get any in your mouth. If the fluid comes into contact with your skin or clothes promptly wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the bezel since this may cause the color tone to vary.
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle the polarizer carefully.
- (5) If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is still not completely clear use a moist cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol
 Solvents other than those mentioned above may damage the polarizer. Specifically, do not use the following:
 - Water
 - Ketone
 - Aromatic solvents
- (6) Do not disassemble the LCD Module.
- (7) If powered off, do not apply the input signals.
- (8) To prevent destruction of the module by static electricity, be careful to maintain an optimum work environment.
- (9) Be sure to ground your body when handling the LCD Modules.
- (10) Tools used for assembly, must be properly grounded.
- (11) To reduce the amount of static electricity generated, do not conduct assembly or other work under very low humidity conditions.
- (12) The LCD Module is covered with a film to protect the display surface, remove film slowly under the ionizer.

12.2 Storage precautions

- (1) When storing the LCD modules avoid exposure to direct sunlight or to the light of fluorescent lamps.
- (2) The LCD modules should be stored within the rated storage temperature range. The recommend condition is: Temperature: 0 ~ 35 °C at normal humidity.
- (3) The LCD modules should be stored in a room without acid, alkali or other harmful gas.

12.3 Transportation Precautions

The LCD modules should not be dropped or subject to violent mechanical shock during transportation. Also they should avoid excessive pressure, water, high humidity and direct sunlight.

12.4 Screen saver Precautions

Not display the fixed pattern for a long time. Use a screen saver, if the fixed pattern is displayed on the screen

12.5 Safety Precautions

- (1) When you waste damaged or unnecessary LCDs, it is recommended to crush LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned
- (2) Be sure to turn off the power supply when inserting or disconnecting the LED backlight cable.

- (3) LED driver should be designed to limit or stop its function when over current is detected on the LED.

Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

FORTEC

GROUP

FORTEC Elektronik AG | Augsburger Straße 2b | 82110 Germering
+49 89 894450-0
info@fortecag.de | www.fortecag.de

FORTEC

INTEGRATED

FORTEC Integrated GmbH | Augsburger Straße 2b | 82110 Germering
+49 89 894363-0
info@fortec-integrated.de | www.fortec-integrated.de

FORTEC

POWER

FORTEC Power GmbH | Lise-Meitner-Straße 3 | 64560 Riedstadt
+49 6158 8285-0
weborder@fortec-power.de | www.fortec-power.de

FORTEC

EGYPT

FORTEC Electronic Design and Solution Egypt SMLC | Linx Business Park
Unit B318 | Smart Village | Giza Governorate
info@fortec-integrated.de | www.fortec-integrated.de

Autronic Steuer- und Regeltechnik GmbH | Siemensstraße 17
74343 Sachsenheim
+49 7147 24-0
vertrieb@autronic.de | www.autronic.de

FORTEC

CZECH REPUBLIC

FORTEC Czech Republic s.r.o. | Přátelství 275 | 330 02 Dýšina
+49 89 894363-0
info@fortec.cz | www.fortec.cz

FORTEC

UNITED STATES

FORTEC United States, Corp. | 87 Raynor Avenue Unit 1 | Ronkonkoma
NY | 11779 | +1 631 5804360
info@fortec.us | www.fortec.us

FORTEC

SWITZERLAND

FORTEC Switzerland AG | Bahnhofstraße 3 | 5436 Würenlos
+41 44 7446111
info@fortec.ch | www.fortec.ch

FORTEC

UNITED KINGDOM

FORTEC Technology UK Ltd. | Osprey House | 1 Osprey Court
Hinchbrooke Business Park | Huntingdon | Cambridgeshire | PE29 6FN
+44 1480 411600
info@fortec.uk | www.fortec.uk