

INDUSTRIAL STRENGTH... Start Your Application From YOURITECH

Integration support

Obsolescence Management

PCAP

Embedded Systems

Human Machine Interface

Touch Solutions

Software

Production Custom designs Installation

Quality Management

Open Frame Monitors

LCD Controller

Research & Development

Firmware

EMC tests

on-site service

In-house Manufacturing

Optical Bonding

Cover glass

System solutions

OEM Solutions

Custom Display

Mechanical design



SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1YBV05-00
Date	March 30, 2020

TYPE : TN0216ANVNANN-GN00
< 2.16 inch Reflective (slightly transmissive)
Dot Matrix Memory LC Display>

CONTENTS

1. Application
2. Construction and Outline
3. Mechanical Specifications
4. Absolute Maximum Ratings
5. Optical Characteristics
6. Electrical Characteristics
7. Address Mapping
8. Interface Signals
9. Input Timing Characteristics
10. Warranty
11. Precautions for Use
12. Reliability test data
13. Outline drawing



KYOCERA CORPORATION

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.	
	Prepared	Checked	Approved	Checked	Approved
March 30, 2020	T. Onodera	Y. Akimura	A. Inoue	S. Kishimoto	O. Sato

Spec No. TQ3C-8EAF0-E1YBV05-00	Part No. TN0216ANVNANN-GN00	Page -
-----------------------------------	--------------------------------	-----------

Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss.
Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Spec No. TQ3C-8EAF0-E1YBV05-00	Part No. TN0216ANVNANN-GN00	Page -
-----------------------------------	--------------------------------	-----------

Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.	
		Prepared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page	Descriptions			

Spec No. TQ3C-8EAF0-E1YBV05-00	Part No. TN0216ANVNANN-GN00	ページ 1
-----------------------------------	--------------------------------	----------

1. Application

This document defines the specification of TN0216ANVNANN-GN00. (RoHS Compliant)

2. Construction and Outline

LCD : Reflective (slightly transmissive) dot matrix memory LC display
 Backlight System : None
 Polarizer : Glare type
 Additional circuit : 1-bit pixel memory function in LCD

3. Mechanical Specifications

Item	Specification	Unit
Outline dimensions 1)	51.10(W) × 31.85(H)	mm
Glass thickness	0.5 + 0.5	mm
Active area	48.00(W) × 26.40(H) (Diagonal 2.16 inch)	mm
Dot format	320(W) × 176(H)	Dot
Dot pitch	150(W) × 150(H)	μm
Color	Black-and-White (Binary)	-
LC Mode	ECB mode(Normally black)	-
Weight	5.0	g

1) FPC is not included. Please refer to the drawing for details.

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage	V _{DD}	-0.3	4.0	V
Input signal voltage 1)	V _{IN}	-0.3	4.0	V

1) Input signals : SCLK, SI, SCS, RST, VCOM

4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature 1)	T _{OP}	-25	80	°C
Storage temperature 2)	T _{STO}	-30	85	°C
Operating humidity 3)	H _{OP}	10	4)	%RH
Storage humidity 3)	H _{STO}	10	4)	%RH

- 1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.
- 2) Store LCD at normal temperature/humidity. Keep them free from vibration and shock. An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard. (Please refer to "Precautions for Use" for details.)
- 3) Non-condensing
- 4) Temp. ≤40°C , 85%RH Max.
Temp. >40°C, Absolute humidity shall be less than 85%RH at 40°C

5. Optical Characteristics

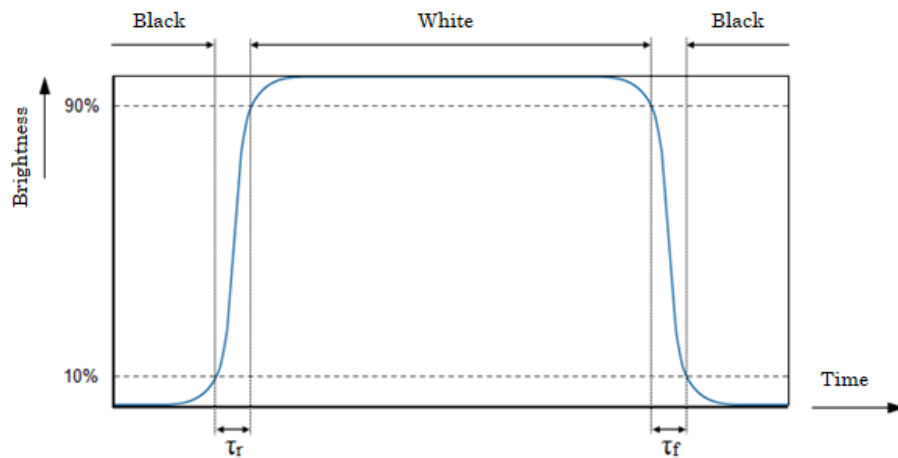
Measurement spot = $\phi 6.0$ mm, Temp. = 25°C

Item	Symbol	Temp.	Unit	Min.	Typ.	Max.
Contrast ratio 1)	CR	25°C	-	-	40	-
Reflectance	-	25°C	%	-	17	-
Response time 2)3)	Rise	τ_r	msec	-20°C	-	16
				25°C	-	12
				40°C	-	12
	Fall	τ_f	msec	-20°C	-	32
				25°C	-	13
				40°C	-	12
Viewing angle CR ≥ 2 4)5)	θ_{upper}	25°C	deg.	-	60	-
	θ_{lower}			-	60	-
	θ_{left}			-	60	-
	θ_{right}			-	60	-
Chromaticity coordinates	Wx	25°C	-	-	0.31	-
	Wy			-	0.34	-

1) Definition of contrast ratio

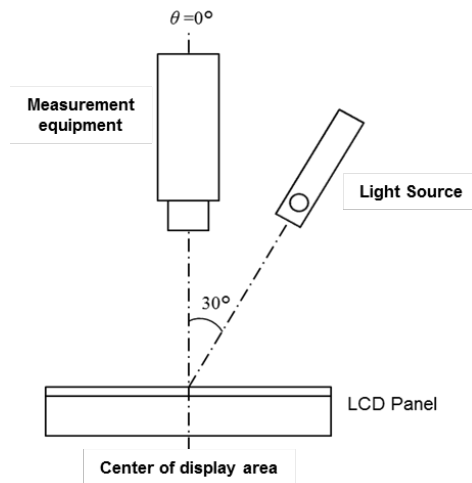
$$CR(\text{Contrast ratio}) = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

2) Definition of response time

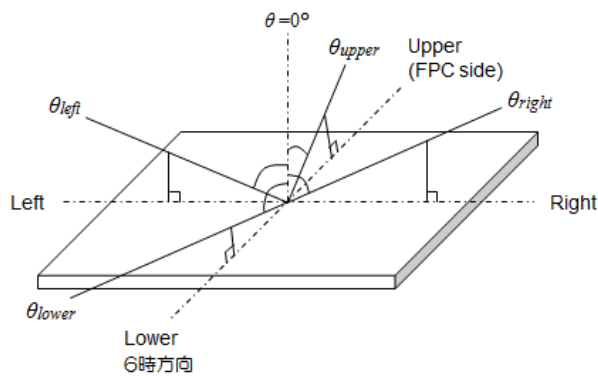


3) About the measurement of Response time

Measurement Equipment (LCD-5200)



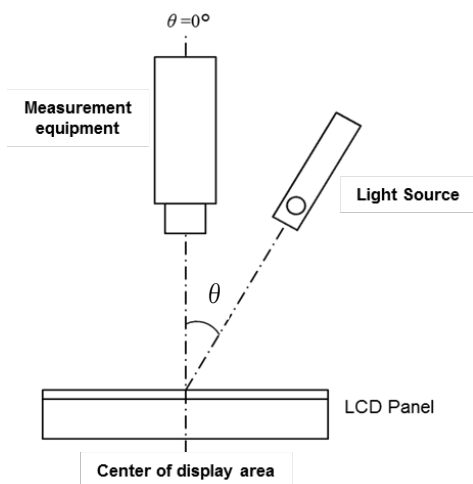
4) Definition of viewing angle



5) About the measurement of viewing angle

Measurement Equipment (LCD-5200)

The viewing angle is measured by changing the angle of the light source.



6. Electrical Characteristics

6-1. LCD driving characteristics

Temp. = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	V _{DD}	—	3.0	3.3	3.6	V
Input signal voltage 1)2)	V _{IL}	“Low” level	GND-0.2	—	VDD*0.25	V
	V _{IH}	“High” level	VDD*0.75	—	VDD+0.2	V
Input leak current 1)	I _{IN}	T _{OP} =25°C	—	5	20	nA
Current consumption 3)	I _{DD_opr}	4)	—	22	45	μA
	I _{DD_stb}	5)	—	1.5	5.5	μA
Input capacitance 1)	C _{IN1}	—	—	5	10	pF

1) SCLK, SI, SCS, RST, VCOM

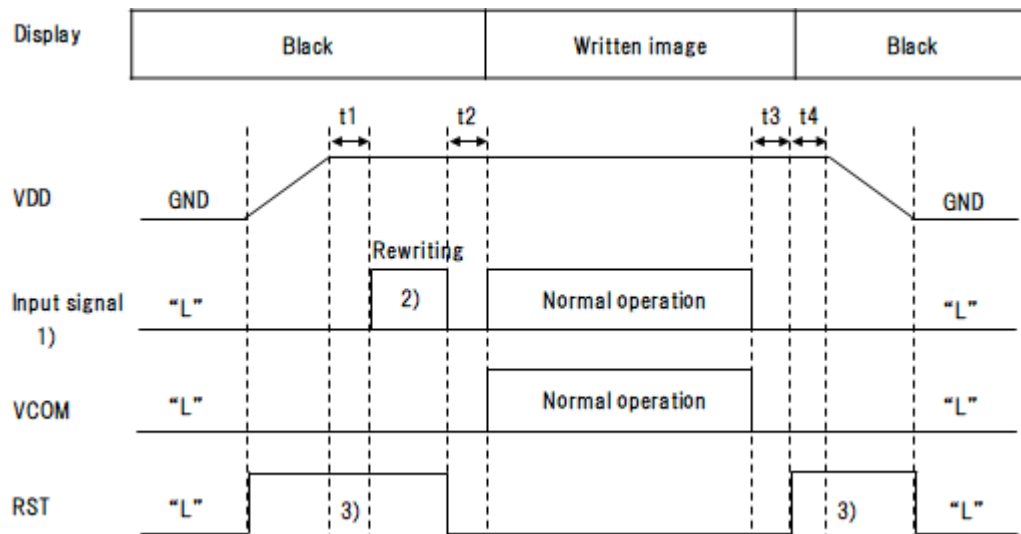
2) Since input leakage current is less than 20 nA, please control voltage so that V_{IL}=GND and V_{IH}=VDD in static state.

3) Value of the current consumption is without smoothing capacitor.

4) 1Hz full display rewriting (Without backlight)

5) Keeping static image, and polarity inversion of VCOM with a period of 1 sec.
(Without backlight)
SCLK, SCS, SI, RST: “L” state.

6-2. Power ON-OFF sequence

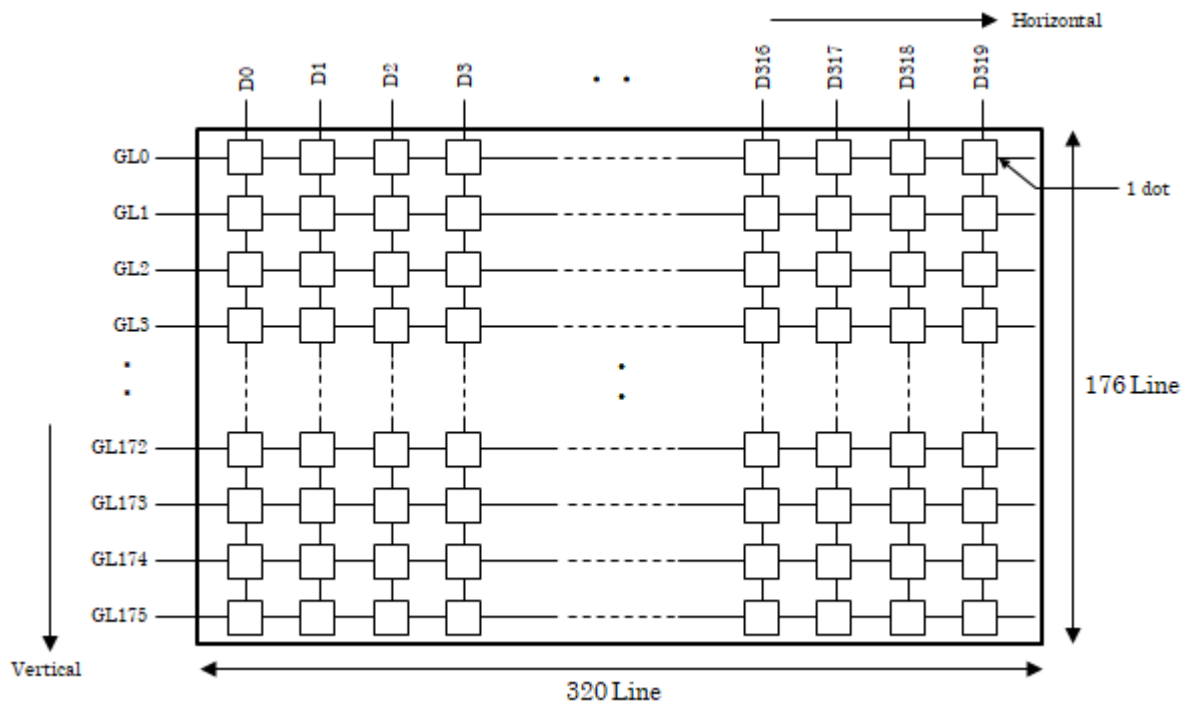


Item	Symbol	Min.	Typ.	Max.	Unit
Power ON-OFF sequence	t_1	0	—	—	μsec
	t_2	1	10	—	msec
	t_3	1	10	—	msec
	t_4	1	10	—	msec

- 1) SCLK, SI, SCS
- 2) Rewriting to black for all addresses.
- 3) Display OFF when RST = "H". VCOM = "L" is necessary when RST = "H". Black display when display is OFF.

Data in pixel memory is random with black or white at power ON.
Possible to prevent black-white random data display by black display when RST = "H".

7. Address Mapping



Addressing of Gate line (GL*) to be selected is necessary for vertical direction.
Rewriting data for one line is necessary for horizontal direction due to rewriting one line at once.

-Addressing table for vertical direction

AG7	AG6	AG5	AG4	AG3	AG2	AG1	AG0	Selected GL
0	0	0	0	0	0	0	0	GL0
0	0	0	0	0	0	0	1	GL1
0	0	0	0	0	0	1	0	GL2
0	0	0	0	0	0	1	1	GL3
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮
1	0	1	0	1	1	0	1	GL173
1	0	1	0	1	1	1	0	GL174
1	0	1	0	1	1	1	1	GL175

8. Interface Signals

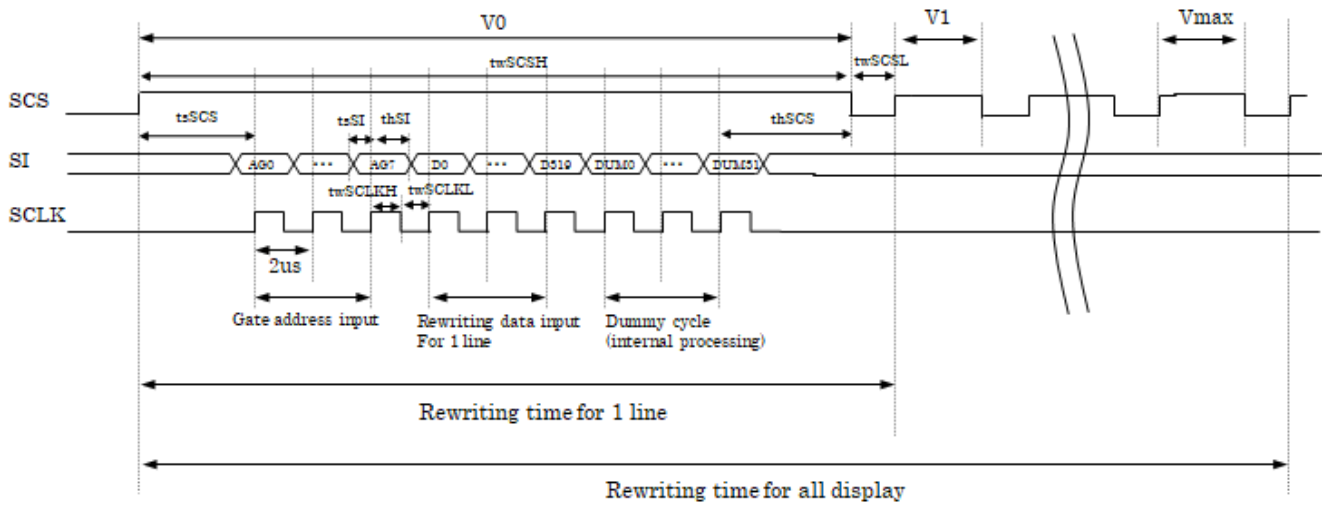
No.	Symbol	Description	I/O 1)	Voltage	Unit	Note
1	VDD	Power supply (3.3 V)	P	3.3	V	
2	VSS	GND	P	0.0	V	
3	SCLK	Clock signal for serial input	I	0.0/3.3	V	3)
4	SCS	Chip select signal	I	0.0/3.3	V	3)
5	SI	Serial input signal	I	0.0/3.3	V	
6	RST	Display ON/OFF signal	I	0.0/3.3	V	2) 3)
7	VCOM	Common power supply control	I	0.0/3.3	V	
8	VSS	GND	P	0.0	V	
9	VDD	Power supply (3.3 V)	P	3.3	V	

Matching connector: FH34SRJ-9S-0.5SH(50) (HIROSE)

- 1) P: Power supply
I: Input
- 2) RST = "L": Display ON
RST = "H": Display OFF (Black display keeping pixel memory)
VCOM = "L" is necessary when RST = "H"
(When VCOM="H", Display does not turn to black, and current consumption increases by shoot-through-current in panel. (several mA))
- 3) Need to guard from signal noise
If there is an abnormal signal not described in the timing chart on these signals, display may be distorted. Please carefully guard these signals since even if signal noise with small pulse width may cause malfunction.

9. Input Timing Characteristics

9-1. Rewriting timing <SCS activating period: 1 line>



3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

1. Input gate address
2. Input rewriting data for 1 horizontal line
3. Dummy cycle (internal processing for rewriting)

Relationship between rewriting data and display.

Data 0: Display BLACK

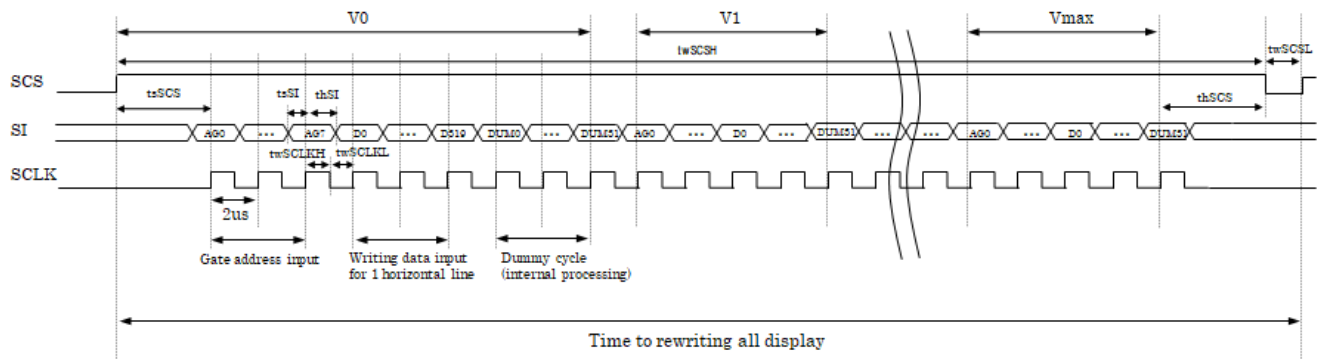
Data 1: Display WHITE

Time to rewrite in case of SCLK frequency = 1.5MHz (twSCLKH=twSCLKL=0.33us)

No.	Item	Parameters
Serial data input (1 line)	Gate address	AG0~AG7
	Rewriting data	D0~D319
	Dummy cycle	DUM0~DUM31
Total cycle for 1line		360 cycs
Time to rewrite 1 line		5.247 msec
Number of gate line		176 lines
Time to rewrite full display		923.5 msec

9-2. Rewriting timing <SCS activating period: 1 frame>

Rewriting V_0 to V_{max} continuously during SCS activating period



3 lines (SCS, SCLK, and SI) are control signals to rewrite.

Rewriting 1 gate line by 1 gate line.

Input order of serial data

1. Input gate address
2. Input rewriting data for 1 horizontal line
3. Dummy cycle (internal processing for rewriting)

Possible to rewrite numerous lines continuously during SCS activating period.
Gate addresses for continuous rewriting of numerous lines can be set in any order.

Relationship between rewriting data and display.

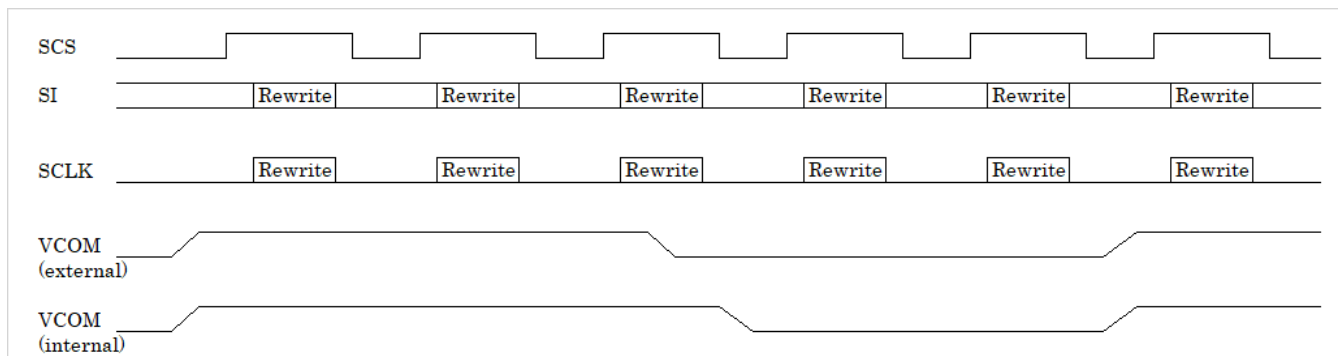
Data 0: Display BLACK

Data 1: Display WHITE

Time to rewrite in case of SCLK frequency = 1.5MHz ($twSCLKH=twSCLKL=0.33\mu s$)

No.	Item	Parameters
Serial data input (1 line)	Gate address	AG0~AG7
	Rewriting data	D0~D319
	Dummy cycle	DUM0~DUM31
Total cycle for 1line		360 cycs
Number of gate line		176 lines
Time to rewrite full display		46.8 msec

9-3. VCOM AC driving



AC drive between 0 V and 3.3 V is required for VCOM signal.

External VCOM switching is reflected immediately on VCOM inside the panel when switching is performed at SCS = "L", however when switching is performed in SCS = "H", external VCOM switching is reflected at the next timing when SCS = "L".

9-4. Timing characteristics

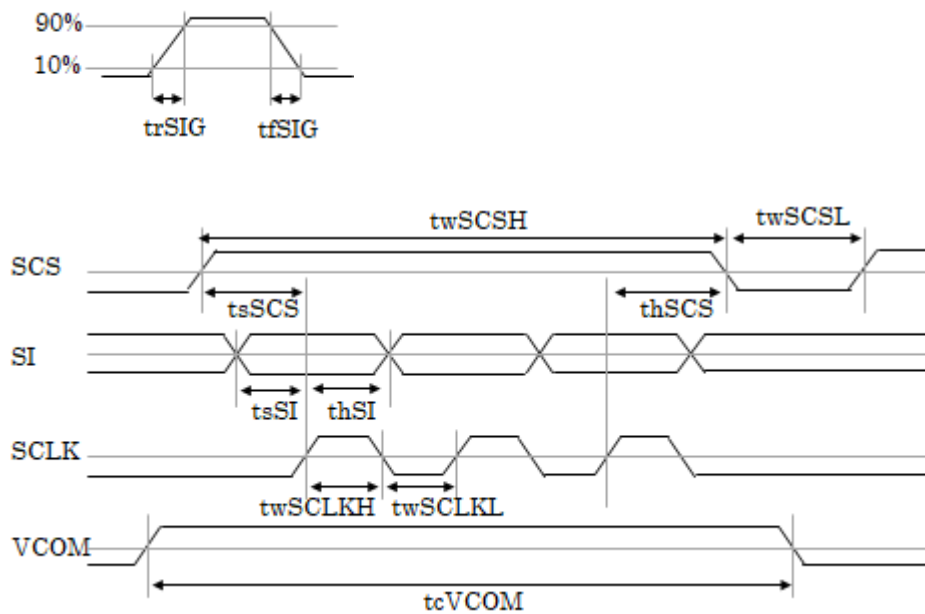
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Time to rewrite 1 line			5.247	—	—	msec	
Time to rewrite full display			46.8	—	—	msec	
Input signal 1)	Signal rise time	trSIG	—	—	50	nsec	2)
	Signal fall time	tfSIG	—	—	50	nsec	2)
SCLK	SCLK high width	twSCLKH	0.33	—	—	μsec	3)
	SCLK low width	twSCLKL	0.33	—	—	μsec	3)
SI	SI set-up time	tsSI	0.33	—	—	μsec	3)
	SI hold time	thSI	0.33	—	—	μsec	3)
SCS	SCS high width	twSCSH	5.237	—	—	msec	3)
	SCS low width	twSCSL	10	—	—	μsec	3)
	SCS set-up time	tsSCS	4	—	—	msec	3)
	SCS hold time	thSCS	1	—	—	msec	3)
VCOM	VCOM duty	—	—	50	—	%	
	VCOM cycle time	tcVCOM	—	1000	—	msec	3)

1) SCLK, SI, SCS, RST, VCOM

2) Time for the signal to shift from 10% and 90% or 90% to 10%

3) Defined as 50% value of the signal.

-Timing charts



Spec No. TQ3C-8EAF0-E1YBV05-00	Part No. TN0216ANVNANN-GN00	ページ 13
-----------------------------------	--------------------------------	-----------

10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

11. Precautions for Use

11-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) The LCD shall be installed flat, without twisting or bending.
- 3) A transparent protection sheet is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.
- 4) FPC shall be installed after fixing the glass.
- 5) Do not apply a load to the FPC.
- 6) Do not use an assembly structure where the FPC is pulled or partially stressed.

11-2. Static electricity

- 1) Protect the LCD from static electricity.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.

11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

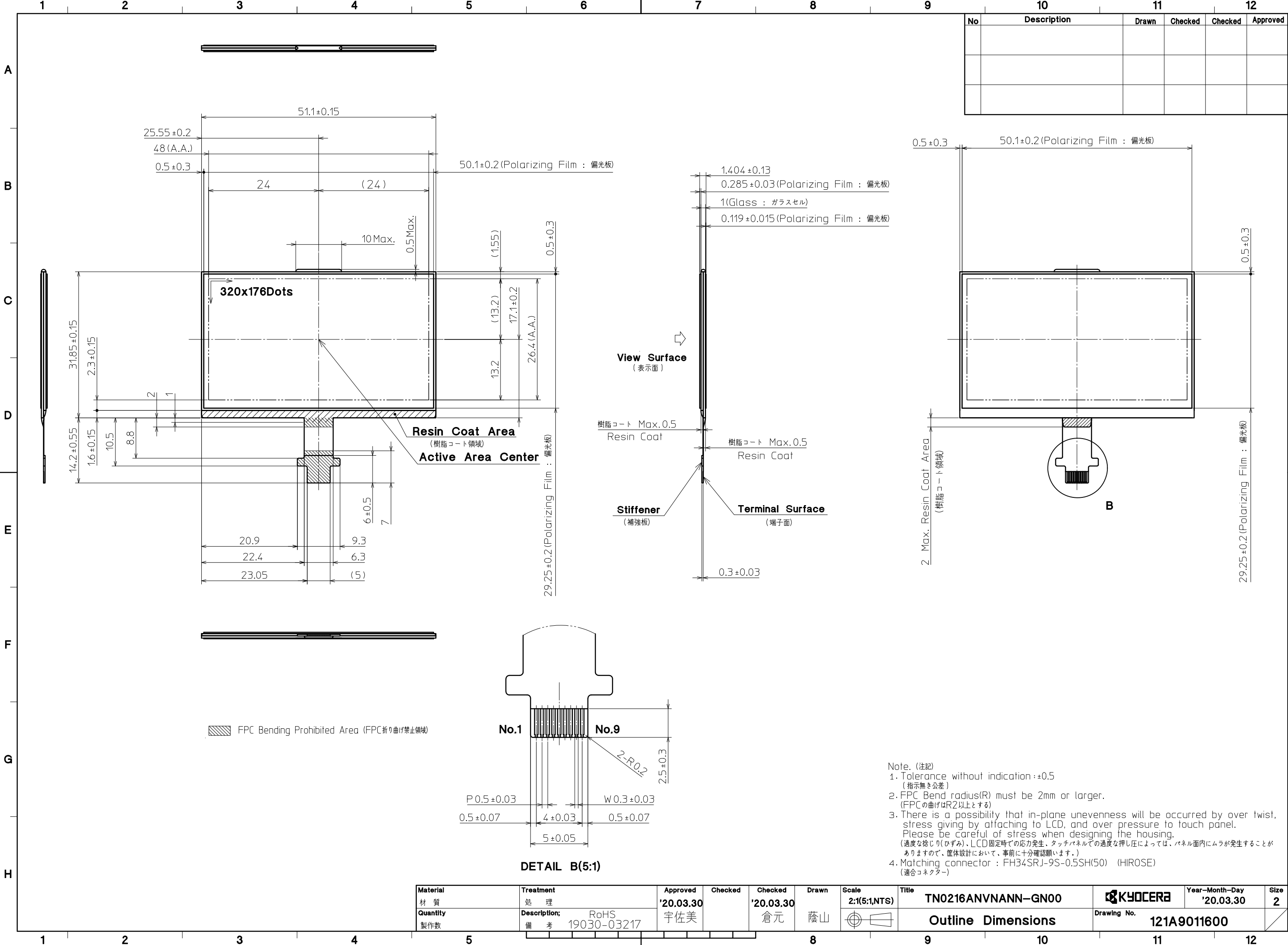
11-5. Usage

- 1) **DO NOT** store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.
- 10) Do not hold the FPC alone while working.
- 11) Do not scratch or damage the exposed conductor of the FPC or FPC.
- 12) Do not touch the exposed conductor of the FPC.
- 13) Do not bend the FPC toward the surface of LCD near the glass.
- 14) Do not bend the FPC so that stress is concentrated on the FPC.

12. Reliability Test Data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	85℃	240h	Function/Display : No defect Current consumption : No defect
Low temp. atmosphere	-30℃	240h	Function/Display : No defect Current consumption : No defect
High temp. humidity atmosphere	60℃ 90%RH	240h	Function/Display : No defect Current consumption : No defect
Temp. cycle	-30℃ 0.5h R.T. 0.5h 85℃ 0.5h	10cycles	Function/Display : No defect Current consumption : No defect
High temp. operation	80℃	240h	Function/Display : No defect Current consumption : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.



No	Description	Drawn	Checked	Checked	Approved

Material 材質	Treatment 処理	Approved '20.03.30 宇佐美	Checked	Checked '20.03.30 倉元	Drawn 蔭山	Scale 2:1(5:1,NTS)	Title TN0216ANVNANN-GN00	KYOCERA	Year-Month-Day '20.03.30	Size 2
Quantity 製作数	Description; 備考	RoHS 19030-03217					Outline Dimensions	Drawing No. 121A9011600		

Spec No.	TQ3C-8EAF0-E2YBV05-00
Date	March 30, 2020

KYOCERA INSPECTION STANDARD

TYPE : TN0216ANVNANN-GN00

KYOCERA CORPORATION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
March 30, 2020	T. Onodera	Y. Asakura	A. Iwasaki	S. Kishimoto	O. Sato

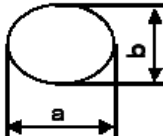
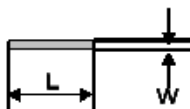
Spec No. TQ3C-8EAF0-E2YBV05-00	Part No. TN0216ANVNANN-GN00	Page -
-----------------------------------	--------------------------------	-----------

Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.	
		Prepared	Checked	Approved	Checked	Approved
Rev.No.	Date	Page	Descriptions			

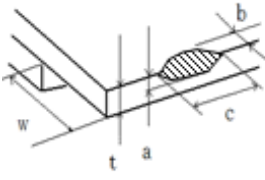
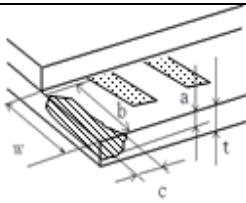
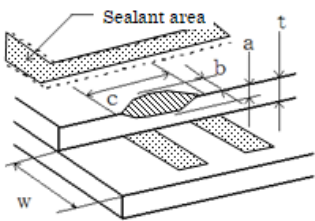
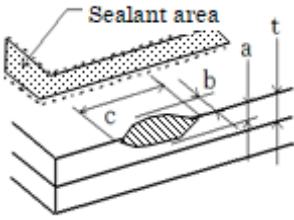
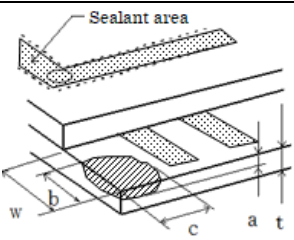
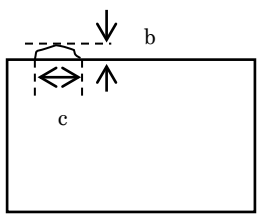
Visuals specification

1) Note

			Note															
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. Dot defect and external inspection about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the active area. 3. Inspection conditions Luminance : 500 Lux min. Inspection distance : 300 mm. Temperature : 25 ± 5℃ Direction : Directly above																	
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly “on” when power applied to the LCD, even when all “Black” data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. <table><tr><td>D(1,1)</td><td>D(2,1)</td><td>D(3,1)</td><td>D(4,1)</td><td>D(5,1)</td></tr><tr><td>D(1,2)</td><td>D(2,2)</td><td>D(3,2)</td><td>D(4,2)</td><td>D(5,2)</td></tr><tr><td>D(1,3)</td><td>D(2,3)</td><td>D(3,3)</td><td>D(4,3)</td><td>D(5,3)</td></tr></table> dot defect	D(1,1)	D(2,1)	D(3,1)	D(4,1)	D(5,1)	D(1,2)	D(2,2)	D(3,2)	D(4,2)	D(5,2)	D(1,3)	D(2,3)	D(3,3)	D(4,3)	D(5,3)
		D(1,1)	D(2,1)	D(3,1)	D(4,1)	D(5,1)												
		D(1,2)	D(2,2)	D(3,2)	D(4,2)	D(5,2)												
	D(1,3)	D(2,3)	D(3,3)	D(4,3)	D(5,3)													
	Black dot defect (Included circular shape)	The dot is constantly “off” when power applied to the LCD, even when all “White” data sent to the screen. Similar size compared to bright dot. For circular shape black dot, refer to inspection item of White dot, Black dot (Circular shape).																
White dot (Circular shape)	Pixel works electrically, however, circular/foreign particle makes dot appear to be “on” even when all “Black” data is sent to the screen.																	
External inspection	Bubble, Scratch, Foreign particle	Visible operating (all pixels “Black” or “White”) and non-operating.																
	Appearance inspection	Does not satisfy the value at the spec.																
Definition of size	Definition of circle size  d =(a + b)/2 Definition of linear size 																	

2) Standard

Classification		Inspection item	Judgement standard																	
Defect (In LCD glass)	Dot defect	Bright dot defect	Acceptable number : 0																	
		Black dot defect	Acceptable number : 0																	
		Total dot defects	Acceptable number : 0																	
	Others	White dot, Black dot (Circular shape)	<table><tr><th>Size (mm)</th><th colspan="2">Acceptable number</th></tr><tr><td>d ≤ 0.1</td><td colspan="2">Neglected</td></tr><tr><td>0.1 < d ≤ 0.15</td><td colspan="2">2</td></tr><tr><td>0.15 < d</td><td colspan="2">0</td></tr></table>			Size (mm)	Acceptable number		d ≤ 0.1	Neglected		0.1 < d ≤ 0.15	2		0.15 < d	0				
Size (mm)	Acceptable number																			
d ≤ 0.1	Neglected																			
0.1 < d ≤ 0.15	2																			
0.15 < d	0																			
External inspection (Defect on Polarizer or between Polarizer and LCD glass)		Foreign particle Scratch (Linear shape)	<table><tr><th>Width (mm)</th><th>Length (mm)</th><th>Acceptable number</th></tr><tr><td>W ≤ 0.02</td><td>—</td><td>Neglected</td></tr><tr><td>W ≤ 0.03</td><td>L ≤ 2.0</td><td>2</td></tr><tr><td>W ≤ 0.05</td><td>L ≤ 0.5</td><td>1</td></tr><tr><td>0.05 < W</td><td>—</td><td>0</td></tr></table>			Width (mm)	Length (mm)	Acceptable number	W ≤ 0.02	—	Neglected	W ≤ 0.03	L ≤ 2.0	2	W ≤ 0.05	L ≤ 0.5	1	0.05 < W	—	0
			Width (mm)	Length (mm)	Acceptable number															
			W ≤ 0.02	—	Neglected															
			W ≤ 0.03	L ≤ 2.0	2															
			W ≤ 0.05	L ≤ 0.5	1															
		0.05 < W	—	0																
Foreign particle Bubble Scratch (Circular shape)	<table><tr><th>Size (mm)</th><th colspan="2">Acceptable number</th></tr><tr><td>d ≤ 0.1</td><td colspan="2">Neglected</td></tr><tr><td>0.1 < d ≤ 0.2</td><td colspan="2">2</td></tr><tr><td>0.2 < d</td><td colspan="2">0</td></tr></table>			Size (mm)	Acceptable number		d ≤ 0.1	Neglected		0.1 < d ≤ 0.2	2		0.2 < d	0						
Size (mm)	Acceptable number																			
d ≤ 0.1	Neglected																			
0.1 < d ≤ 0.2	2																			
0.2 < d	0																			

Inspection item	Judgement standard					
Glass crack	(t = Glass Thickness)					
	Item	Size (mm)				Judgement
	Back side of the terminal		a	b	c	Neglected
			≦ t	≦ w	≦ 2.0	
	Terminal		a	b	c	Neglected
			≦ t	≦ w	≦ 2.0	
	Display side glass		a	b	c	Neglected
			≦ t	< Sealant area inner boundary	≦ 3.0	
	Double glass		a	b	c	Neglected
			-	-	≦ 0.2	
			≦ t	< Sealant area inner boundary	≦ 3.0	
			> t	< Sealant area outer boundary	≦ 2.0	
	Corner		a	b	c	Neglected
			≦ t	≦ w	≦ 2.0	
	Projection	a:Thickness 	a	b	c	Neglected
			≦ t	≦ 0.3	-	